



**CHENMKO ENTERPRISE CO.,LTD**

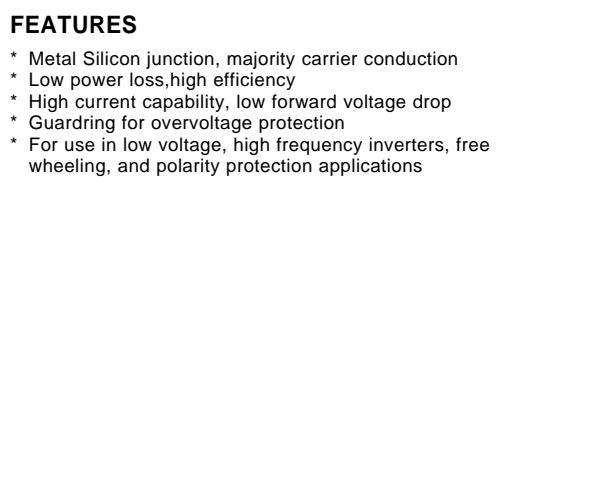
*Halogens free devices*    **SCHOTTKY BARRIER RECTIFIER**

VOLTAGE RANGE 30 - 60 Volts    CURRENT 20 Amperes

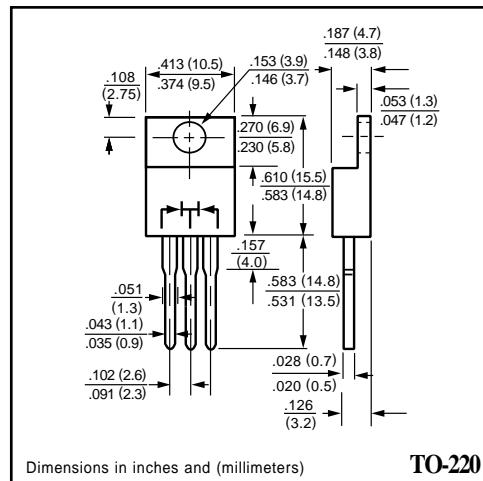
**S20C30GP**

**THRU**

**S20C60GP**



**TO-220**



**TO-220**

<b>MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS</b>	
Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz, resistive or inductive load.	
For capacitive load, derate current by 20%.	

**MAXIMUM RATINGS ( At TA = 25°C unless otherwise noted )**

RATINGS	SYMBOL	S20C30GP	S20C35GP	S20C40GP	S20C45GP	S20C50GP	S20C60GP	UNITS
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	30	35	40	45	50	60	Volts
Maximum RMS Voltage	V <sub>RMS</sub>	21	24	28	31	35	42	Volts
Maximum DC Blocking Voltage	V <sub>D</sub> C	30	35	40	45	50	60	Volts
Maximum Average Forward Rectified Current	I <sub>O</sub>	20.0					Amps	
Peak Forward Surge Current 8.3 ms single half sine-wave superimposed on rated load ( JEDEC method )	I <sub>FSM</sub>	200					Amps	
Typical thermal resistance per leg ( NOTE 1 )	R <sub>θJC</sub>	2.2					°C / W	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-60 to +125					°C	

**ELECTRICAL CHARACTERISTICS ( At TA = 25°C unless otherwise noted )**

CHARACTERISTICS	SYMBOL	S20C30GP	S20C35GP	S20C40GP	S20C45GP	S20C50GP	S20C60GP	UNITS
Maximum Instantaneous Forward Voltage at 10.0 A DC	V <sub>F</sub>	0.55			0.65			Volts
Maximum instantaneous reverse current at rated DC blocking voltage per leg ( NOTE 2 )	I <sub>R</sub>	5.0					mAmps	
T <sub>C</sub> = 25°C		50					mAmps	
T <sub>C</sub> = 100°C								

NOTES : 1. Thermal resistance from junction to case per leg  
2. Pulse test : 300 us pulse width, 1% duty cycle

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## RATING CHARACTERISTIC CURVES ( S20C30GP THRU S20C60GP )

FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE

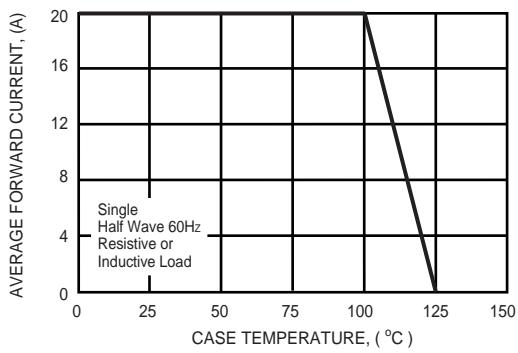


FIG. 2 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

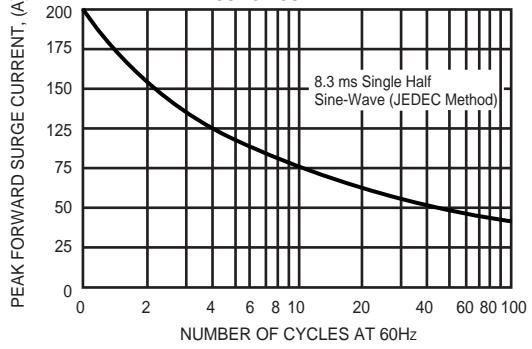


FIG. 3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

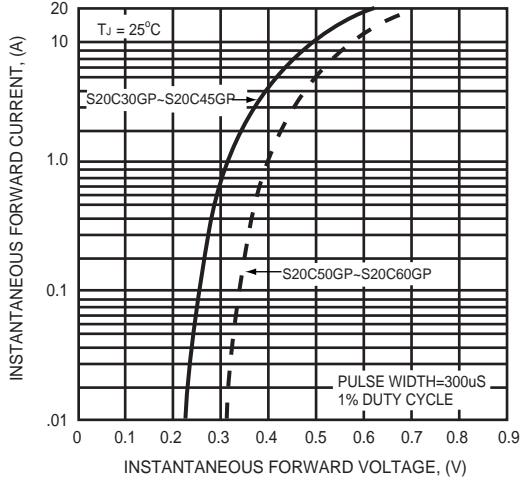


FIG. 4 - TYPICAL REVERSE CHARACTERISTICS

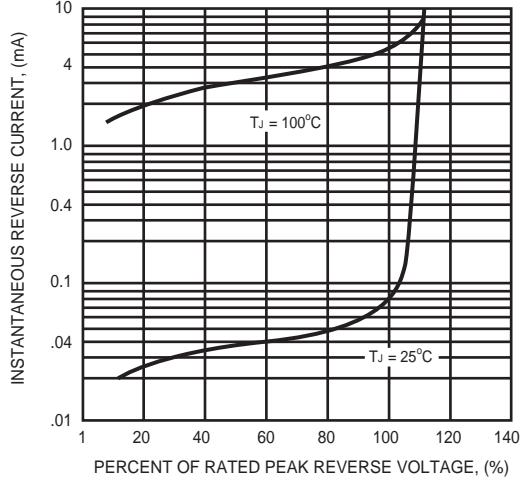


FIG. 5 - TYPICAL JUNCTION CAPACITANCE

