

CMLM0574

**MULTI DISCRETE MODULE™**  
SURFACE MOUNT SILICON  
N-CHANNEL MOSFET AND  
LOW V<sub>F</sub> SCHOTTKY DIODE



[www.centralsemi.com](http://www.centralsemi.com)

**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMLM0574 is a Multi Discrete Module™ consisting of a single N-Channel enhancement-mode MOSFET and a low V<sub>F</sub> Schottky diode packaged in a space saving SOT-563 surface mount case. This device is designed for small signal general purpose applications where size and operational efficiency are prime requirements.



SOT-563 CASE

- Device is *Halogen Free* by design

**APPLICATIONS:**

- DC-DC Converters
- Battery Powered Portable Equipment

**MAXIMUM RATINGS - CASE: (T<sub>A</sub>=25°C)**

Power Dissipation (Note 1)	P <sub>D</sub>	350	mW
Power Dissipation (Note 2)	P <sub>D</sub>	300	mW
Power Dissipation (Note 3)	P <sub>D</sub>	150	mW
Operating and Storage Junction Temperature	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C
Thermal Resistance	θ <sub>JA</sub>	357	°C/W

**MAXIMUM RATINGS - Q1: (T<sub>A</sub>=25°C)**

Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>GS</sub>	8.0	V
Continuous Drain Current	I <sub>D</sub>	450	mA

**MAXIMUM RATINGS - D1: (T<sub>A</sub>=25°C)**

Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	40	V
Continuous Forward Current	I <sub>F</sub>	500	mA
Peak Repetitive Forward Current, t <sub>p</sub> ≤1.0ms	I <sub>FRM</sub>	3.5	A
Peak Forward Surge Current, t <sub>p</sub> =8.0ms	I <sub>FSM</sub>	10	A

**ELECTRICAL CHARACTERISTICS - Q1: (T<sub>A</sub>=25°C unless otherwise noted)**

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I <sub>GSSF</sub> , I <sub>GSSR</sub>	V <sub>GS</sub> =8.0V, V <sub>DS</sub> =0			3.0	µA
I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0			1.0	µA
BV <sub>DSS</sub>	V <sub>GS</sub> =0, I <sub>D</sub> =10µA	30			V
V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	0.5		1.0	V
V <sub>SD</sub>	V <sub>GS</sub> =0, I <sub>S</sub> =400mA	0.5		1.1	V
r <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =200mA		280	460	mΩ
r <sub>DS(ON)</sub>	V <sub>GS</sub> =2.5V, I <sub>D</sub> =100mA		390	560	mΩ
r <sub>DS(ON)</sub>	V <sub>GS</sub> =1.8V, I <sub>D</sub> =75mA		550	730	mΩ
g <sub>FS</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =100mA	200			mS

Notes: (1) Ceramic or aluminum core PC Board with copper mounting pad area of 4.0mm<sup>2</sup>

(2) FR-4 Epoxy PC Board with copper mounting pad area of 4.0mm<sup>2</sup>

(3) FR-4 Epoxy PC Board with copper mounting pad area of 1.4mm<sup>2</sup>

R3 (18-February 2014)

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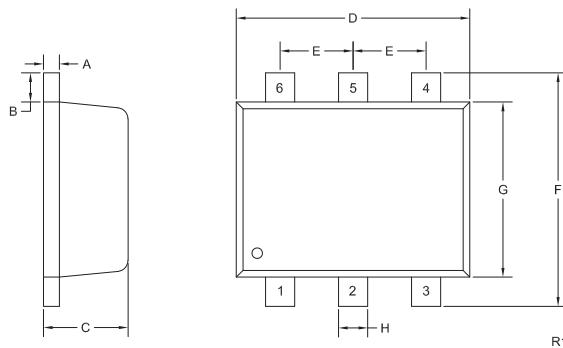
ELECTRICAL CHARACTERISTICS - Q1 - Continued: ( $T_A=25^\circ\text{C}$  unless otherwise noted)

SYMBOL	TEST CONDITIONS	TYP	UNITS
$Q_g(\text{tot})$	$V_{DS}=15\text{V}$ , $V_{GS}=4.5$ , $I_D=1.0\text{A}$	0.792	nC
$Q_{gs}$	$V_{DS}=15\text{V}$ , $V_{GS}=4.5$ , $I_D=1.0\text{A}$	0.15	nC
$Q_{gd}$	$V_{DS}=15\text{V}$ , $V_{GS}=4.5$ , $I_D=1.0\text{A}$	0.23	nC
$C_{rss}$	$V_{DS}=25\text{V}$ , $V_{GS}=0$ , $f=1.0\text{MHz}$	5.0	pF
$C_{iss}$	$V_{DS}=25\text{V}$ , $V_{GS}=0$ , $f=1.0\text{MHz}$	43	pF
$C_{oss}$	$V_{DS}=25\text{V}$ , $V_{GS}=0$ , $f=1.0\text{MHz}$	8.0	pF

ELECTRICAL CHARACTERISTICS - D1: ( $T_A=25^\circ\text{C}$ )

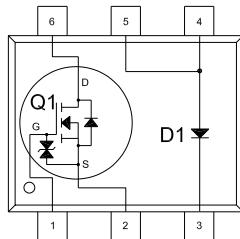
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
$I_R$	$V_R=10\text{V}$		20	$\mu\text{A}$
$I_R$	$V_R=30\text{V}$		100	$\mu\text{A}$
$BV_R$	$I_R=500\mu\text{A}$	40		V
$V_F$	$I_F=100\mu\text{A}$		0.13	V
$V_F$	$I_F=1.0\text{mA}$		0.21	V
$V_F$	$I_F=10\text{mA}$		0.27	V
$V_F$	$I_F=100\text{mA}$		0.35	V
$V_F$	$I_F=500\text{mA}$		0.47	V
$C_J$	$V_R=1.0\text{V}$ , $f=1.0\text{MHz}$		50	pF

**SOT-563 CASE - MECHANICAL OUTLINE**



SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.0027	0.007	0.07	0.18
B		0.008		0.20
C	0.017	0.024	0.45	0.60
D	0.059	0.067	1.50	1.70
E		0.020		0.50
F	0.061	0.067	1.55	1.70
G	0.045	0.049	1.15	1.25
H	0.006	0.012	0.15	0.30

SOT-563 (REV: R1)



**LEAD CODE:**

- 1) Gate Q1
- 2) Source Q1
- 3) Cathode D1
- 4) Anode D1
- 5) Anode D1
- 6) Drain Q1

**MARKING CODE: 57C**

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