

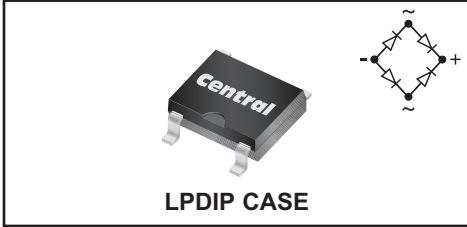
**CBRLDSH2-60**  
**SURFACE MOUNT**  
**LOW PROFILE**  
**2 AMP SILICON**  
**SCHOTTKY BRIDGE RECTIFIER**



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**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CBRLDSH2-60 is a full wave Schottky bridge rectifier mounted in a low profile epoxy surface mount molded case, utilizing glass passivated chips.



**MARKING CODE: CL60**

**APPLICATIONS:**

- Solid state lighting (SSL)
- DC-DC converters
- Polarity protection

**FEATURES:**

- Low leakage current (7.0μA TYP @ 60V)
- Low profile case (1.45mm MAX)
- Low  $V_F$  Schottky diodes (630mV TYP @  $I_F=2.0A$ )

**MAXIMUM RATINGS:** ( $T_A=25^\circ C$  unless otherwise noted)

	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	$V_{RRM}$	60	V
DC Blocking Voltage	$V_R$	60	V
RMS Reverse Voltage	$V_{R(RMS)}$	42	V
Average Forward Current ( $T_L=50^\circ C$ )	$I_O$	2.0	A
Peak Forward Surge Current ( $t_p=8.3ms$ )	$I_{FSM}$	50	A
Operating Junction Temperature	$T_J$	-55 to +125	$^\circ C$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ C$
Thermal Resistance (Note 1)	$\theta_{JA}$	95	$^\circ C/W$

**ELECTRICAL CHARACTERISTICS PER DIODE:** ( $T_A=25^\circ C$  unless otherwise noted)

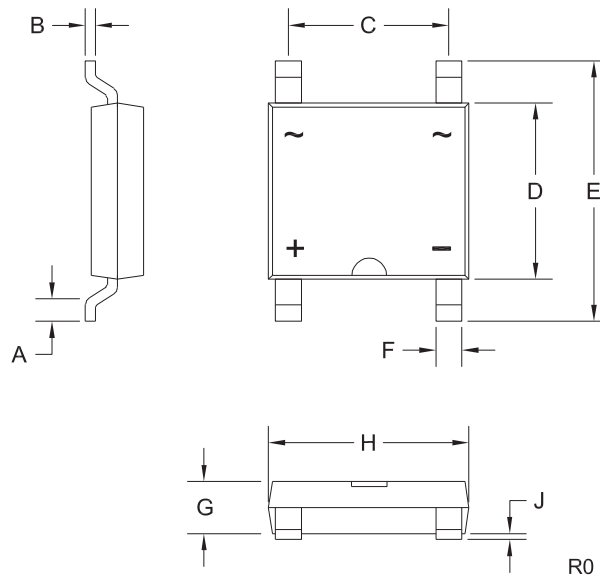
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
$I_R$	$V_R=60V$		7.0	500	$\mu A$
$BV_R$	$I_R=150\mu A$	60			V
$V_F$	$I_F=2.0A$		630	700	mV
$C_J$	$V_R=4.0V, f=1.0MHz$		117		pF

Notes: (1) FR-4 epoxy PCB with minimum copper pad area.

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LPDIP CASE - MECHANICAL OUTLINE



R0

DIMENSIONS				
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.019	0.028	0.50	0.70
B	0.007	0.012	0.20	0.30
C	0.153	0.162	3.90	4.10
D	0.167	0.179	4.25	4.55
E	0.248	0.264	6.30	6.70
F	0.023	0.028	0.60	0.70
G	0.045	0.058	1.15	1.45
H	0.190	0.203	4.85	5.15
J	0.002	0.006	0.05	0.15

LPDIP (REV: R0)

MARKING CODE: CL60

R1 (13-August 2012)