

### SCHOTTKY BARRIER RECTIFIERS

VOLTAGE RANGE: 30 - 100 V  
CURRENT: 10 A

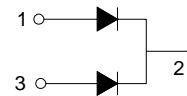
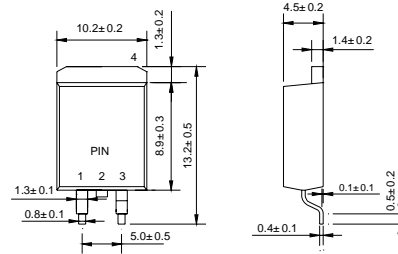
#### FEATURES

- ◇ High surge capacity.
- ◇ For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications.
- ◇ Metal silicon junction, majority carrier conduction.
- ◇ High current capacity, low forward voltage drop.
- ◇ Guard ring for over voltage protection.

#### MECHANICAL DATA

- ◇ Case: JEDEC D<sup>2</sup>PAK, molded plastic body
- ◇ Terminals: Solderable per MIL-STD-750, Method 2026
- ◇ Polarity: As marked
- ◇ Position: Any
- ◇ Weight: 0.087 ounces, 2.2 gram

#### D<sup>2</sup>PAK



Dimensions in millimeters

#### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

		MBRB 1030CT	MBRB 1035CT	MBRB 1040CT	MBRB 1045CT	MBRB 1050CT	MBRB 1060CT	MBRB 1090CT	MBRB 10100CT	UNITS
Maximum recurrent peak reverse voltage	$V_{RRM}$	30	35	40	45	50	60	90	100	V
Maximum RMS Voltage	$V_{RMS}$	21	25	28	32	35	42	63	70	V
Maximum DC blocking voltage	$V_{DC}$	30	35	40	45	50	60	90	100	V
Maximum average forward total device rectified current @ $T_c = 105^\circ\text{C}$	$I_{F(AV)}$	10								A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load	$I_{FSM}$	125								A
Maximum forward voltage ( $I_F=5.0\text{A}, T_c=125^\circ\text{C}$ ) ( $I_F=5.0\text{A}, T_c=25^\circ\text{C}$ ) (Note 1) ( $I_F=10\text{A}, T_c=25^\circ\text{C}$ )	$V_F$		0.57			0.70		-		V
			0.70			0.80		0.85		
			0.84			0.95		-		
Maximum reverse current @ $T_c=25^\circ\text{C}$ at rated DC blocking voltage @ $T_c=125^\circ\text{C}$	$I_R$	0.1								m A
		15						6.0 <sup>3)</sup>		
Maximum thermal resistance (Note2)	$R_{\theta JC}$	6.8						4.4		$^\circ\text{C/W}$
Operating junction temperature range	$T_J$	- 55 ---- + 150								$^\circ\text{C}$
Storage temperature range	$T_{STG}$	- 55 ---- + 150								$^\circ\text{C}$

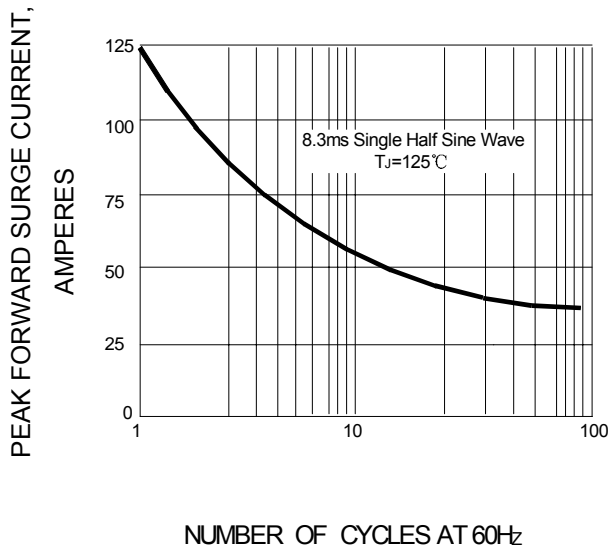
NOTE: 1. Pulse test: 300µs pulse width, 1% duty cycle.

2. Thermal resistance from junction to case.

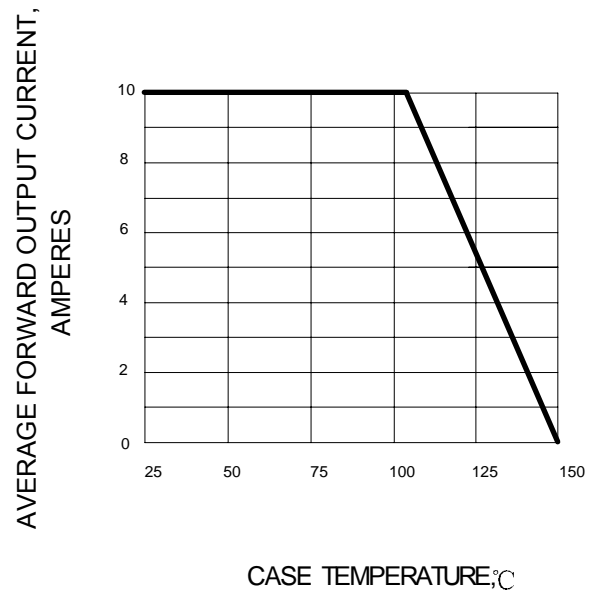
3.  $T_c=100^\circ\text{C}$

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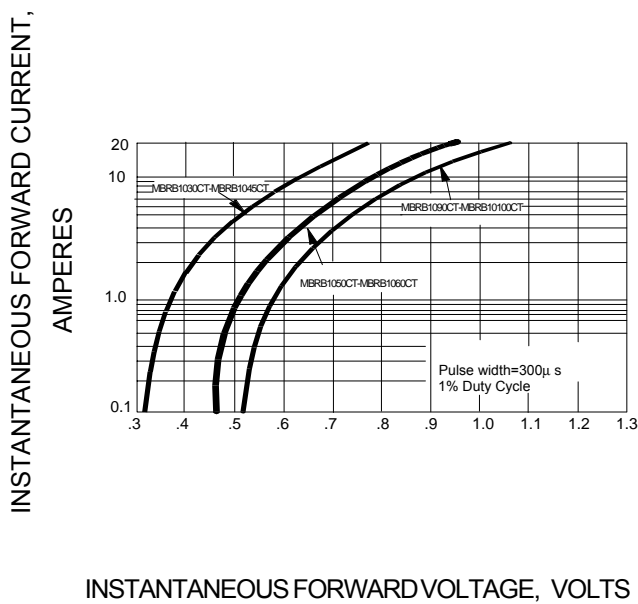
**FIG.1 – PEAK FORWARD SURGE CURRENT**



**FIG.2 – FORWARD DERATING CURVE**



**FIG.3 – TYPICAL FORWARD CHARACTERISTIC**



**FIG.4 – TYPICAL REVERSE CHARACTERISTIC**

