

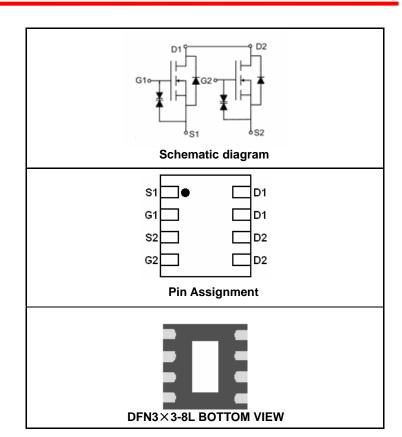
# **GENERAL FEATURES**

ESD Rating: 2000V HBM

- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

#### Application

- Battery protection
- Load switch
- Power management



#### PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2316E	SSF2316E	DFN3×3-8L	-	-	-

#### ABSOLUTE MAXIMUM RATINGS(TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	20	V
Gate-Source Voltage	Vgs	±12	V
	I <sub>D</sub>	7	A
Drain Current-Continuous@ Current-Pulsed (Note 1)	I <sub>DM</sub>	40	А
Maximum Power Dissipation	P <sub>D</sub>	1.4	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 150	°C

#### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient (Note 2)	R <sub>0JA</sub>	83	°C/W	
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#### ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	20			V



# **SSF2316E**

Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =20V,V <sub>GS</sub> =0V			1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±8V,V <sub>DS</sub> =0V			±10	μA
ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	0.5		1.3	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A		17	23	mΩ
		V <sub>GS</sub> =4V, I <sub>D</sub> =4A		18	24	mΩ
		V <sub>GS</sub> =3.1V, I <sub>D</sub> =4A		20	30	mΩ
		$V_{GS}$ =2.5V, I <sub>D</sub> =2A		24	35	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =10V,I <sub>D</sub> =3.5A		11		S
DYNAMIC CHARACTERISTICS (Note4)						
Input Capacitance	C <sub>lss</sub>			900		PF
Output Capacitance	Coss	V <sub>DS</sub> =8V,V <sub>GS</sub> =0V, F=1.0MHz		350		PF
Reverse Transfer Capacitance	C <sub>rss</sub>			150		PF
SWITCHING CHARACTERISTICS (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>			15		nS
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> =10V,I <sub>D</sub> =1A		100		nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =4.5V,R <sub>GEN</sub> =6 $\Omega$		60		nS
Turn-Off Fall Time	t <sub>f</sub>			90		nS
Total Gate Charge	Qg			20		nC
Gate-Source Charge	$Q_gs$	V <sub>DS</sub> =10V,I <sub>D</sub> =7A, V <sub>GS</sub> =4.5V		2.5		nC
Gate-Drain Charge	Q <sub>gd</sub>			3		nC
DRAIN-SOURCE DIODE CHARACTERISTICS			•			
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =7A		0.83	1.2	V

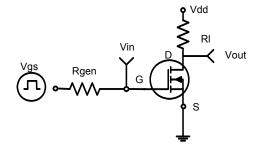
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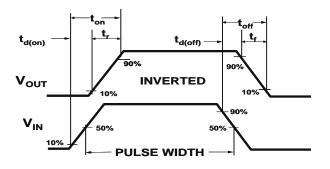
# NOTES:

Repetitive Rating: Pulse width limited by maximum junction temperature.
Surface Mounted on FR4 Board, t ≤ 10 sec.
Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
Guaranteed by design, not subject to production testing.



# **TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**





# Figure 1:Switching Test Circuit

Figure 2:Switching Waveforms

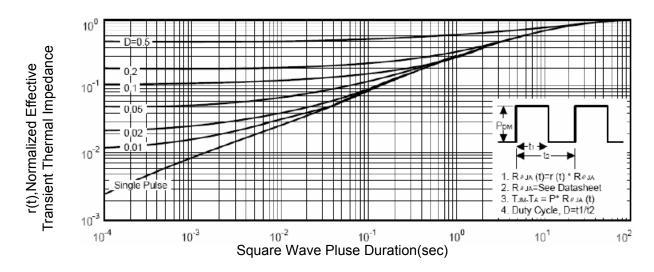
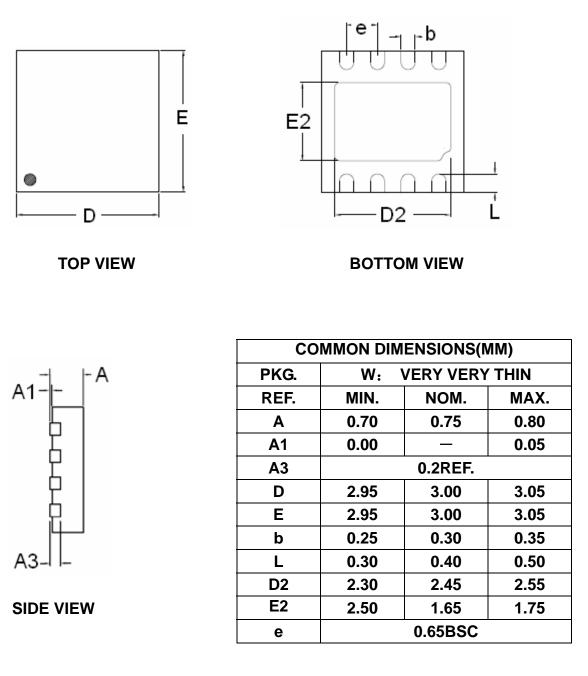


Figure 3 Normalized Maximum Transient Thermal Impedance



# **DFN3×3-8L PACKAGE INFORMATION**



#### NOTES:

- 1. Dimensions are inclusive of plating
- 2. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 6 mils.

- 3. Dimension L is measured in gauge plane.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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