

DESCRIPTION

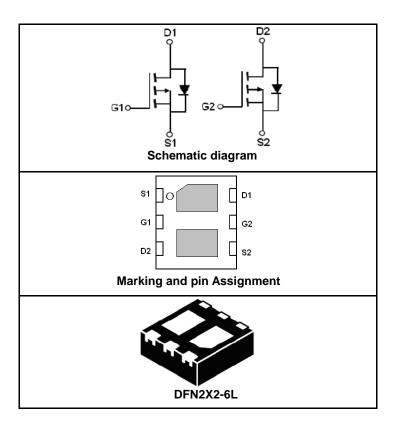
The SSFN2269 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V.

GENERAL FEATURES

- V_{DS} = -20V, I_{D} =-3.3A $R_{DS(ON)}$ < 90mΩ @ V_{GS} =-4.5V $R_{DS(ON)}$ < 120mΩ @ V_{GS} =-2.5V
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package



- Battery protection
- Load switch
- Power management



PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
SSFN2269	SSFN2269	DFN2X2-6L	_		

ABSOLUTE MAXIMUM RATINGS(TA=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	-20	V
Gate-Source Voltage	V _{GS}	±8	V
Durain Coursest Continuous @ Coursest Duland (Nate 4)	I _D	-3.3	А
Drain Current-Continuous@ Current-Pulsed (Note 1)	I _{DM}	-20	А
Maximum Power Dissipation	P _D	1.5	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	83	°C/W
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ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS				•		
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-20			V



I _{DSS}	V _{DS} =-16V,V _{GS} =0V			-1	μA
I _{GSS}	V _{GS} =±8V,V _{DS} =0V			±100	nA
V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250μA	-0.4	-0.7	-1	V
R _{DS(ON)}	V _{GS} =-4.5V, I _D =-2A		70	90	mΩ
	V _{GS} =-2.5V, I _D =-2A		91	120	mΩ
g FS	V _{DS} =-5V,I _D =-2A	3			S
C _{lss}	V _{DS} =-10V,V _{GS} =0V, F=1 0MHz		530	636	PF
C _{oss}			100	120	PF
C _{rss}			60	72	PF
t _{d(on)}			6	7.2	nS
t _r	V _{DD} =-10V,I _D =-2A		12	14.4	nS
t _{d(off)}	V_{GS} =-4.5V, R_{GEN} =2 Ω		20	24	nS
t _f	1		6	7.2	nS
Qg			5.5	6.6	nC
Q _{gs}	V_{DS} =-10V, I_{D} =-2A, V_{GS} =-4.5V		1.0	1.2	nC
Q_gd			1.5	1.8	nC
S	•				
V _{SD}	V _{GS} =0V,I _S =-1A			-1	V
	V _{GS(th)}	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

NOTES:

- Repetitive Rating: Pulse width limited by maximum junction temperature.
 Surface Mounted on 1in² FR4 Board, t ≤ 10 sec.
 Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.

- 4. Guaranteed by design, not subject to production testing.



ELECTRICAL AND THERMAL CHARACTERISTICS

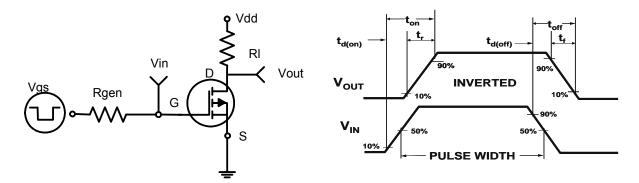
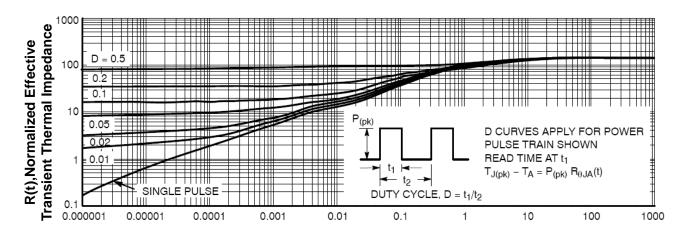


Figure 1:Switching Test Circuit

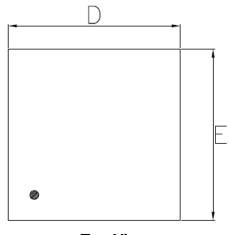
Figure 2:Switching Waveforms

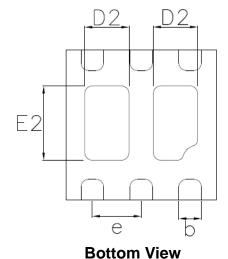


Square Wave Pluse Duration(sec)
Figure 3: Normalized Maximum Transient Thermal Impedance

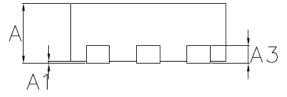


DFN2X2-6L PACKAGE INFORMATION









Side View

Symbol	Dimensions In Millimeters(mm)			
	Min.	Nom.	Max.	
Α	0.70	0.75	0.80	
A1	0.00		0.05	
А3	0.20 REF.			
D	1.95	2.00	2.05	
E	1.95	2.00	2.05	
D2	0.44	0.59	0.69	
E2	0.84	0.99	1.09	
b	0.25	0.30	0.35	
L	0.175	0.275	0.375	
е	0.65 BSC			

NOTES:

- 1. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 2. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 3. Dimension L is measured in gauge plane.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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