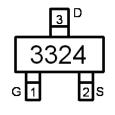
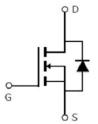


Main Product Characteristics:

V _{DSS}	30V
R _{DS} (on)	26.5mohm(typ.)
I _D	5.8A ①







SOT23

Marking and pin
Assignment

Schematic diagram

Features and Benefits:

- Advanced trench MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest trench processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications

Absolute max Rating:

Symbol	Parameter	Max.	Units
I _D @ TC = 25°C	Continuous Drain Current, V _{GS} @ 10V	5.8 ①	
I _D @ TC = 100°C	Continuous Drain Current, V _{GS} @ 10V	4.2 ①	Α
I _{DM}	Pulsed Drain Current ②	23	
P _D @TC = 25°C	Power Dissipation	1.4	W
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-to-Source Voltage	± 12	V
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to + 150	°C

Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
$R_{\theta JA}$	Junction-to-ambient (t ≤ 10s) ③		90	°CW





Electrical Characterizes $@T_A=25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source breakdown voltage	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
D	R _{DS(on)} Static Drain-to-Source on-resistance	_	26.5	35	mΩ	V_{GS} =4.5 V , I_{D} = 2 A
$R_{DS(on)}$	Static Drain-to-Source on-resistance	_	43.7	_		T _J = 125℃
В	Static Drain-to-Source on-resistance	_	31	52	mΩ	V _{GS} =2.5V,I _D =1.5A
$R_{DS(on)}$	Static Drain-to-Source on-resistance	-	50.2	_	11177	T _J = 125℃
\/	Cata threshold voltage	0.7	_	1.4	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
$V_{GS(th)}$	Gate threshold voltage	-	0.63	_	V	T _J = 125℃
	Drain to Source leakage current	-	_	1		$V_{DS} = 24V, V_{GS} = 0V$
I _{DSS}	Drain-to-Source leakage current	_	_	50	μA	T _J = 125°C
	Cata to Source forward looked	_	_	100	A	V _{GS} =12V
I _{GSS}	Gate-to-Source forward leakage	_	_	-100	nA	V _{GS} = -12V
Qg	Total gate charge	_	10	_		I _D = 5.8A,
Q _{gs}	Gate-to-Source charge	_	2	_	nC	V _{DS} =15V,
Q_{gd}	Gate-to-Drain("Miller") charge	_	3	_		$V_{GS} = 4.5V$
$t_{d(on)}$	Turn-on delay time		3			
tr	Rise time		5		no	V_{GS} =10V, V_{DS} =15V,
t _{d(off)}	Turn-Off delay time	_	26	_	ns	$R_{GEN}=3\Omega$,
t _f	Fall time	_	4	_		
C _{iss}	Input capacitance	_	1245	_		$V_{GS} = 0V$,
C _{oss}	Output capacitance	_	85	_	pF	V _{DS} =15V,
C _{rss}	Reverse transfer capacitance	_	70	_		f = 1MHz

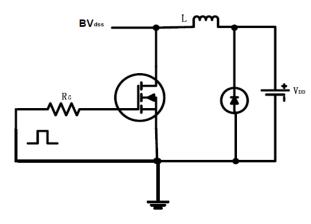
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
-	Continuous Source Current			5.8 ①	Α	MOSFET symbol
Is	(Body Diode)	_				showing the
I _{SM}	Pulsed Source Current	_	_	23	А	integral reverse
	(Body Diode)					p-n junction diode.
V _{SD}	Diode Forward Voltage	_	0.72	1.2	V	I _S =1A, V _{GS} =0V

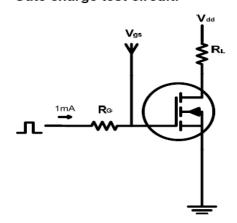


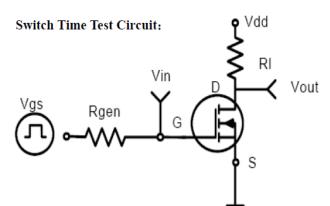
Test circuits and Waveforms

EAS test circuits:

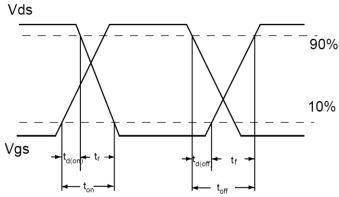


Gate charge test circuit:





Switch Waveforms:

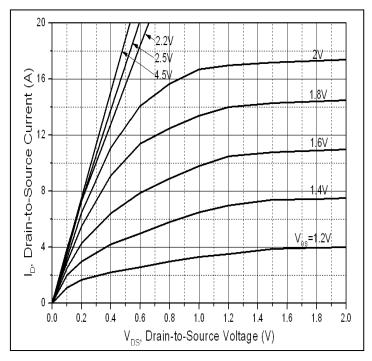


Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max junction temperature.
- 4 These curves are based on the junction-to-case thermal impedence which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_{J(MAX)}=150$ °C.



Typical electrical and thermal characteristics



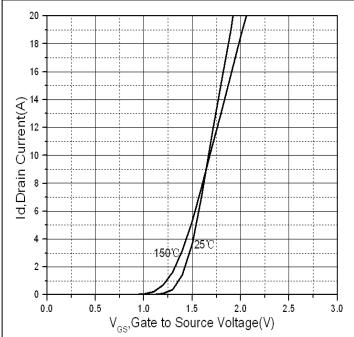


Figure 1: Typical Output Characteristics

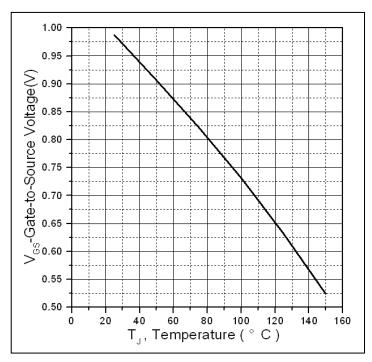


Figure 2. Typical Transfer Characteristics

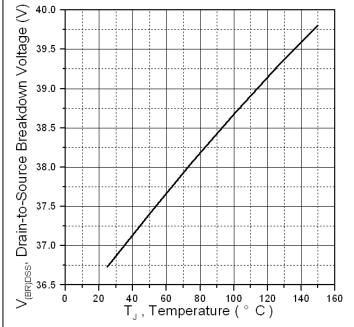
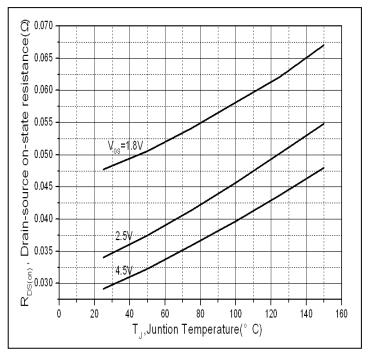


Figure 3. Gate to source cut-off voltage

Figure 4: Drain-to-Source Breakdown Voltage vs.
Temperature



Typical electrical and thermal characteristics



6.0 5.5 5.0 4.5 Drain Current (A) 4.0 3.5 3.0 2.5 2.0 <u> </u> 1.5 1.0 0.5 0.0 75 100 125 25 150 T_{c} , Case Temperature ($^{\circ}$ C)

Figure 5. Normalized On-Resistance Vs. Case Temperature

Figure 6. Maximum Drain Current Vs. Case Temperature

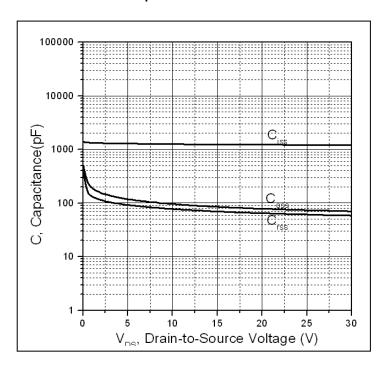


Figure 7. Typical Capacitance Vs. Drain-to-Source Voltage





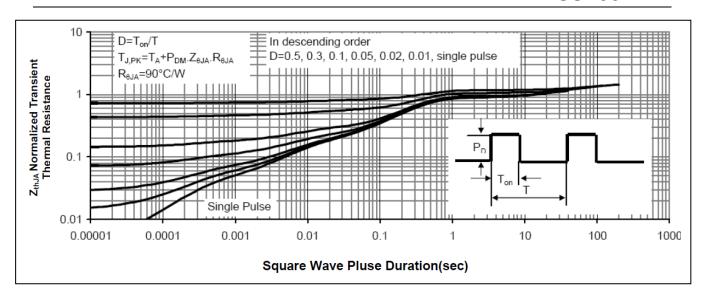
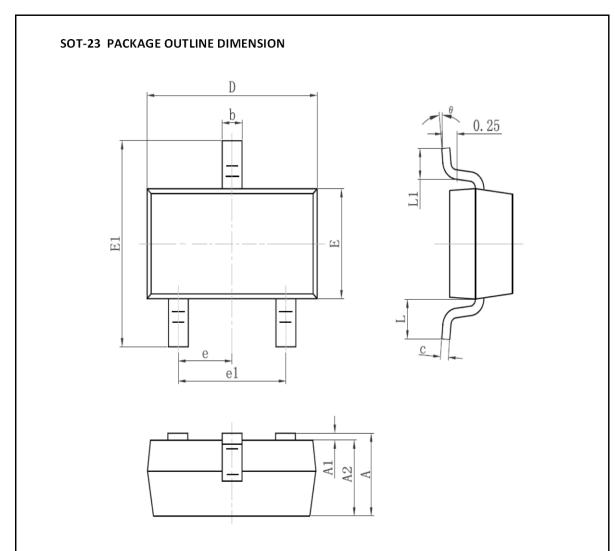


Figure8. Normalized Maximum Transient Thermal Impedance



Mechanical Data:



Symbol	Dimension I	Dimension In Millimeters		n In Inches
Sylfibol	Min	Max	Min	Max
Α	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
С	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
е	0.95	0.95TYP		7TYP
e1	1.800	2.000	0.071	0.079
L	0.55REF		0.02	2REF
L1	0.300	0.500	0.012	0.020
θ	00	80	00	8 ⁰



Ordering and Marking Information

Device Marking: 3324

Package (Available) SOT-23 Operating Temperature Range C:-55 to 150 °C

Devices per Unit

Package	Units/	Tubes/Inner	Units/Inner	Inner	Units/Carton
Type	Tube	Box	Box	Boxes/Carton	Box
				Box	
				DUX	

Reliability Test Program

Test Item	Conditions	Duration	Sample Size
High	Tj= 150℃ @ 80% of	168 hours	3 lots x 77 devices
Temperature	Max V _{DSS} /V _{CES} /V _R	500 hours	
Reverse		1000 hours	
Bias(HTRB)			
High	Tj=150℃ @ 100% of	168 hours	3 lots x 77 devices
Temperature	Max V _{GSS}	500 hours	
Gate		1000 hours	
Bias(HTGB)			



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