

DESCRIPTION

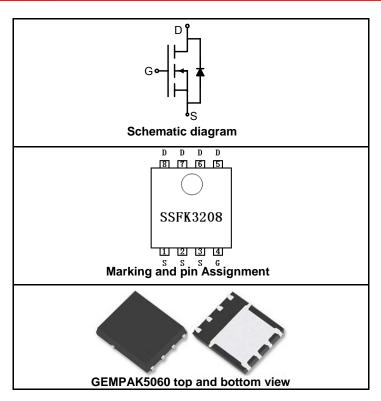
The SSFK3208 uses advanced trench technology to provide excellent $R_{\text{DS(ON)}}$ and low gate charge .This device is suitable for use as a load switch or in PWM applications.

GENERAL FEATURES

- $V_{DS} = 30V, I_D = 11A$ $R_{DS(ON)} < 13mΩ @ V_{GS} = 4.5V$ $R_{DS(ON)} < 9mΩ @ V_{GS} = 10V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

- ●PWM applications
- Load switch
- Power management



PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
SSFK3208	SSFK3208	GEMPAK5060	-	-	-

ABSOLUTE MAXIMUM RATINGS(TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _G s	±20	V
	I _D (25℃)	11	Α
Orain Current-Continuous@ Current-Pulsed (Note 1)	I _D (70℃)	8.6	Α
	I _{DM}	50	Α
Maximum Power Dissipation	P _D	2	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$ C

THERMAL CHARACTERISTICS

Thermal Resistance.Junction-to-Ambient (Note 2)	$R_{\theta JA}$	62.5	°C/W
	- 100/4	VV	

ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	30			V



Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V			1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V			±100	nA
ON CHARACTERISTICS (Note 3)	1					
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1	1.9	3	V
Drain-Source On-State Resistance	В	V _{GS} =4.5V, I _D =8A		9.5	13	mΩ
Dialii-Source Oil-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =11A		6.5	9	mΩ
Forward Transconductance	G FS	V _{DS} =5V,I _D =11A		20		S
DYNAMIC CHARACTERISTICS (Note4)						
Input Capacitance	C _{lss}			1200		PF
Output Capacitance	Coss	V_{DS} =25V, V_{GS} =0V, F=1.0MHz		300		PF
Reverse Transfer Capacitance	C _{rss}	1		120		PF
SWITCHING CHARACTERISTICS (Note 4)						
Turn-on Delay Time	t _{d(on)}			10		nS
Turn-on Rise Time	t _r	V_{DS} =15V, V_{GS} =10V, R_{GEN} =6 Ω		6.5		nS
Turn-Off Delay Time	t _{d(off)}	I _D =1A		25		nS
Turn-Off Fall Time	t _f			9.7		nS
Total Gate Charge	Qg			12		nC
Gate-Source Charge	Q_{gs}	V _{DS} =15V,I _D =12A,V _{GS} =10V		3.2		nC
Gate-Drain Charge	Q _{gd}			3.8		nC
Body Diode Reverse Recovery Time	T _{rr}	L =12A d1/dt=100A/::-		24		nS
Body Diode Reverse Recovery Charge	Q _{rr}	- I _F =12A, dI/dt=100A/μs		27		nC
DRAIN-SOURCE DIODE CHARACTERISTIC	s					
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =2.3A		0.74	1.2	V

NOTES:

- Repetitive Rating: Pulse width limited by maximum junction temperature.
 Surface Mounted on 1in² FR4 Board, t ≤ 10 sec.
 Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
 Guaranteed by design, not subject to production testing.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

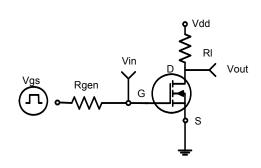


Figure 1:Switching Test Circuit

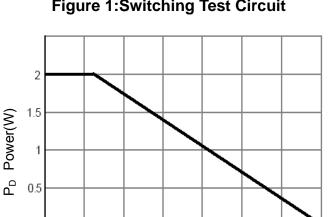


Figure 3 Power Dissipation

T_J-Junction Temperature(°C)

60

20

40

80

100

140

120

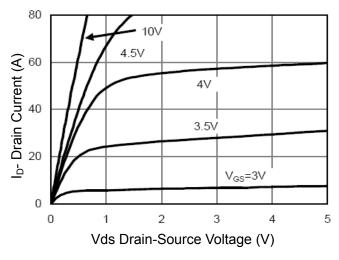


Figure 5 Output CHARACTERISTICS

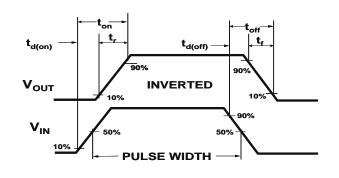


Figure 2:Switching Waveforms

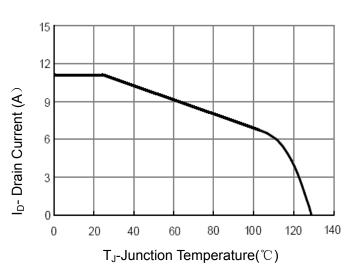


Figure 4 Drain Current

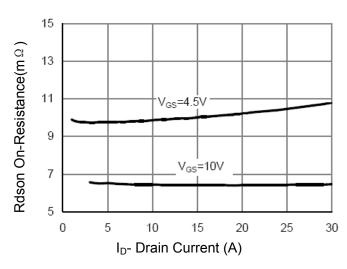


Figure 6 Drain-Source On-Resistance



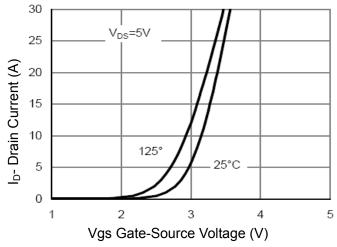


Figure 7 Transfer Characteristics

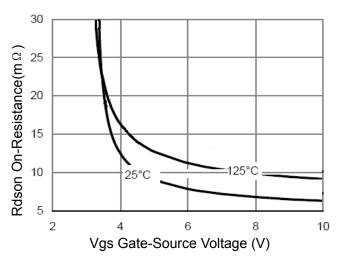


Figure 9 Rdson vs Vgs

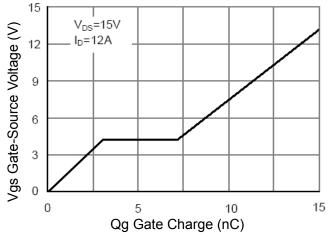


Figure 11 Gate Charge

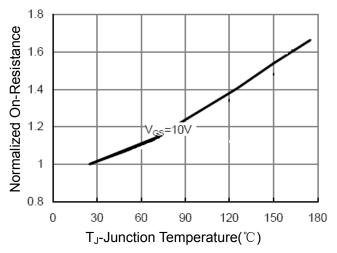


Figure 8 Drain-Source On-Resistance

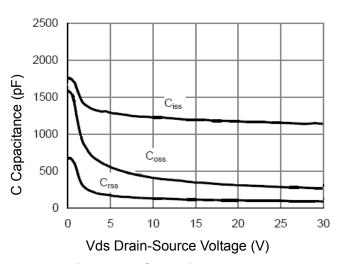


Figure 10 Capacitance vs Vds

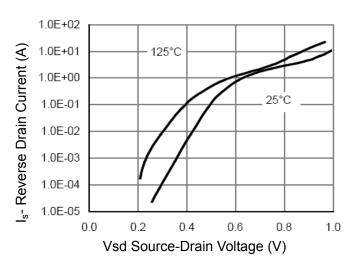


Figure 12 Source- Drain Diode Forward



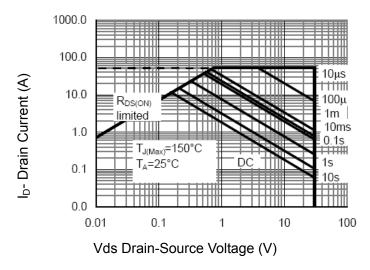
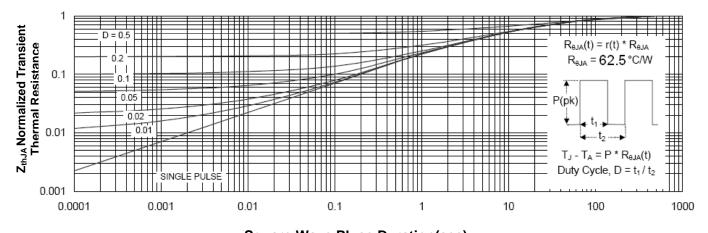


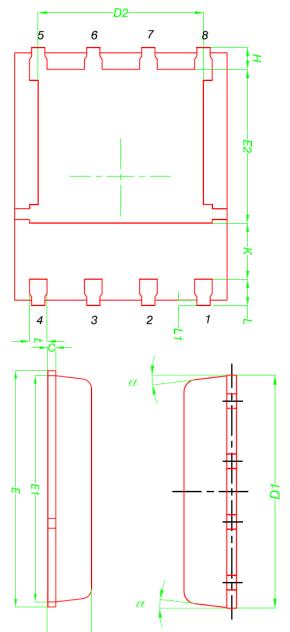
Figure 13 Safe Operation Area

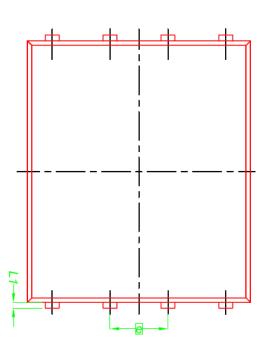


Square Wave Pluse Duration(sec)
Figure 14 Normalized Maximum Transient Thermal Impedance



GEMPAK5060 PACKAGE INFORMATION





DIM	MILLIMETERS					
DIIVI	MIN.	NOM.	MAX.			
Α	0.90	1.00	1.10			
b	0.33	0.41	0.51			
С	0.20	0.25	0.30			
D1	4.80	4.90	5.00			
D2	3.61	3.81	3.96			
Е	5.90	6.00	6.10			
E1	5.70	5.75	5.80			
E2	3.38	3.58	3.78			
е	1.27 BSC					
Н	0.41	0.51	0.61			
K	1.10	-	-			
L	0.51	0.61	0.71			
L1	0.06	0.13	0.20			
α	0°	-	1 2°			

NOTES:

- Dimensions are inclusive of plating
 Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 6 mils.
 Dimension L is measured in gauge plane.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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