

DESCRIPTION

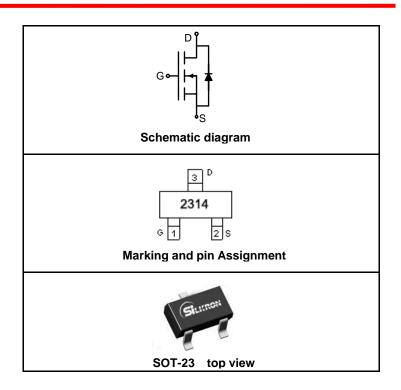
The SSF2314 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 0.65V. This device is suitable for use as a Battery protection or in other Switching application.

GENERAL FEATURES

- $V_{DS} = 20V, I_D = 4.5A$ $R_{DS(ON)} < 40mΩ @ V_{GS} = 2.5V$ $R_{DS(ON)} < 33mΩ @ V_{GS} = 4.5V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

- Battery protection
- Load switch
- Power management



PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2314	SSF2314	SOT-23	Ø180mm	8 mm	3000 units

ABSOLUTE MAXIMUM RATINGS(TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	Vgs	±8	V
Drain Current Continuous @ Current Duland (Note 1)	I _D	4.5	А
Drain Current-Continuous@ Current-Pulsed (Note 1)	I _{DM}	13.5	А
Maximum Power Dissipation	P _D	1.25	W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	100	°C/W

ELECTRICAL CHARACTERISTICS (TA=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS			<u>'</u>			
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0 V I_D =250 μ A	20			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V,V _{GS} =0V			1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±8V,V _{DS} =0V			±100	nA
ON CHARACTERISTICS (Note 3)	1		'			



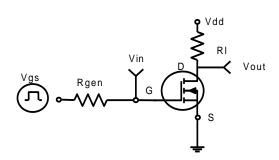
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.5	0.65	1.2	V
Drain-Source On-State Resistance	R _{DS(ON)}	V_{GS} =2.5V, I_{D} =4.5A		33	40	mΩ
		V _{GS} =4.5V, I _D =5A		27	33	mΩ
Forward Transconductance	g FS	V _{DS} =10V,I _D =5A		10		S
DYNAMIC CHARACTERISTICS (Note4)						
Input Capacitance	C _{lss}			500		PF
Output Capacitance	C _{oss}	V _{DS} =8V,V _{GS} =0V, F=1.0MHz		300		PF
Reverse Transfer Capacitance	C _{rss}	1 - 1.000112		140		PF
SWITCHING CHARACTERISTICS (Note 4)						
Turn-on Delay Time	t _{d(on)}			20	40	nS
Turn-on Rise Time	t _r	V_{DD} =10V, I_{D} =1A V_{GS} =4.5V, R_{GEN} =6 Ω		18	40	nS
Turn-Off Delay Time	$t_{d(off)}$			60	108	nS
Turn-Off Fall Time	t _f	1		28	56	nS
Total Gate Charge	Qg			10	15	nC
Gate-Source Charge	Q_{gs}	V _{DS} =10V,I _D =5A,V _{GS} =4.5V		2.3		nC
Gate-Drain Charge	Q_{gd}			2.9		nC
DRAIN-SOURCE DIODE CHARACTERISTICS						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =1A			1.2	V
Diode Forward Current (Note 2)	Is				1	Α

NOTES:

- Repetitive Rating: Pulse width limited by maximum junction temperature.
 Surface Mounted on FR4 Board, t ≤ 10 sec.
 Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
 Guaranteed by design, not subject to production testing.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



Vds 90%

Vgs +t_{d(or)} -t_r +t_{d(of)} -t_f +t_f +t

Figure 1: Switching Test Circuit

Figure 2:Switching Waveforms

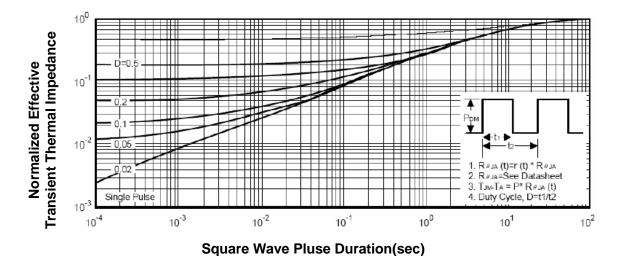
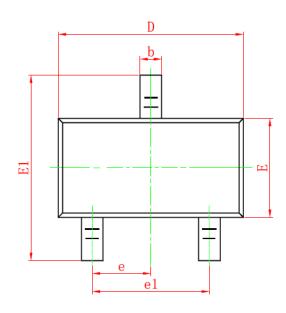


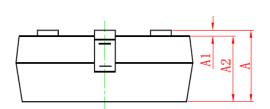
Figure 3: Normalized Maximum Transient Thermal Impedance

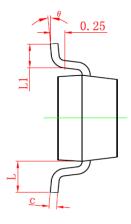


SOT-23 PACKAGE INFORMATION

Dimensions in Millimeters (UNIT:mm)







Symbol	Dimensions in Millimeters				
	MIN.	MAX.			
Α	0.900	1.150			
A 1	0.000	0.100			
A2	0.900	1.050			
b	0.300	0.500			
С	0.080	0.150			
D	2.800	3.000			
E	1.200	1.400			
E1	2.250	2.550			
е	0.950TYP				
e1	1.800	2.000			
L	L 0.550REF				
L1	0.300	0.500			
θ	0°	8°			

NOTES

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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