



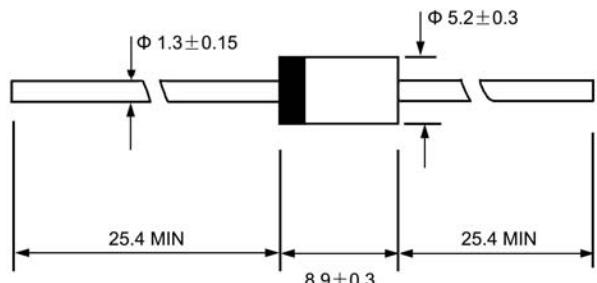
VOLTAGE RANGE: 600 V
CURRENT: 3.0 A

Features

- ◊ Low cost
- ◊ Diffused junction
- ◊ Low leakage
- ◊ Low forward voltage drop
- ◊ High current capability
- ◊ Easily cleaned with alcohol, Isopropanol and similar solvents
- ◊ The plastic material carries U/L recognition 94V-0

Mechanical Data

- ◊ Case: JEDEC DO-27, molded plastic
- ◊ Terminals: Axial leads, solderable per MIL-STD-202, Method 208
- ◊ Polarity: Color band denotes cathode
- ◊ Weight: 0.041 ounces, 1.15 grams
- ◊ Mounting: Any



Dimensions in millimeters

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate by 20%.

		RL4A	UNITS
Maximum recurrent peak reverse voltage	V_{RRM}	600	V
Maximum RMS voltage	V_{RMS}	420	V
Maximum DC blocking voltage	V_{DC}	600	V
Maximum average forward rectified current 9.5mm lead length, $@ T_A = 75^\circ C$	$I_{F(AV)}$	3.0	A
Peak forward surge current 8.3ms single half-sine-wave superimposed on rated load $@ T_J = 125^\circ C$	I_{FSM}	80.0	A
Maximum instantaneous forward voltage $@ 3.0A$	V_F	1.5	V
Maximum reverse current $@ T_A = 25^\circ C$ at rated DC blocking voltage $@ T_A = 100^\circ C$	I_R	50.0 100.0	μA
Maximum reverse recovery time (Note1)	t_{rr}	35	ns
Typical junction capacitance (Note2)	C_J	95	pF
Typical thermal resistance (Note3)	$R_{\theta JL}$	8	$^\circ C / W$
Operating junction temperature range	T_J	- 55 ----- + 150	$^\circ C$
Storage temperature range	T_{STG}	- 55 ----- + 150	$^\circ C$

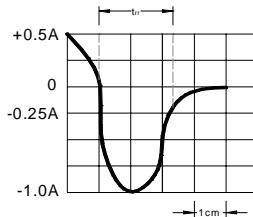
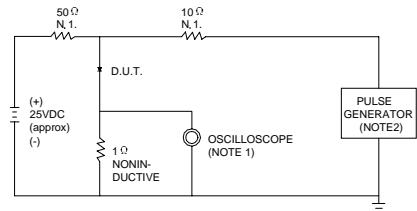
NOTE: 1. Measured with $I_F=0.5A$, $I_R=1A$, $I_{rr}=0.25A$.

2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

3. Thermal resistance from junction to ambient.

Ratings AND Characteristic Curves

FIG.1 – TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



NOTES:
 1. RISE TIME = 7ns MAX INPUT IMPEDANCE = 1MΩ. 22pF.
 2. RISE TIME = 10ns MAX SOURCE IMPEDANCE = 50 Ω.

FIG.2 – TYPICAL FORWARD CHARACTERISTIC

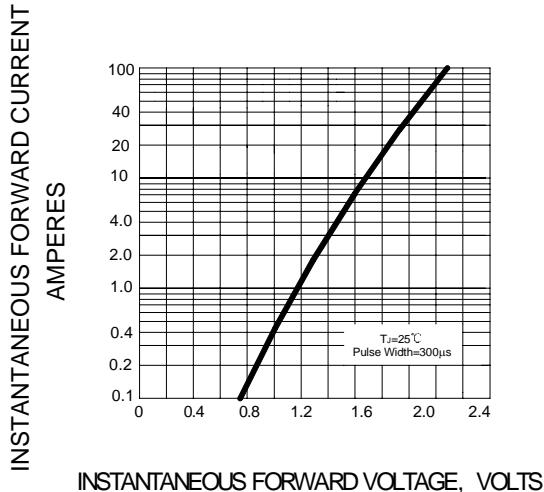


FIG.3 – FORWARD DERATING CURVE

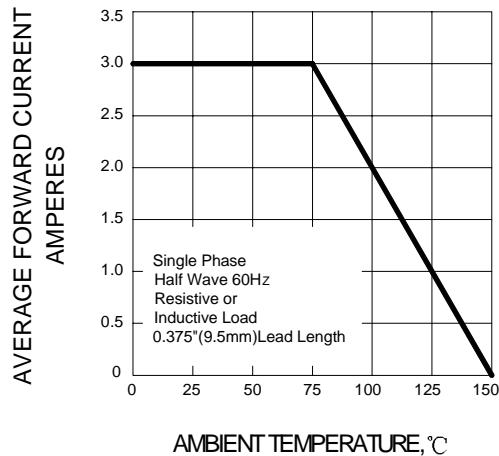


FIG.4 – PEAK FORWARD SURGE CURRENT

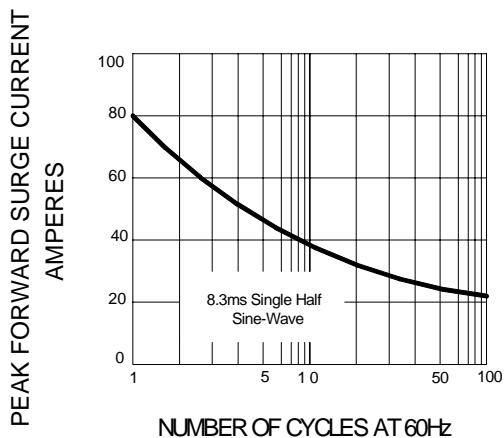


FIG.5 – TYPICAL JUNCTION CAPACITANCE

