



# HER2010BC-HER2060BC

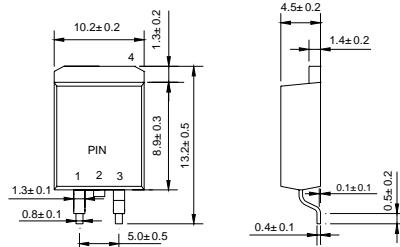
High Efficiency Rectifiers

**VOLTAGE RANGE: 100 --- 600 V**  
**CURRENT: 20 A**

## Features

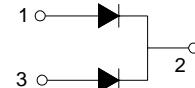
- ◇ Low cost
- ◇ Low leakage
- ◇ Low forward voltage drop
- ◇ High current capability
- ◇ Easily cleaned with alcohol, Isopropanol and similar solvents
- ◇ The plastic material carries U/L recognition 94V-0

## D<sup>2</sup>PAK



## Mechanical Data

- ◇ Case: JEDEC D<sup>2</sup>PAK, molded plastic
- ◇ Polarity: As marked
- ◇ Weight: 0.087 ounces, 2.2 grams
- ◇ Mounting position: Any



Dimensions in millimeters

## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate by 20%.

		HER 2010BC	HER 2020BC	HER 2040BC	HER 2060BC	UNITS
Maximum recurrent peak reverse voltage	V <sub>RRM</sub>	100	200	400	600	V
Maximum RMS voltage	V <sub>RMS</sub>	70	140	280	420	V
Maximum DC blocking voltage	V <sub>DC</sub>	100	200	400	600	V
Maximum average forward rectified current @ T <sub>C</sub> =75 °C	I <sub>F(AV)</sub>	20				A
Peak forward surge current 8.3ms single half-sine-wave superimposed on rated load @ T <sub>J</sub> =125°C	I <sub>FSM</sub>	200				A
Maximum instantaneous forward voltage @ 10A	V <sub>F</sub>	1.0		1.3	1.7	V
Maximum reverse current @ T <sub>C</sub> =25°C at rated DC blocking voltage @ T <sub>C</sub> =100°C	I <sub>R</sub>	10 150			μA	
Maximum reverse recovery time (Note1)	t <sub>rr</sub>	50		100	ns	
Typical junction capacitance (Note2)	C <sub>J</sub>	40				pF
Typical thermal resistance (Note3)	R <sub>θJC</sub>	2.5				°C/W
Operating junction temperature range	T <sub>J</sub>	-55 ---- +150				°C
Storage temperature range	T <sub>STG</sub>	-55 ---- +150				°C

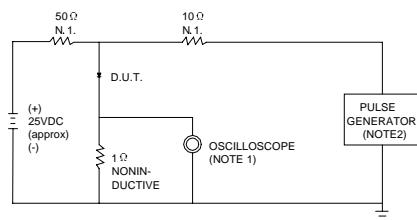
NOTE: 1. Measured with I<sub>F</sub>=0.5A, I<sub>R</sub>=1A, I<sub>rr</sub>=0.25A.

2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

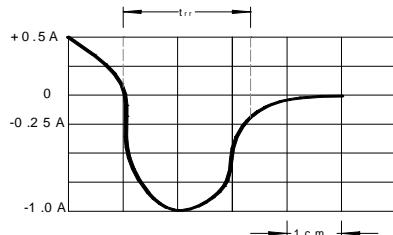
3. Thermal resistance junction to case.

## Ratings AND Characteristic Curves

**FIG.1 -- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC**

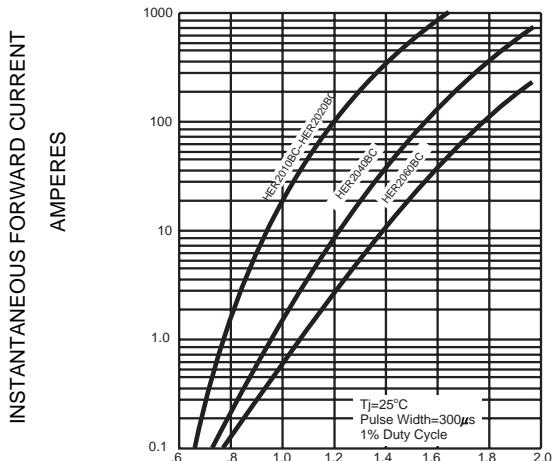


NOTES: 1. RISE TIME = 7ns MAX. INPUT IMPEDANCE = 1M $\Omega$ .22pF.  
 2. RISE TIME = 10ns MAX. SOURCE IMPEDANCE=50  $\Omega$ .

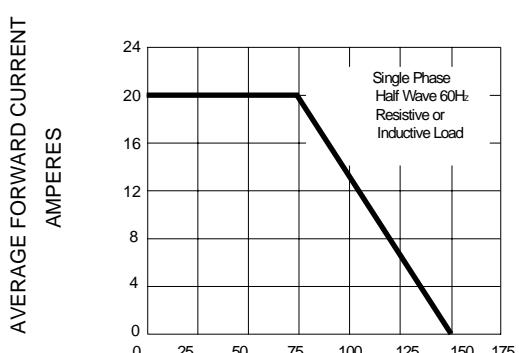


SET TIME BASE FOR 25 ns/cm

**FIG.2 -- TYPICAL FORWARD CHARACTERISTIC**

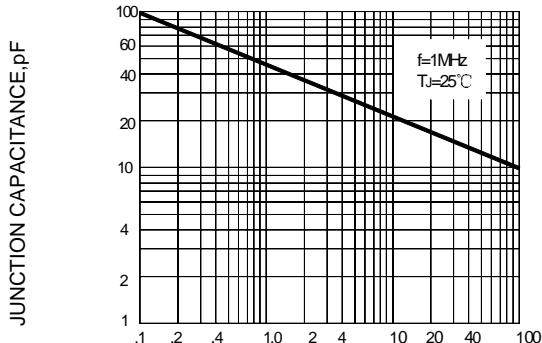


INSTANTANEOUS FORWARD VOLTAGE, VOLTS



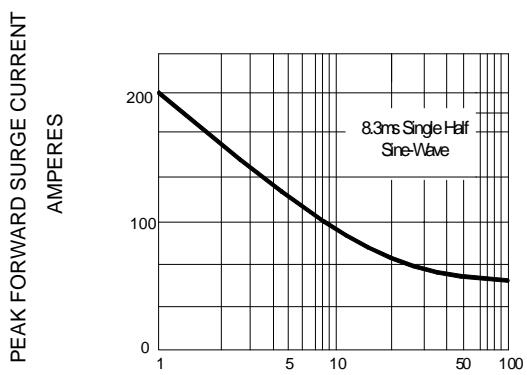
CASE TEMPERATURE, °C

**FIG.4 -- TYPICAL JUNCTION CAPACITANCE**



REVERSE VOLTAGE, VOLTS

**FIG.5 -- PEAK FORWARD SURGE CURRENT**



NUMBER OF CYCLES AT 60Hz