

●Application

General rectification

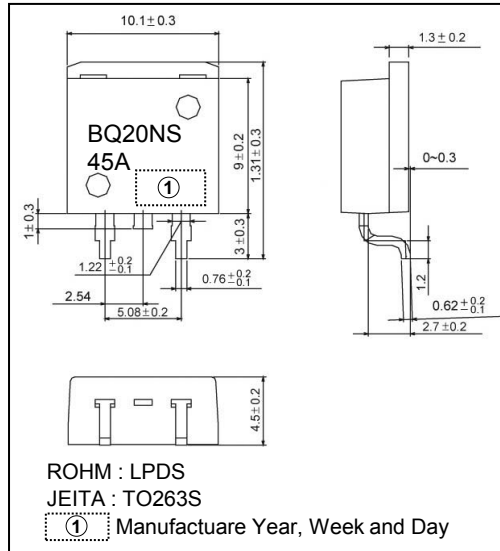
●Features

- 1) Cathode common dual type (LPDS)
- 2) Low I_R

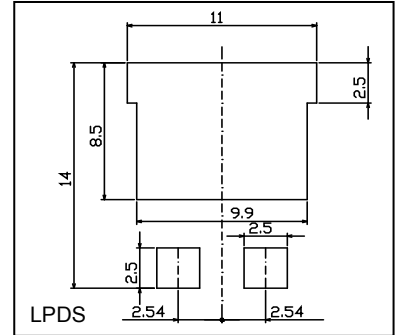
●Construction

Silicon epitaxial planar

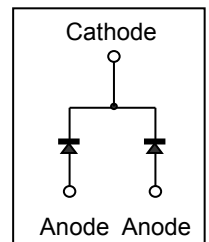
●Dimensions (Unit : mm)



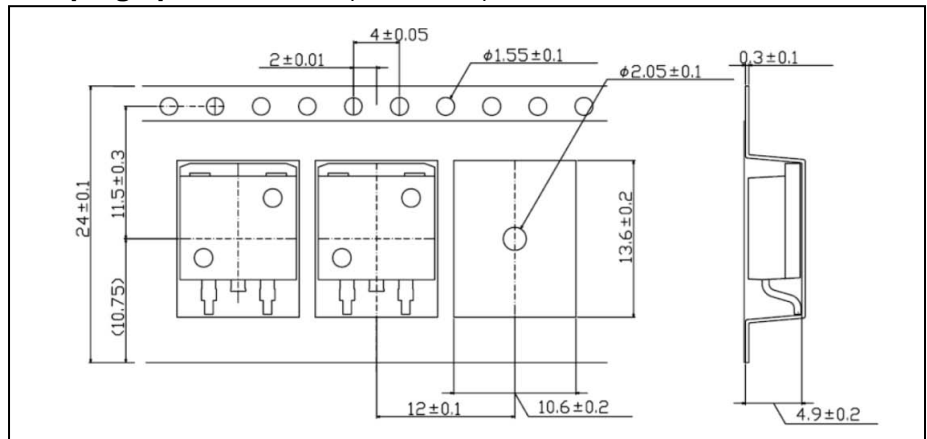
●Land size figure (Unit : mm)



●Structure



●Taping specifications (Unit : mm)



●Absolute maximum ratings (Tc= 25°C)

Parameter	Symbol	Limits	Unit
Reverse voltage (repetitive)	V_{RM}	45	V
Reverse voltage (DC)	V_R	45	V
Average rectified forward current (*1)	I_o	20	A
Forward current surge peak (60Hz·1cyc) (*2)	I_{FSM}	100	A
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-40 to +150	°C

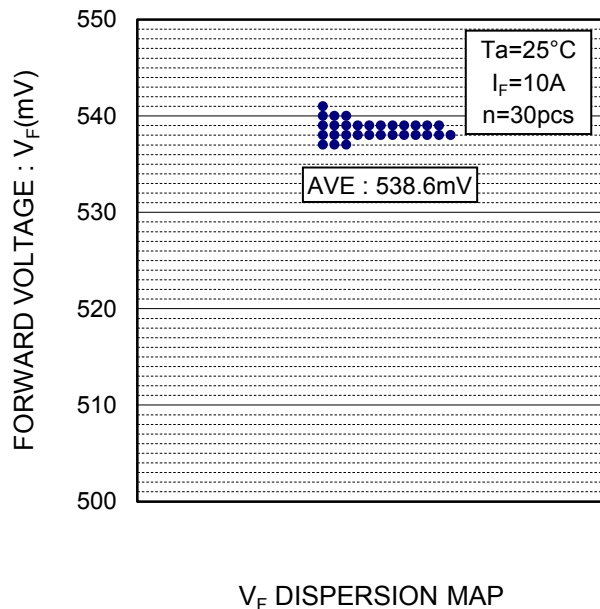
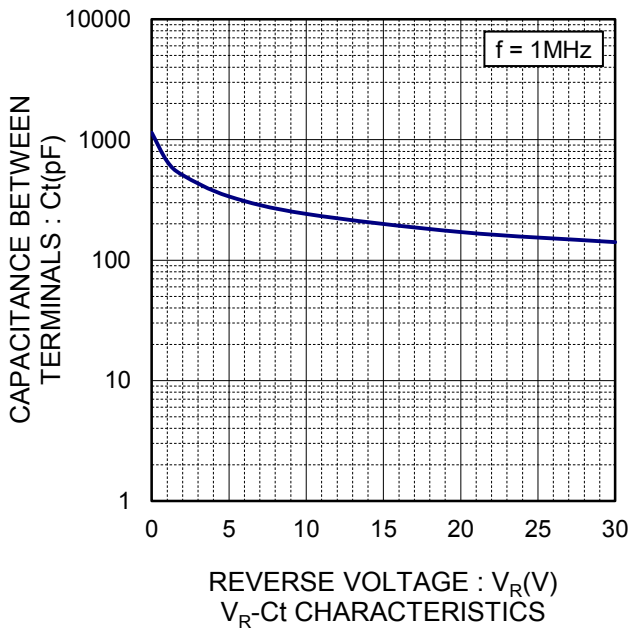
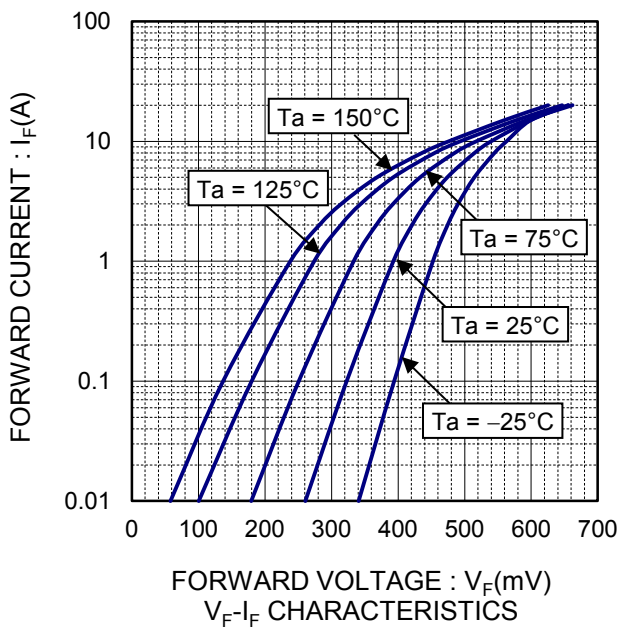
(*1) 60Hz half sin wave, 1/2 I_o per diode.

(*2) 60Hz half sin wave, one cycle, non-repetitive at $T_j=25^\circ\text{C}$.

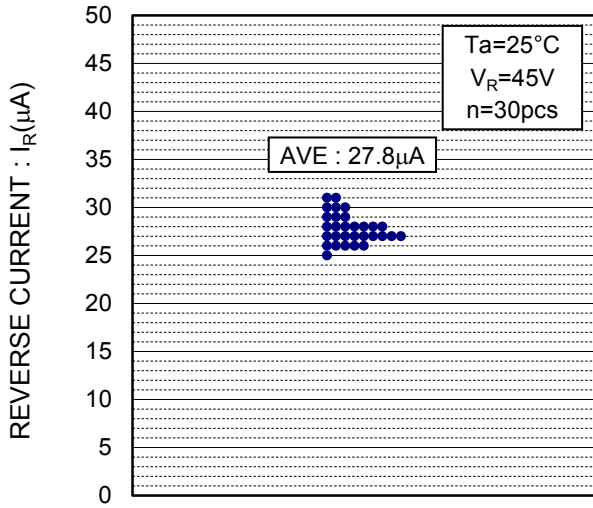
●Electrical characteristics (Tj = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V_F	-	-	0.65	V	$I_F=10\text{A}$
Reverse current	I_R	-	-	0.3	mA	$V_R=45\text{V}$

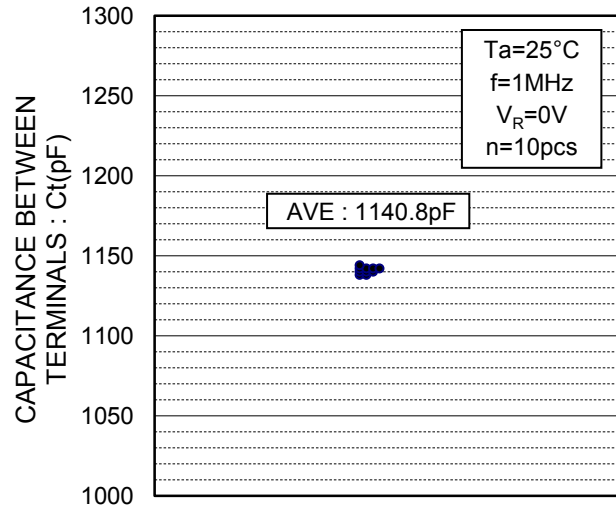
●Electrical characteristic curves



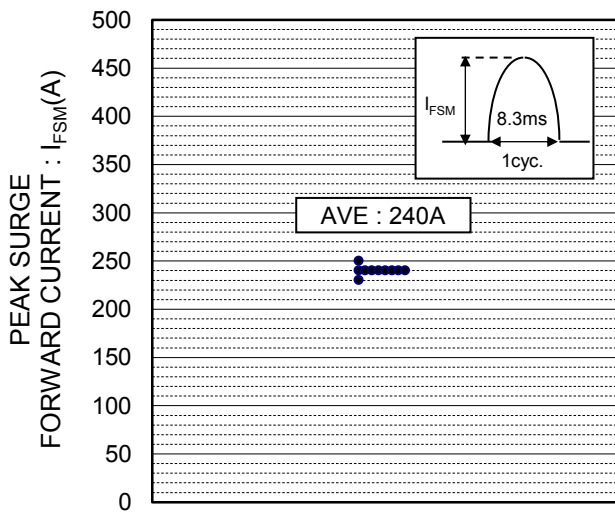
●Electrical characteristic curves



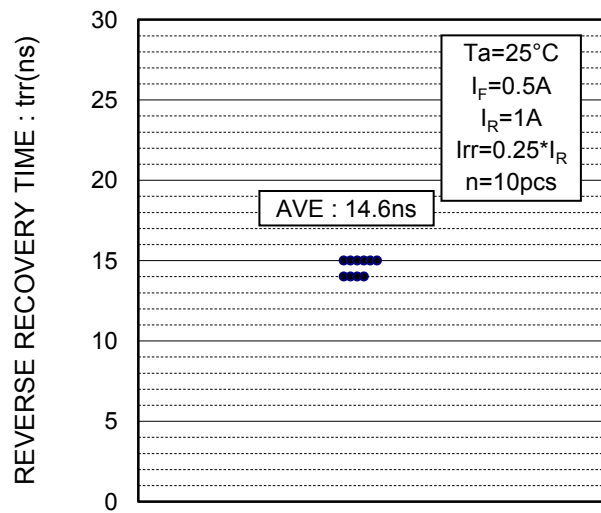
I_R DISPERSION MAP



C_t DISPERSION MAP

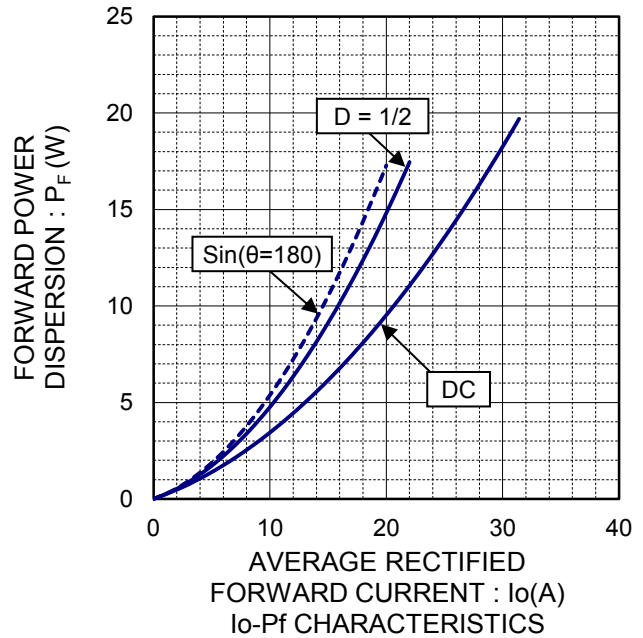
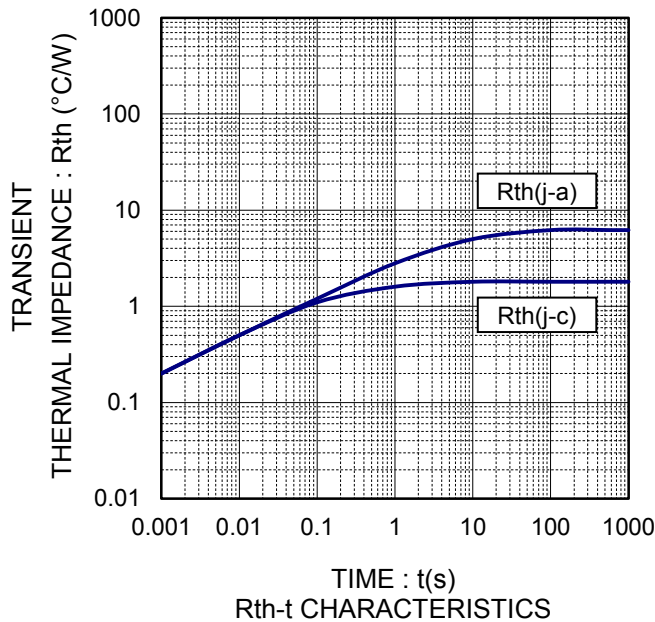
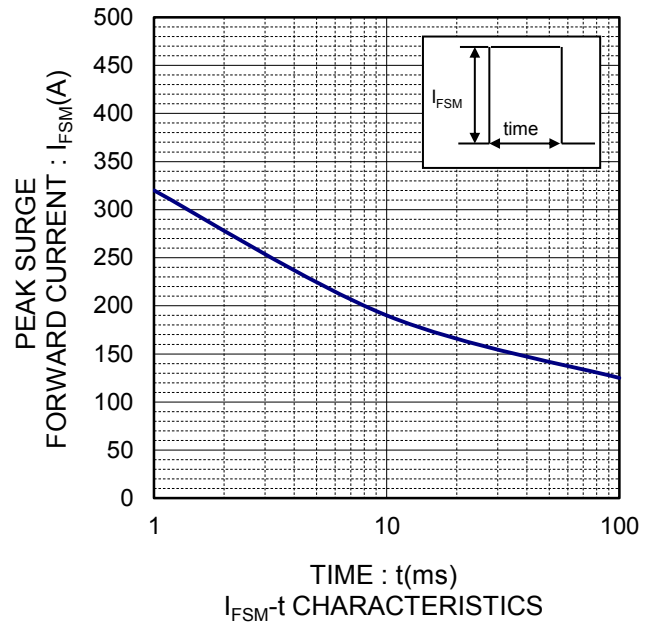
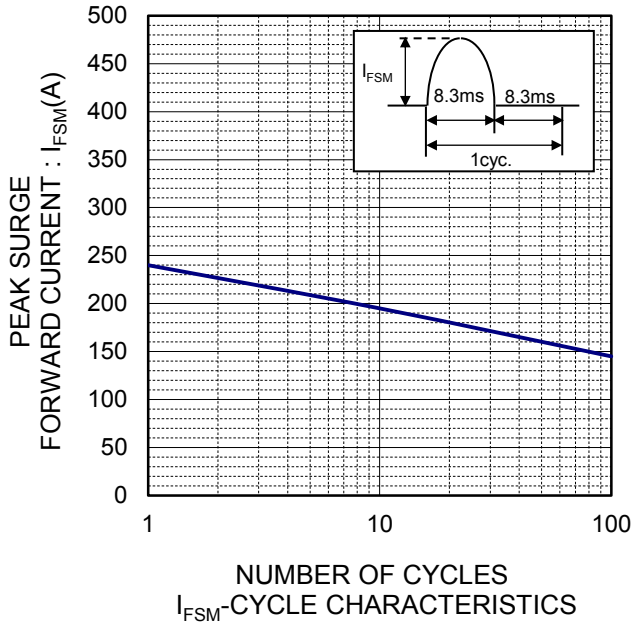


I_{FSM} DISPERSION MAP

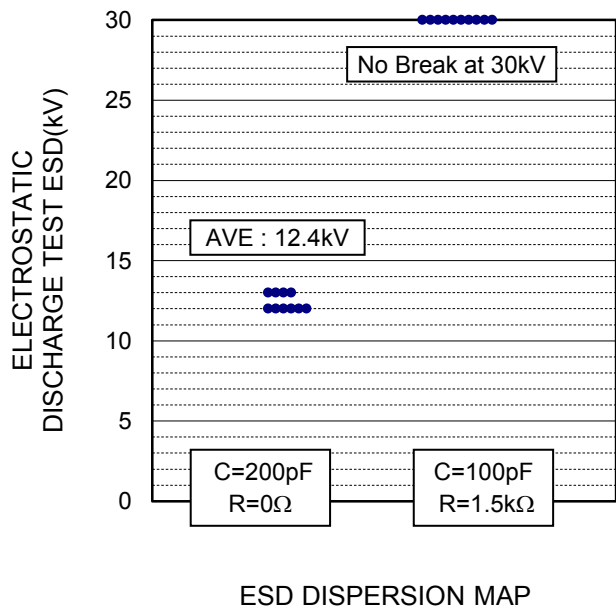
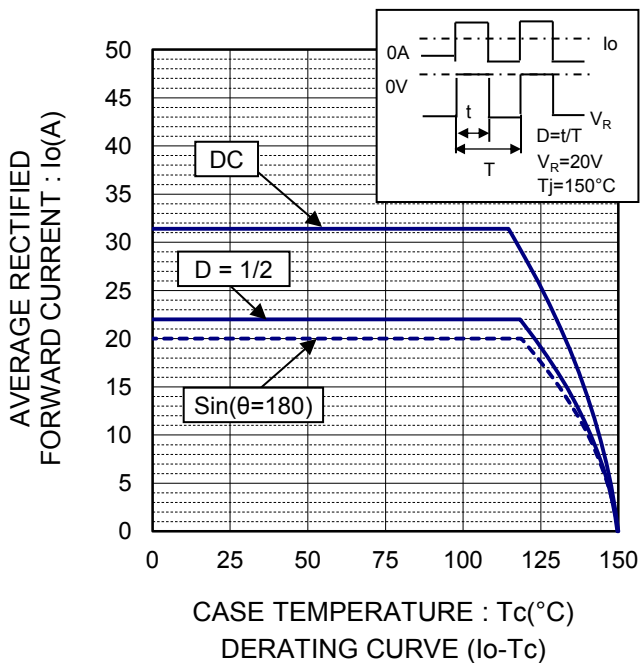
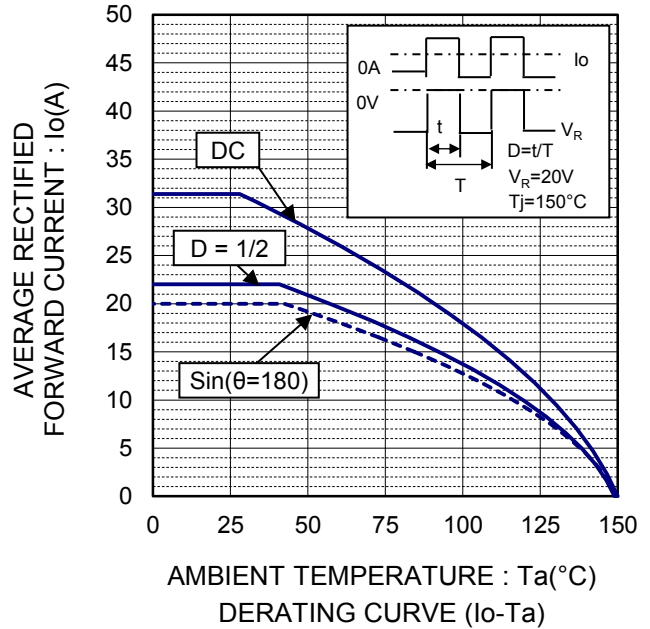
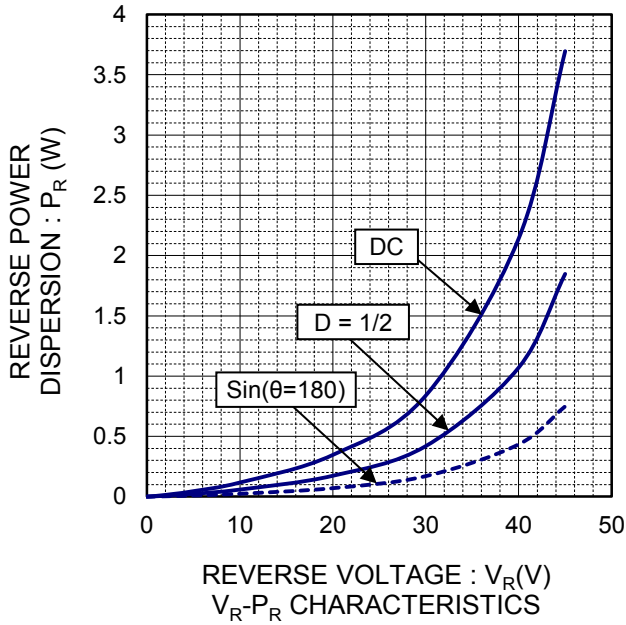


t_{rr} DISPERSION MAP

●Electrical characteristic curves



●Electrical characteristic curves



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