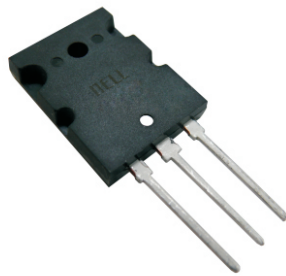


## Silicon PNP triple diffusion planar transistor -15A/-230V/150W



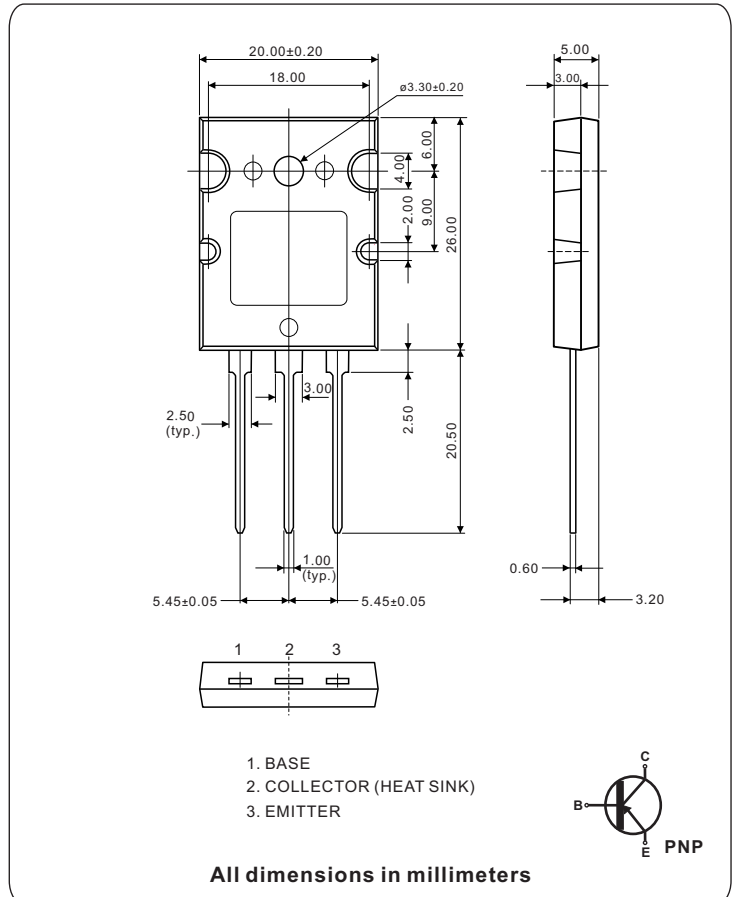
TO-3PL

### FEATURES

- High breakdown voltage,  $V_{CE0} = -230V$  (min)
- Complementary to 2SC5200BL
- TO-3PL package which can be installed to the heat sink with one screw

### APPLICATIONS

- Suitable for use in 100W high fidelity audio amplifier's output stage



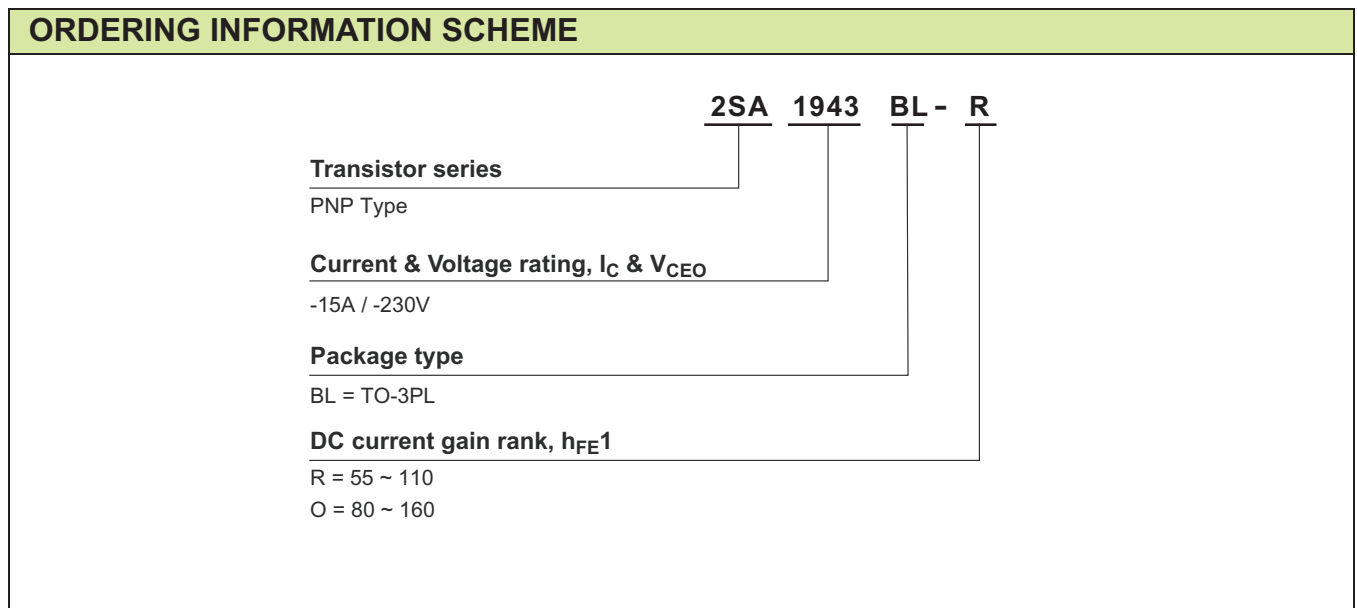
### ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ C$ )

| SYMBOL    | PARAMETER                              | VALUE                  | UNIT       |
|-----------|--|------------------------|------------|
| $V_{CBO}$ | Collector to base voltage              | -230                   | V          |
| $V_{CEO}$ | Collector to emitter voltage           | -230                   |            |
| $V_{EBO}$ | Emitter to base voltage                | -5                     |            |
| $I_{CP}$  | Peak collector current $t_p \leq 5$ ms | -30                    | A          |
| $I_C$     | Collector current                      | -15                    |            |
| $I_B$     | Base current                           | -1.5                   |            |
| $P_C$     | Collector power dissipation            | $T_C = 25^\circ C$ 150 | W          |
| $T_j$     | Junction temperature                   | 150                    | $^\circ C$ |
| $T_{stg}$ | Storage temperature                    | -55 to 150             |            |

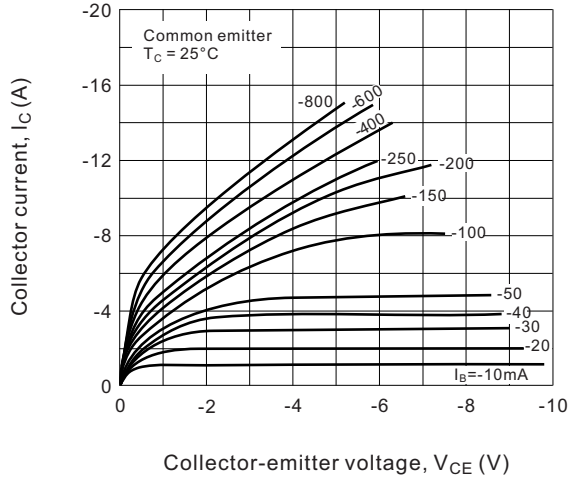
### THERMAL CHARACTERISTICS ( $T_C = 25^\circ C$ )

| SYMBOL        | PARAMETER                                    | VALUE | UNIT         |
|---------------|--|-------|--------------|
| $R_{th(j-c)}$ | Maximum thermal resistance, junction to case | 1.10  | $^\circ C/W$ |

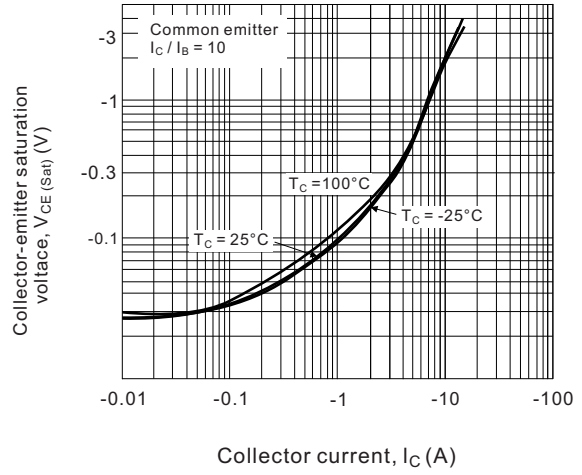
| ELECTRICAL CHARACTERISTICS (T <sub>a</sub> = 25°C) |   |  |        |      |      |      |
|--|---|--|--------|------|------|------|
| SYMBOL   | PARAMETER   | CONDITIONS   | VALUE  |      |      | UNIT |
|  |   |  | MIN.   | TYP. | MAX. |      |
| I <sub>CBO</sub>                                   | Collector cutoff current                            | V <sub>CB0</sub> = -230V, I <sub>E</sub> = 0         |        |      | -5.0 | μA   |
| I <sub>EBO</sub>                                   | Emitter cutoff current                              | V <sub>EBO</sub> = -5V, I <sub>C</sub> = 0           |        |      | -5.0 |      |
| V <sub>(BR)CEO</sub>                               | Collector to emitter breakdown voltage              | I <sub>CEO</sub> = -50mA, I <sub>B</sub> = 0         | -230   |      |      | V    |
| V <sub>CB0</sub>                                   | Collector to base voltage                           | I <sub>CB0</sub> = -100 μA                           | -230   |      |      |      |
| V <sub>EBO</sub>                                   | Emitter to base voltage                             | I <sub>EBO</sub> = -100 μA                           | -5     |      |      |      |
| h <sub>FE1</sub>                                   | Forward current transfer ratio<br>(DC current gain) | V <sub>CE</sub> = -5V, I <sub>C</sub> = -1A          | Rank-R | 55   |      | 110  |
| h <sub>FE2</sub>                                   |   |  | Rank-O | 80   |      | 160  |
|  |   | V <sub>CE</sub> = -5V, I <sub>C</sub> = -7A          | 35     | 60   |      |      |
| V <sub>CE(sat)</sub>                               | Collector to emitter saturation voltage             | I <sub>C</sub> = -8A, I <sub>B</sub> = -0.8A         |        | -1.5 | -3.0 | V    |
| V <sub>BE</sub>                                    | Base to emitter voltage                             | V <sub>CE</sub> = -5V, I <sub>C</sub> = -7A          |        | -1.0 | -1.5 |      |
| f <sub>T</sub>                                     | Transition frequency<br>(Gain-Bandwidth product)    | V <sub>CE</sub> = -5V, I <sub>C</sub> = -1A          |        | 30   |      | MHz  |
| C <sub>ob</sub>                                    | Collector output capacitance                        | V <sub>CB</sub> = -10V, I <sub>E</sub> = 0, f = 1MHz |        | 360  |      | pF   |



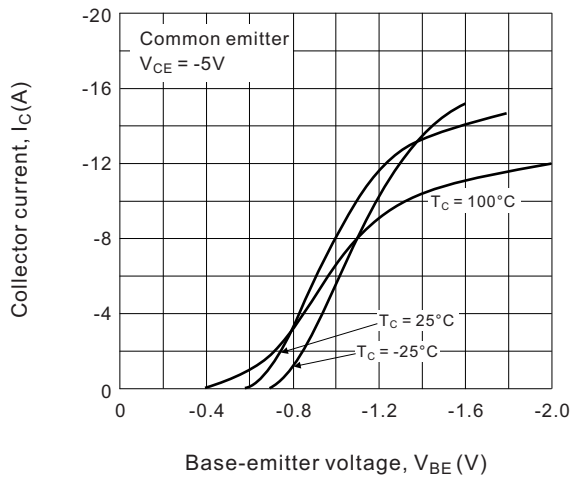
**Fig.1  $I_C - V_{CE}$**



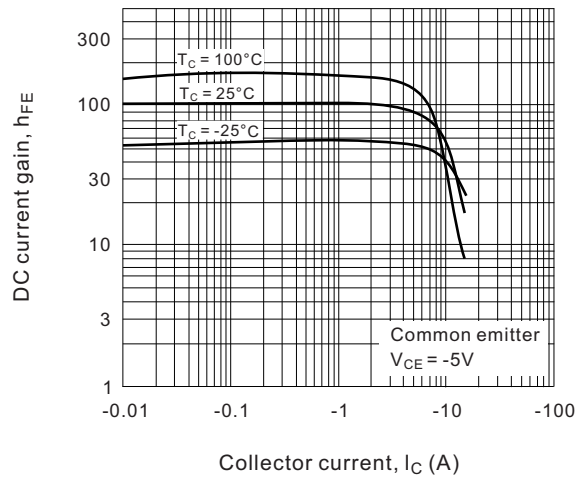
**Fig.2  $V_{CE(sat)} - I_C$**



**Fig.3  $I_C - V_{BE}$**



**Fig.4 DC current gain**



**Fig.5 Safe operating area**

