

### Phase Control Thyristors

#### FEATURES

- Both metric and inch threads
- Qualified for industrial level
- International standard case TO-209AB
- Metal case with glass insulator

#### TYPICAL APPLICATIONS

- Controlled DC power supplies
- AC controllers
- DC motor controls
- AC switches

MAJOR RATINGS AND CHARACTERISTICS			
PARAMETER	TEST CONDITIONS	VALUES	UNITS
I <sub>T(AV)</sub>		180	A
	T <sub>Case</sub>	85	°C
I <sub>T(RMS)</sub>		285	A
	T <sub>Case</sub>	85	°C
I <sub>TSM</sub>	50 Hz	5230	A
	60 Hz	5700	A
I <sup>2</sup> t	50 Hz	124.77	kA <sup>2</sup> s
	60 Hz	136.00	kA <sup>2</sup> s
V <sub>DRM</sub> /V <sub>RSM</sub>		200 to 1600	V
t <sub>q</sub>	Typical	100	µs
T <sub>J</sub>		-40 to 125	°C

#### ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS				
SERIES	VOLTAGE CODE	V <sub>DRM</sub> /V <sub>RSM</sub> , MAX. RE- PETITIVE PEAK AND OFF-STATE VOLTAGE	V <sub>RSM</sub> , MAX. NON- REPETITIVE PEAK VOLTAGE (V)	I <sub>DRM</sub> /I <sub>RRM</sub> , MAX. at T <sub>J</sub> = T <sub>J(Max.)</sub> (mA)
N180/...M(I)	02	200	300	30
N180/...M(I)	04	400	500	
N180/...M(I)	06	600	700	
N180/...M(I)	08	800	900	
N180/...M(I)	10	1000	1100	
N180/...M(I)	12	1200	1300	
N180/...M(I)	14	1330	1500	
N180/...M(I)	16	1520	1700	

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MAXIMUM ALLOWABLE RATINGS					
SYMBOL	DESCRIPTION	TEST CONDITIONS		VALUE	UNITS
$I_{T(AV)}$	Maximum average on-state current at heatsink temperature	180° conduction, half sine wave		180	A
				85	°C
$I_{T(RMS)}$	Maximum RMS on-state current	DC at 25°C heatsink temperature		285	A
$I_{TSM}$	Maximum peak, one-cycle non-repetitive surge current	t = 10 ms	100% $V_{RRM}$ reapplied	Sinusoidal half wave, initial $T_J = T_J \text{ max.}$	kA
		t = 8.3 ms			
		t = 10 ms	No voltage reapplied		
		t = 8.3 ms			
$I^2t$	Maximum $I^2t$	t = 10 ms	100% $V_{RRM}$ reapplied	88.40	kA <sup>2</sup> s
		t = 8.3 ms			
		t = 10 ms	No voltage reapplied	124.77	
		t = 8.3 ms			
$I^2t^{1/2}$	Maximum $I^2t^{1/2}$	t = 0.1 to 10 ms, no voltage reapplied		1490	kA <sup>2</sup> s <sup>1/2</sup>
$V_{T(TO)}$	Low level threshold voltage	(16.7% $\times \pi \times I_{T(AV)} < I < \pi \times I_{T(AV)}$ ), $T_J = T_J \text{ max.}$		0.86	V
$r_T$	Low level on-state slope resistance			1.32	mΩ
$V_{TM}$	Maximum on-state voltage	$I_{pk} = 565A$ , 50Hz half sine pulse, $T_J = T_J \text{ max.}$		1.61	V
$I_H$	Maximum holding current	$T_J = 25^\circ\text{C}$ , anode supply 12V resistive load		500	mA
$I_L$	Typical latching current			400	

SWITCHING					
SYMBOL	DESCRIPTION	TEST CONDITIONS		VALUE	UNITS
dI/dt	Maximum non-repetitive rate of rise of turned-on current	Gate drive 20 V, 20 Ω, $t_r \leq 1 \mu\text{s}$ , $T_J = T_J \text{ max.}$ , anode voltage $\leq 80\% V_{DRM}$		800	A/ $\mu\text{s}$
$t_d$	Typical delay time	Gate current 1 A, $dI_g/dt = 1 \text{ A}/\mu\text{s}$ , $V_d = 0.67\% V_{DRM}$ , $T_J = 25^\circ\text{C}$		0.7	$\mu\text{s}$
$t_q$	Typical turn-off time			100	

BLOCKING					
SYMBOL	DESCRIPTION	TEST CONDITIONS		VALUE	UNITS
dV/dt	Maximum critical rate of rise of off-state voltage	$T_J = T_J \text{ max.}$ Linear to 80% rated $V_{DRM}$		1000	V/ $\mu\text{s}$
$I_{RRM}, I_{DRM}$	Maximum peak reverse and off-state leakage current	$T_J = T_J \text{ max.}$ , rated $V_{DRM}/V_{RRM}$ applied		30	mA

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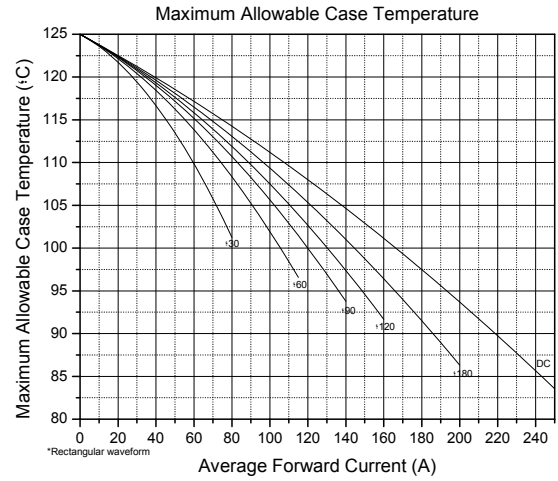
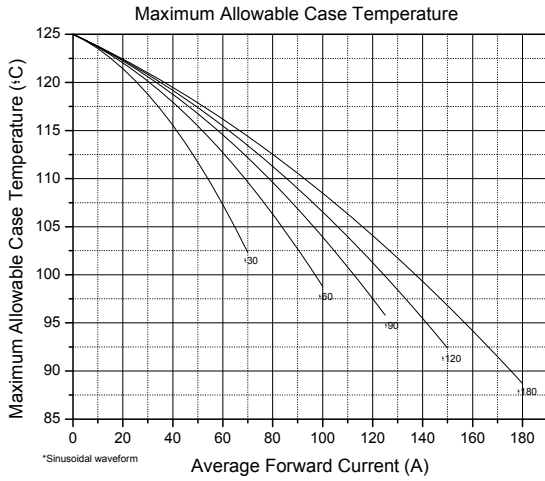
TRIGGERING						
SYMBOL	DESCRIPTION	TEST CONDITIONS		VALUE		UNITS
				TYP.	MAX.	
$P_{GM}$	Maximum peak gate power	$T_J = T_J \text{ max.}, t_p \leq 5\text{ms}$		10		W
$P_{G(AV)}$	Maximum average gate power	$T_J = T_J \text{ max.}, f = 50\text{Hz}, d\% = 50$		2		
$I_{GM}$	Maximum peak gate current	$T_J = T_J \text{ max.}, t_p \leq 5\text{ms}$		1		A
$-V_{GM}$	Maximum negative gate voltage	$T_J = T_J \text{ max.}, t_p \leq 5\text{ms}$		2		V
$I_{GT}$	DC gate current to trigger	$T_J = -40^\circ\text{C}$	Maximum required gate trigger/current/voltage are the lowest values which will trigger all units, 12V anode to cathode applied	-	360	mA
		$T_J = 25^\circ\text{C}$		-	180	
$V_{GT}$	DC gate voltage to trigger	$T_J = -40^\circ\text{C}$		-	6	V
		$T_J = 25^\circ\text{C}$		-	3	
$V_{GD}$	DC gate voltage not to trigger	$T_J = T_J \text{ max.}$	Maximum gate current/voltage not to trigger is the maximum value which will not trigger any unit with rated $V_{DRM}$ anode to cathode applied	0.3		V

THERMAL AND MECHANICAL SPECIFICATIONS						
SYMBOL	DESCRIPTION	TEST CONDITIONS	VALUE	UNITS		
$T_J$	Maximum operating junction temperature	-	-40 to 125	$^\circ\text{C}$		
$T_{Stg}$	Maximum storage temperature	-	-40 to 150			
$R_{thJ-hs}$	Maximum thermal resistance, junction to heatsink	DC	0.140	$^\circ\text{C/W}$		
		180° sine wave	0.160			
		120° rectangular wave	0.164			
$R_{thC-hs}$	Maximum thermal resistance, case to heat-sink	Mtg. Surface smooth, flat, greased	0.080			
-	Mounting force, $\pm 10\%$	-	31			N.m
-	Approximate weight	-	280			g
-	Case style	-	TO-209AB	JEDEC		

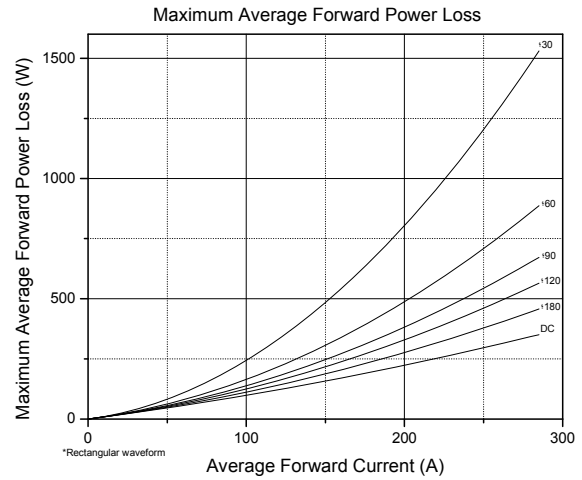
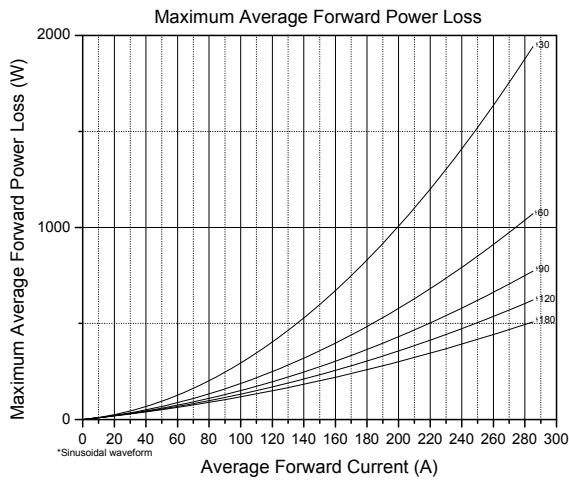
CURRENT FORM FACTOR								
FORM FACTOR	CONDUCTION ANGLE							
	15°	30°	45°	60°	90°	120°	180°	
Sine wave	31.956	15.832	10.452	7.721	4.933	3.527	2.468	
Rectangular wave	24.000	12.000	8.000	6.000	4.000	3.000	2.000	

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#### CURRENT RATINGS CHARACTERISTICS

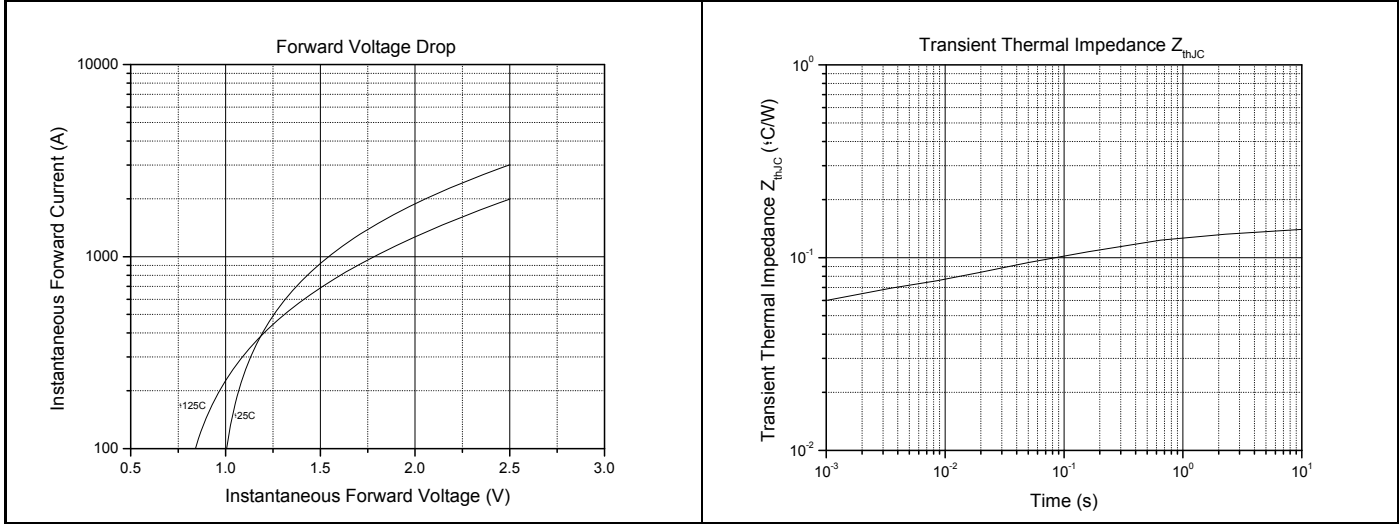


#### ON-STATE POWER LOSS CHARACTERISTICS

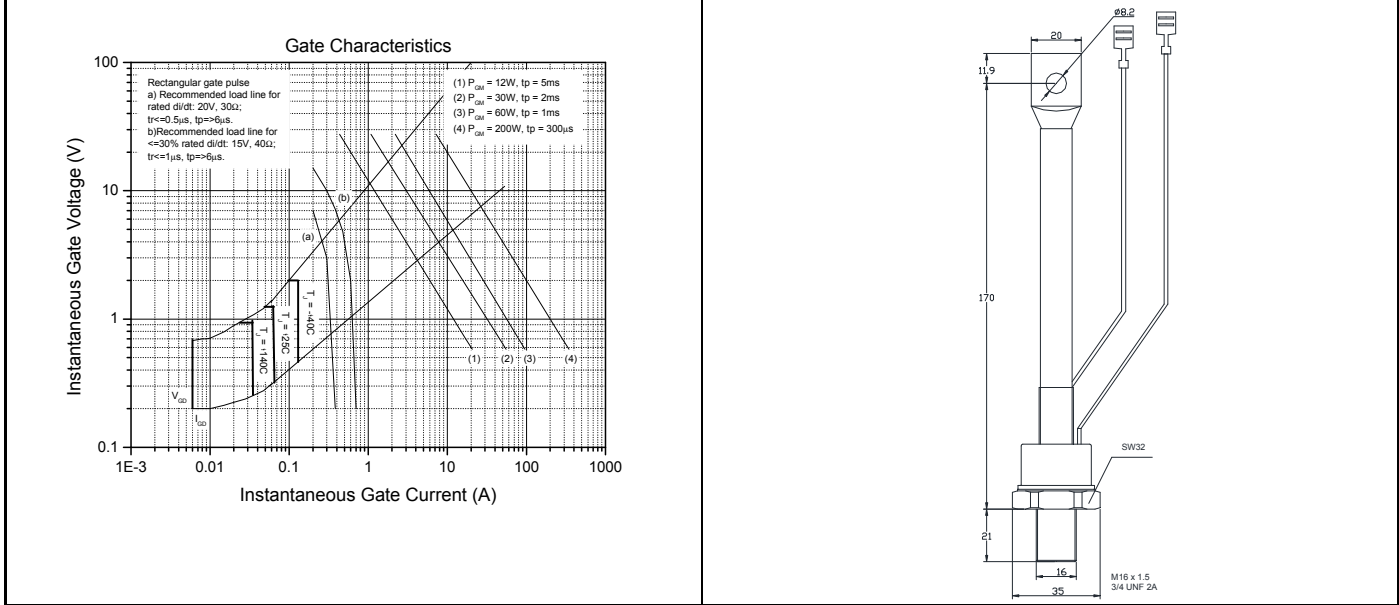


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#### FORWARD VOLTAGE DROP / THERMAL IMPEDANCE CHARACTERISTICS



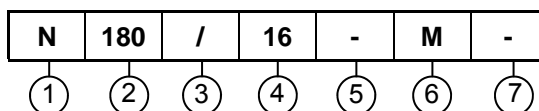
#### GATE TRIGGER / OUTLINE CHARACTERISTICS



### Phase Control Thyristors

#### ORDERING INFORMATION

Device code



- |   |  |
|---|--|
| 1 | <ul style="list-style-type: none"> <li>- N = Phase Control Thyristors</li> <li>- F = Fast Thyristors (inverter grade)</li> <li>- D = Normal Recovery Diodes</li> <li>- DF = Fast Recovery Diodes</li> <li>- DD = Module (diode-diode)</li> <li>- DT = Module (diode-thyristor)</li> <li>- TD = Module (thyristor-diode)</li> <li>- TT = Module (thyristor-thyristor)</li> <li>- P = Press-fit diode</li> </ul> |
| 2 | <ul style="list-style-type: none"> <li>- Average Current Code</li> </ul>   |
| 3 | <ul style="list-style-type: none"> <li>- Essential Part Number</li> </ul>  |
| 4 | <ul style="list-style-type: none"> <li>- Voltage Code x 100 = <math>V_{RRM}</math></li> </ul>  |
| 5 | <ul style="list-style-type: none"> <li>- Turn-off time (fast thyristors only)</li> <li>- Reverse Recovery Time (fast diodes only)</li> </ul>   |
| 6 | <ul style="list-style-type: none"> <li>- M = Metric Thread</li> <li>- I = Inch Thread</li> </ul>   |
| 7 | <ul style="list-style-type: none"> <li>- None = Anode to stud (stud diodes only)</li> <li>- R = Cathode to stud (stud diodes only)</li> </ul>  |

#### Disclaimer

All product specifications and data are subject to change without notice.

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