

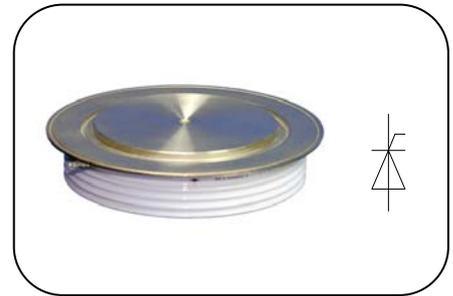
Features

- Center amplifying gate
- Metal case with ceramic insulator
- Low on-state and switching losses

Typical Applications

- AC controllers
- DC and AC motor control
- Controlled rectifiers

$I_{T(AV)}$	1920 A
V_{DRM}/V_{RRM}	4500-5500V
I_{TSM}	22 kA
I^2t	2420 $10^3 A^2S$



SYMBOL	CHARACTERISTIC	TEST CONDITIONS	$T_j(^{\circ}C)$	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled,	$T_C=55^{\circ}C$			2300	A
			$T_C=70^{\circ}C$			1920	
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	$V_{DRM} \& V_{RRM} \ t_p=10ms$ $V_{DSM} \& V_{RSM}= V_{DRM} \& V_{RRM} +100V$	125	4500		5500	V
I_{DRM} I_{RRM}	Repetitive peak current	$V_{DM}= V_{DRM}$ $V_{RM}= V_{RRM}$	125			350	mA
I_{TSM}	Surge on-state current	10ms half sine wave	125			22	kA
I^2t	I^2T for fusing coordination	$V_R=0.6V_{RRM}$				2420	$A^2s \cdot 10^3$
V_{TO}	Threshold voltage		125			1.01	V
r_T	On-state slop resistance					0.36	m Ω
V_{TM}	Peak on-state voltage	$I_{TM}=3000A, F=40kN$	125			2.20	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$	125			2000	V/ μs
di/dt	Critical rate of rise of on-state current	$V_{DM}= 67\%V_{DRM}$ to 3000A, Gate pulse $t_r \leq 0.5\mu s$ IGM=1.5A	125			200	A/ μs
Q_{rr}	Recovery charge	$I_{TM}=2000A, t_p=1000\mu s, di/dt=-5A/\mu s,$ $V_R=50V$	125		3500		μC
I_{GT}	Gate trigger current	$V_A=12V, I_A=1A$	25	40		300	mA
V_{GT}	Gate trigger voltage			0.8		3.0	V
I_H	Holding current			25		200	mA
V_{GD}	Non-trigger gate voltage	$V_{DM}=67\%V_{DRM}$	125	0.3			V
$R_{th(j-c)}$	Thermal resistance Junction to case	At 180° sine double side cooled Clamping force 40.0kN				0.010	$^{\circ}C/W$
$R_{th(c-h)}$	Thermal resistance case to heatsink					0.003	$^{\circ}C/W$
F_m	Mounting force			35	40	47	kN
T_{stg}	Stored temperature			-40		140	$^{\circ}C$
W_t	Weight				880		g
Outline	KT60cT						

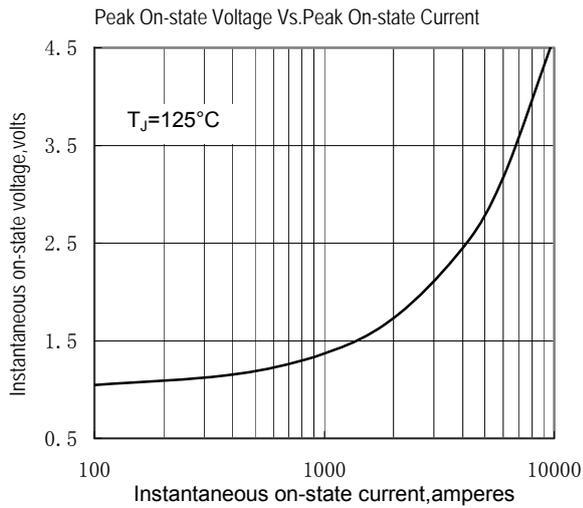


Fig.1

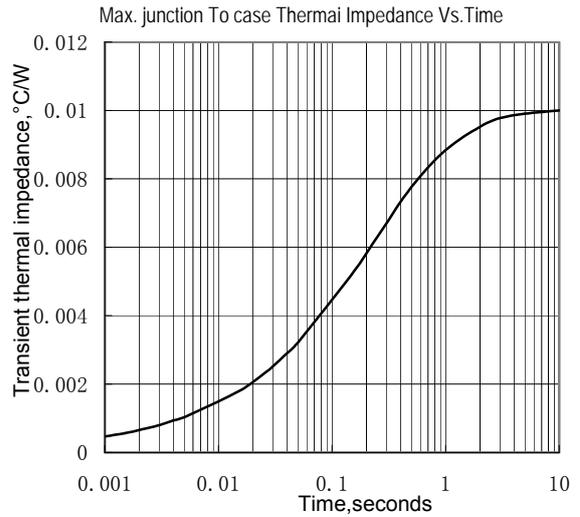


Fig.2

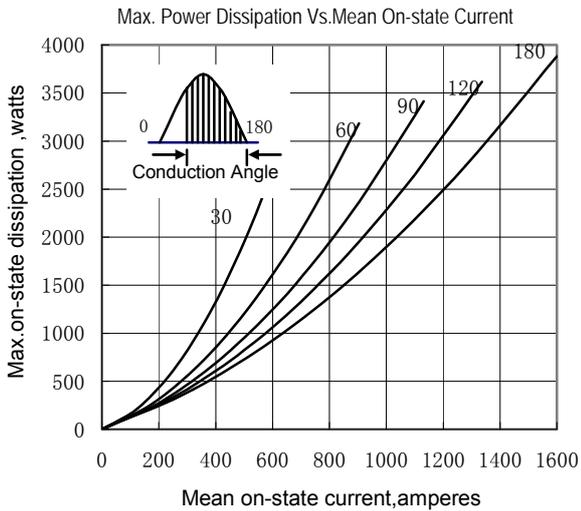


Fig.3

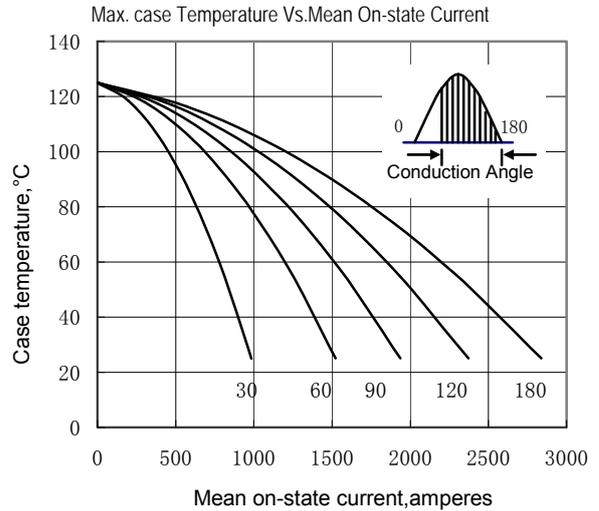


Fig.4

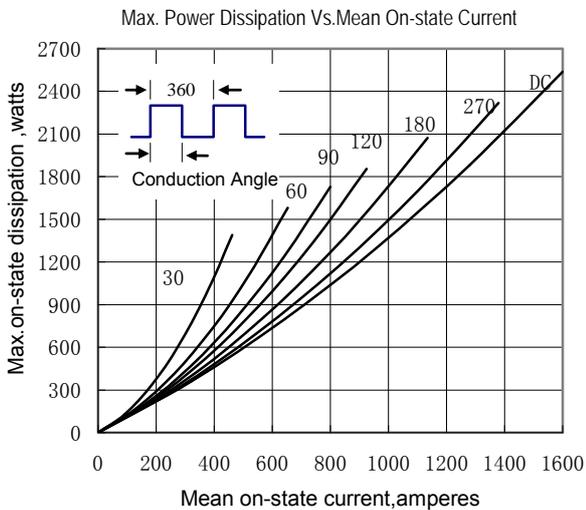


Fig.5

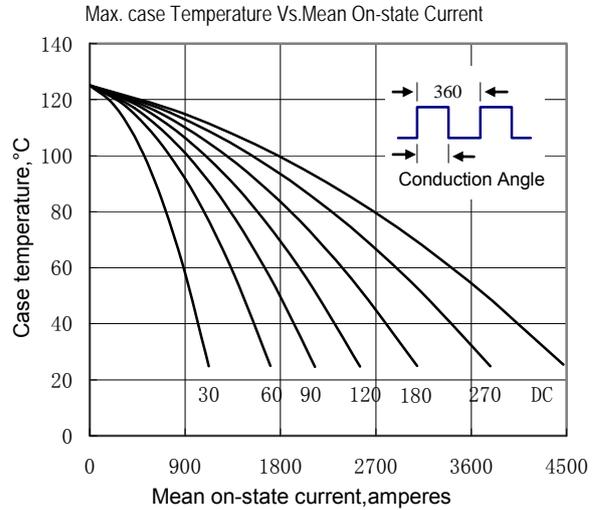


Fig.6

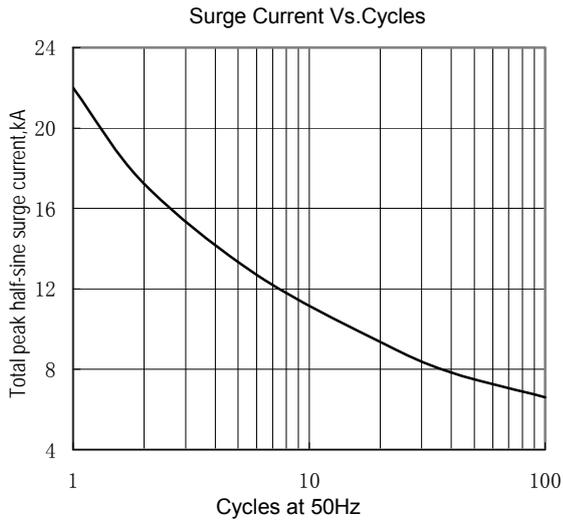


Fig.7

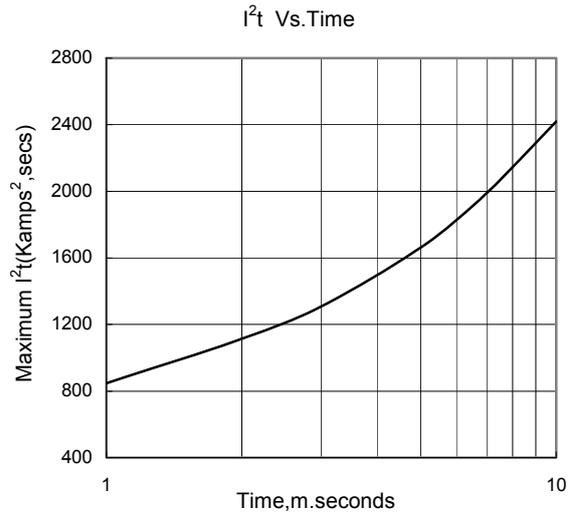


Fig.8

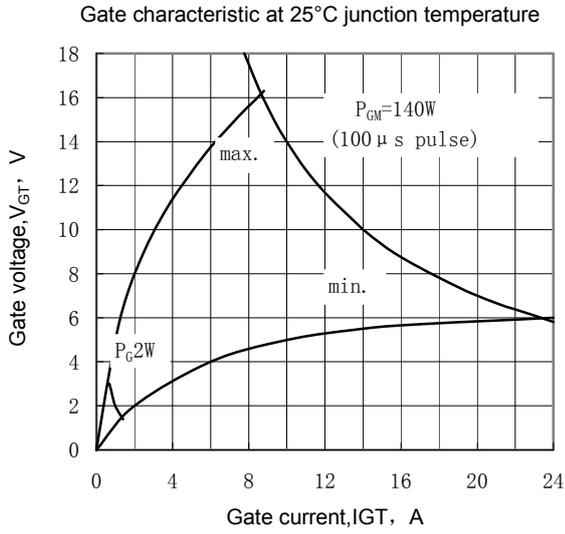


Fig.9

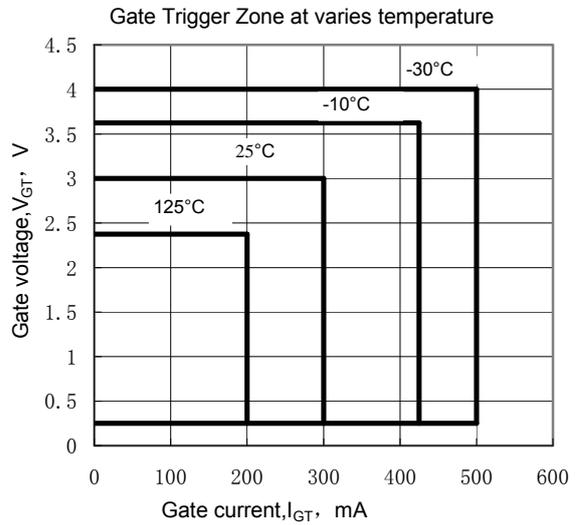


Fig.10

Outline:

