

Features

- Center amplifying gate
- Metal case with ceramic insulator
- Low on-state and switching losses

Typical Applications

- AC controllers
- DC and AC motor control
- Controlled rectifiers

$I_{T(AV)}$ **530A**
 V_{DRM}/V_{RRM} **4300~5200V**
 I_{TSM} **4.5 kA**
 I^2t **101 10³A²S**



SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T _j (°C)	VALUE			UNIT
				Min	Type	Max	
I _{T(AV)}	Mean on-state current	180° half sine wave 50Hz Double side cooled,	T _C =55°C			620	A
			T _C =70°C			530	
V _{DRM} V _{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	V _{DRM} &V _{RRM} tp=10ms V _{DSM} &V _{RSM} = V _{DRM} &V _{RRM} +100V	125	4300		5200	V
I _{DRM} I _{RRM}	Repetitive peak current	V _{DM} =V _{DRM} V _{RM} =V _{RRM}	125			50	mA
I _{TSM}	Surge on-state current	10ms half sine wave	125			4.5	kA
I ² t	I ² T for fusing coordination	V _R =0.6V _{RRM}				101	A ² s*10 ³
V _{TO}	Threshold voltage		125			1.45	V
r _T	On-state slop resistance					1.15	mΩ
V _{TM}	Peak on-state voltage	I _{TM} =1200A, F=15kN	125			2.83	V
dv/dt	Critical rate of rise of off-state voltage	V _{DM} =0.67V _{DRM}	125			1000	V/μs
di/dt	Critical rate of rise of on-state current	V _{DM} = 67%V _{DRM} to800A, Gate pulse t _r ≤0.5μs I _{GM} =1.5A	125			100	A/μs
Q _{rr}	Recovery charge	I _{TM} =800A,tp=2000μs, di/dt=-20A/μs, V _R =50V	125		1400		μC
I _{GT}	Gate trigger current		25	35		300	mA
V _{GT}	Gate trigger voltage	V _A =12V, I _A =1A		0.8		2.5	V
I _H	Holding current			20		250	mA
V _{GD}	Non-trigger gate voltage	V _{DM} =0.67V _{DRM}	125	0.3			V
R _{th(j-c)}	Thermal resistance Junction to case	At 180° sine double side cooled Clamping force15kN				0.035	°C /W
R _{th(c-h)}	Thermal resistance case to heatsink					0.008	
F _m	Mounting force			10		20	kN
T _{slg}	Stored temperature			-40		140	°C
W _t	Weight				250		g
Outline	KT33cT						

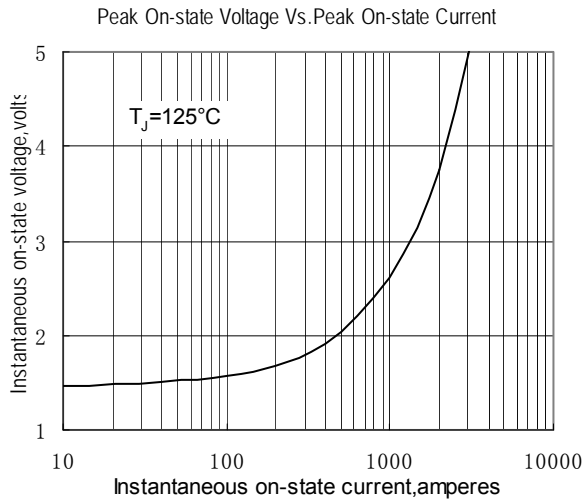


Fig.1

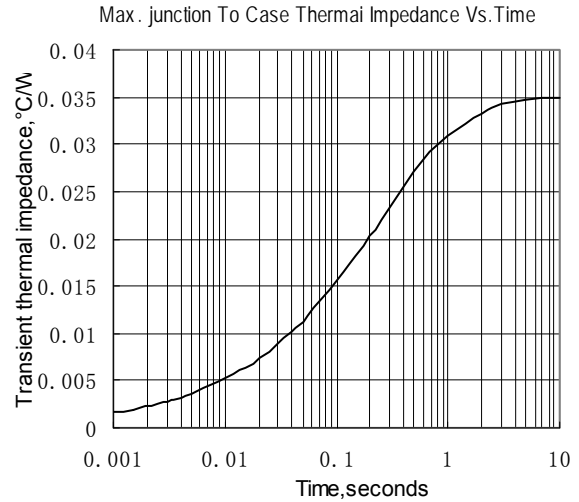


Fig.2

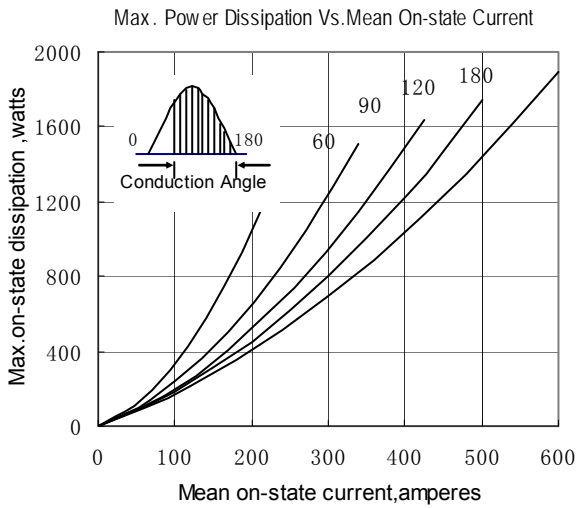


Fig.3

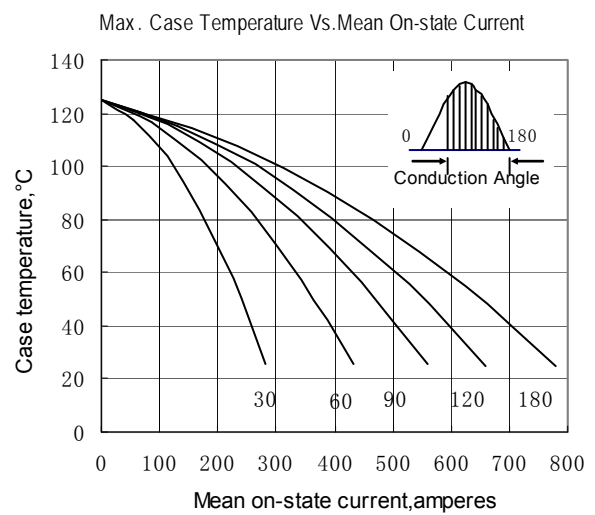


Fig.4

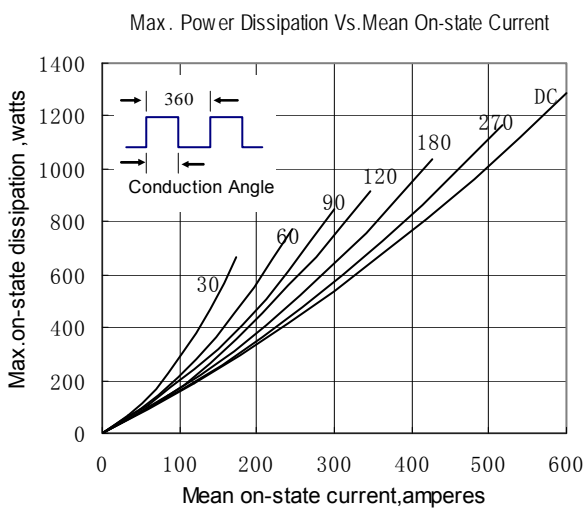


Fig.5

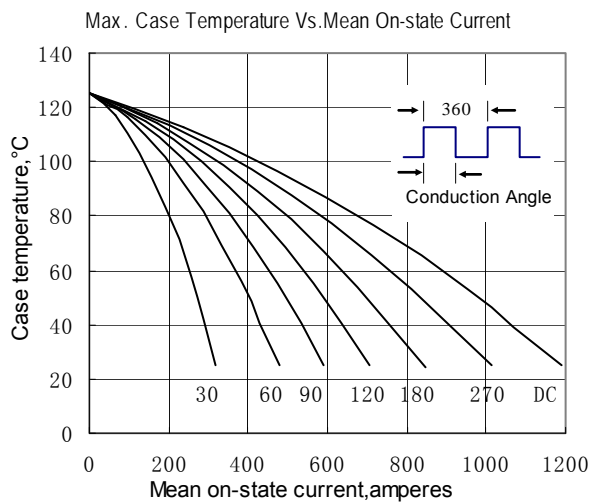


Fig.6

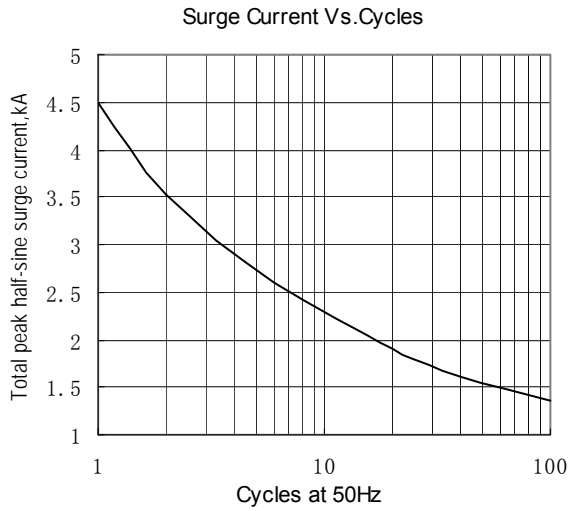


Fig.7

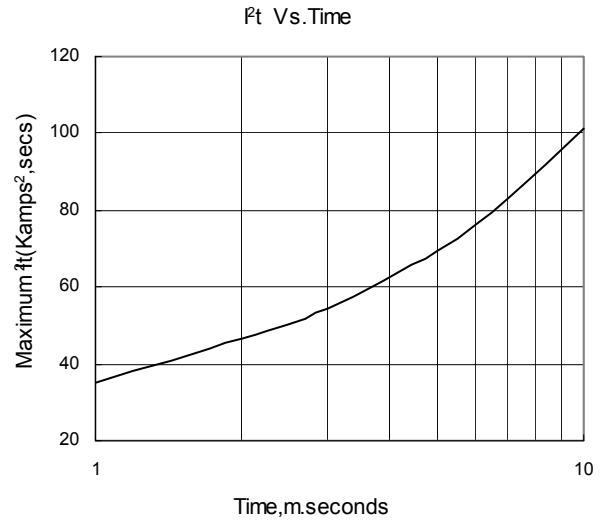


Fig.8

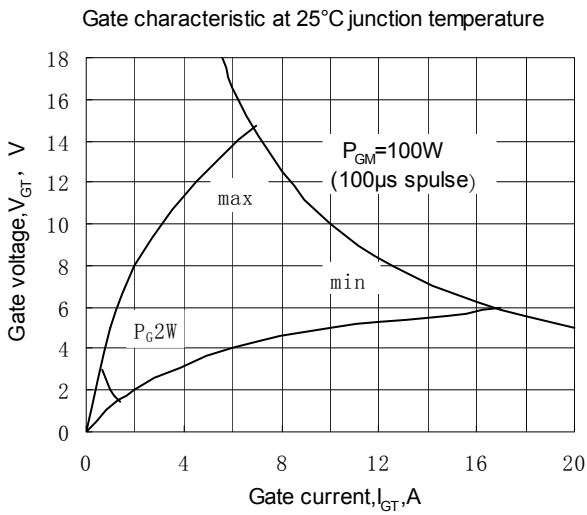


Fig.9

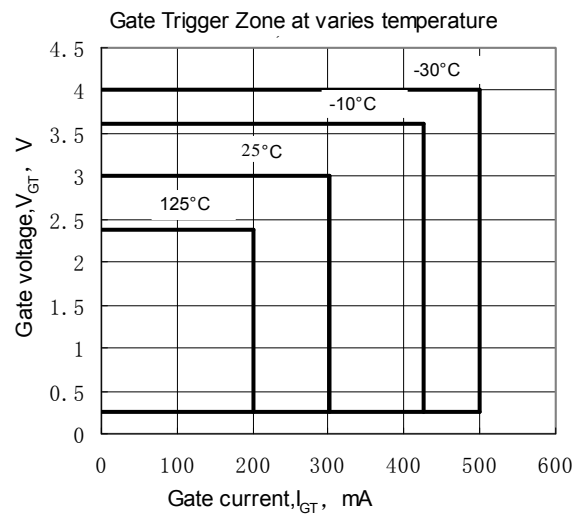


Fig.10

Outline:

