

700V N-Channel MOSFET

Description

The MSF6N70 is a N-channel enhancement-mode MOSFET, providing the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost effectiveness. The TO-220F package is universally preferred for all commercial-industrial applications

Features

- · Low On Resistance
- · Simple Drive Requirement
- Low Gate Charge
- · Fast Switching Characteristic
- · RoHS compliant / Halogen free package available

Application

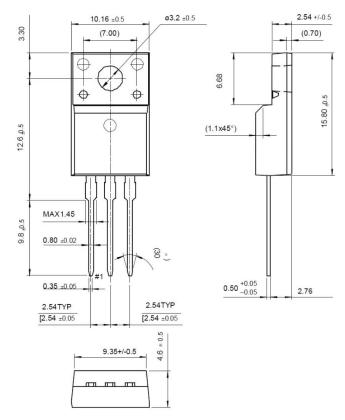
- Adapter
- · Switching Mode Power Supply

Packing & Order Information

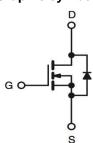
50/Tube; 1,000/Box



RoHS COMPLIANT



Graphic symbol



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)					
Symbol	Parameter	Value	Unit		
V_{DS}	Drain-Source Voltage	700	V		
V_{GS}	Gate-Source Voltage	±30	V		
	Drain Current -Continuous (TC=25°C)	6.0	Α		
I _D	Orain Current -Continuous (TC=25°C) Orain Current -Continuous (TC=100°C) Orain Current Pulsed Single Pulsed Avalanche Energy	3.5	Α		
I _{DM}	Drain Current Pulsed	22	Α		
E _{AS}	Single Pulsed Avalanche Energy	350	mJ		
E _{AR}	Repetitive Avalanche Energy	14.7	mJ		
dv/dt	Peak Diode Recovery dv/dt	5.5	V/ns		
Б	Power Dissipation (TC=25°C)	48	W		
P_{D}	Power Dissipation (TC=100°C)	0.38	W/°C		



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Absolute Maximum Ratings (Tc=25°C unless otherwise noted)				
Symbol	Parameter	Value Unit		
T_{J}, T_{STG}	Operating and Storage Temperature Range	-55 to +150	°C	

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. I_{AS} =5.5A, V_{DD} =50V, L=8mH, V_{G} =10V, starting TJ=+25°C.
- 3. $I_{SD} \le 5.5A$, $dI/dt \le 100A/\mu s$, $V_{DD} \le BVDSS$, starting TJ=+25°C.
- 4. Pulse Test : Pulse Width ≤ 300µs, Duty Cycle ≤ 2%
- 5. Essentially Independent of Operating Temperature

Static Characteristics					
Symbol	Test Conditions	Min	Тур.	Max.	Units
V_{GS}	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.0		4.0	V
*R _{DS(ON)}	$V_{GS} = 10 \text{ V}$, $I_D = 2.8 \text{ A}$		1.5	1.8	Ω
BV _{DSS}	$V_{GS} = 0 \text{ V}$, $I_D = 250 \mu A$	700			V
$\Delta BV_{DSS}/\Delta T_{J}$	I _D = 250μA, Referenced to 25°C		0.7		
I _{DSS}	$V_{DS} = 700 \text{ V}$, $V_{GS} = 0 \text{ V}$ $V_{DS} = 560 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_j = 125^{\circ}\text{C}$			1 10	uA
I _{GSSF}	V _{DS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	V _{DS} = -30 V, V _{DS} =0 V			-100	nA

Dynamic Characteristics					
Symbol	Test Conditions	Min	Тур.	Max.	Units
C_{ISS}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ $f = 1.0 \text{MHz}$		1100	1500	pF
Coss			110	150	pF
C _{RSS}			12	16	pF
t _{d(on)}			10	30	ns
t _r	$V_{DS} = 350 \text{ V}, I_{D} = 5.5 \text{ A},$ $R_{G} = 25 \Omega$		35	80	ns
$t_{d(off)}$			45	100	ns
tf			40	90	ns
Q_g	V _{DS} = 560 V,I _D = 5.5 A, V _{GS} = 10 V		29	37	nC
Q _{gs}			5		
Q_gd			11		



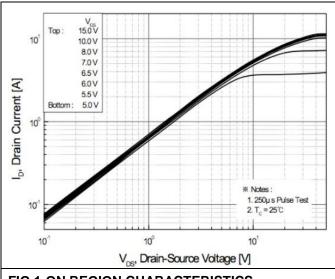
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Source-Drain Diode Characteristics					
Symbol	Test Conditions	Min	Тур.	Max.	Units
Is				5.5	Λ
I _{SM}				22	A
V _{SD}	IF = 5.5 A , V _{GS} = 0			1.5	V
t _{rr}	IF = 5.5 A , V _{GS} = 0 , dIF/dt = 100A/μs		390		ns
Q _{rr}			3.6		uC



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■Characteristics Curve



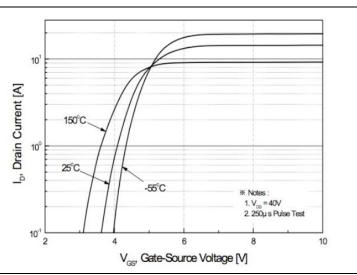


FIG.1-ON REGION CHARACTERISTICS

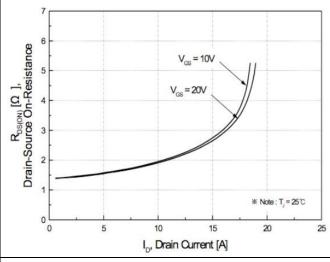


FIG.2-TRANSFER CHARACTERISTICS

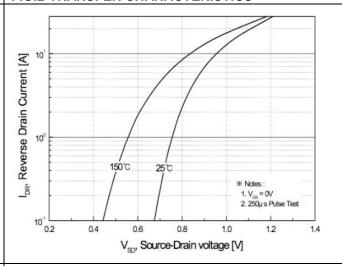


FIG.3-ON RESISTANCE VARIATION VS DRAIN CURRENT AND GATE VOLTAGE

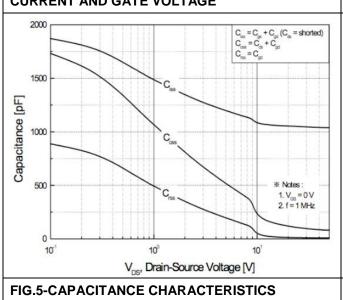
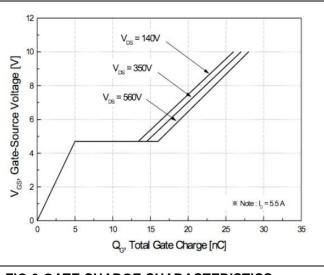


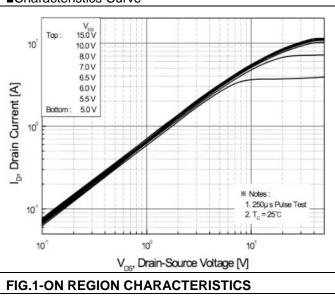
FIG.4-BODY DIODE FORWARD VOLTAGE VARIATION WITH SOURCE CURRENT AND TEMPERATURE

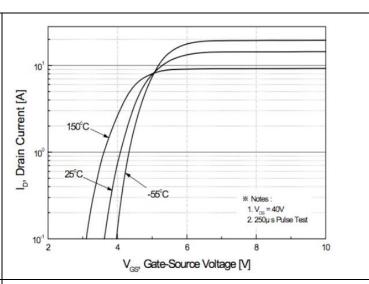




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■Characteristics Curve





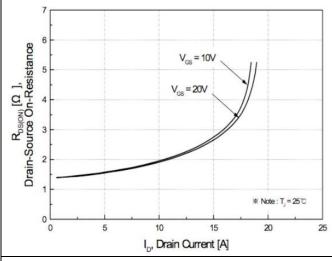


FIG.2-TRANSFER CHARACTERISTICS

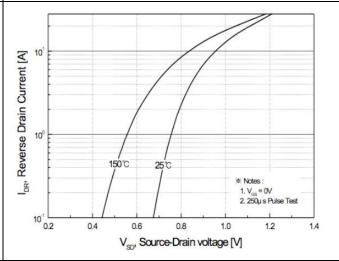


FIG.3-ON RESISTANCE VARIATION VS DRAIN CURRENT AND GATE VOLTAGE

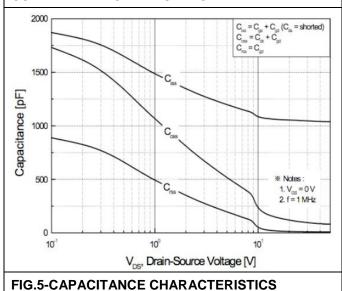


FIG.4-BODY DIODE FORWARD VOLTAGE VARIATION WITH SOURCE CURRENT AND TEMPERATURE

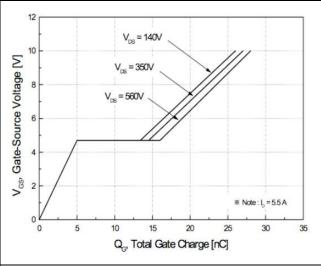
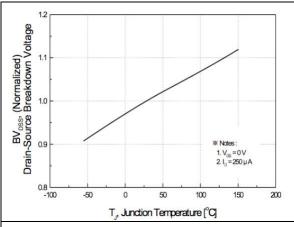


FIG.6-GATE CHARGE CHARACTERISTICS



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■Characteristics Test Circuit & Waveform



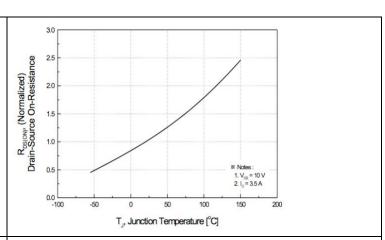


FIG.7-BREAKDOWN VOLTAGE VARIATION VS TEMPERATURE

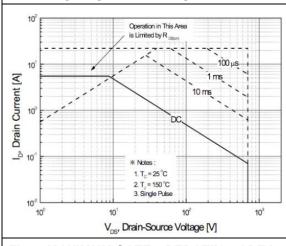


FIG.8-ON-RESISTANCE VARIATION VS TEMPERATURE

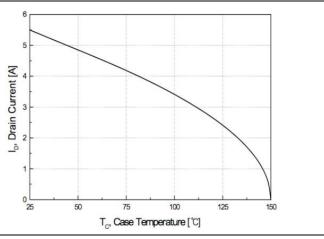
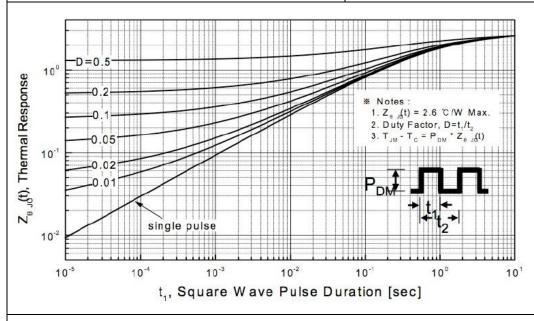


FIG.9-MAXIMUM SAFE OPERATING AREA

FIG.10-MAXIMUM DRAIN CURRENT VS CASE TEMPERATURE





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■Characteristics Test Circuit & Waveform

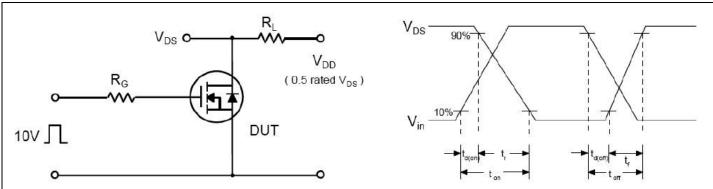


Fig 12. Resistive Switching Test Circuit & Waveforms

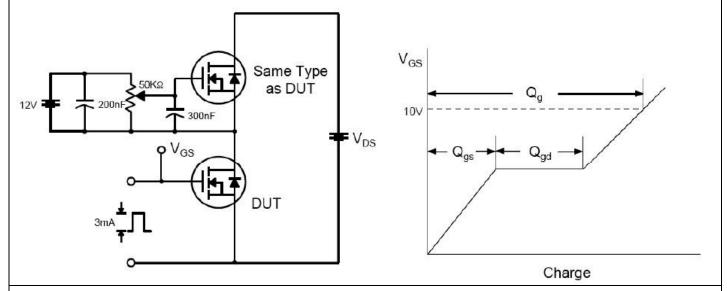


Fig 13. Gate Charge Test Circuit & Waveform

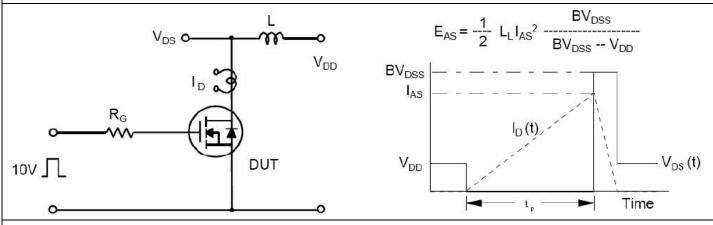


Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms



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