

### P-Channel 30-V (D-S) MOSFET

### **Description**

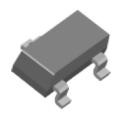
These miniature surface mount MOSFETs utilize a high cell density trench process to provide low rDS(on) and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

#### **Features**

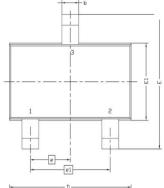
- Low rDS(on) provides higher efficiency and extends battery life
- · Miniature SOT-23 Surface Mount Package
- · Saves Board Space
- · RoHS compliant package

### **Packing & Order Information**

3,000/Reel

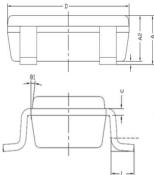


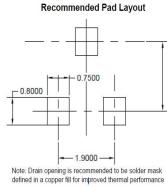




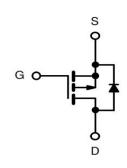
Symbol	IVIILLIIVIETERS			
Gyllibol	MIN	MAX		
Α	0.8	1.2		
A1	0	0.1		
A2	0.7	1.1		
b	0.3	0.5		
С	0.1	0.2		
D	2.7	3.1		
E	2.6	3		
E1	1.4	1.8		
е	0.95	BSC		
e1	1.9 BSC			
L	0.3	0.6		
θ1	7° NOM			

MILLIMETERS





**Graphic symbol** 



Absolute Maximum Ratings (Tc=25°C unless otherwise specified)					
Symbol	Parameter	Value	Unit		
$V_{DS}$	Drain-Source Voltage	30	V		
V <sub>GS</sub>	Gate-Source Voltage	±12	V		
I <sub>D</sub>	Continuous Drain Current <sup>a</sup> (T <sub>A</sub> = 25°C)	5.2	A		
	Continuous Drain Current <sub>a</sub> (T <sub>A</sub> =70°C)	4.1	A		
I <sub>DM</sub>	Pulsed Drain Current <sup>b</sup>	30	A		
Is	Continuous Source Current (Diode Conduction) <sup>a</sup>	1.6	А		
P <sub>D</sub>	Power Dissipation <sup>a</sup> (T <sub>A</sub> =25°C)	1.3	W		
	Power Dissipation <sup>a</sup> (T <sub>A</sub> =70°C)	0.8	W		
T <sub>J</sub> /T <sub>STG</sub>	Operating Junction and Storage Temperature	-55 to +150	°C		

<sup>•</sup> Drain current limited by maximum junction temperature



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Thermal Resistance Ratings					
Symbol	Parameter	Maximum	Units		
$R_{\theta JA}$	Maximum Junction-to-Ambient <sup>a</sup> (t <= 5 sec)	100	°C/W		
	Maximum Junction-to-Ambient <sup>a</sup> (Steady-State)	166	C/VV		

#### Notes:

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Static	Static					
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
$V_{GS(th)}$	Gate-Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	0.6			V
I <sub>GSS</sub>	Gate-Body Leakage	V <sub>DS</sub> = 0 V , V <sub>GS</sub> = 8 V			±100	nA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 24 V , V <sub>GS</sub> = 0 V V <sub>DS</sub> = 24 V , V <sub>GS</sub> = 0 V , T <sub>J</sub> = 55°C			1 25	uA
I <sub>D(on)</sub>	On-State Drain Current <sup>A</sup>	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	20			Α
I <sub>DS(on)</sub>	Drain-Source On-Resistance <sup>A</sup>	$V_{GS} = 4.5 \text{ V}, I_D = 5.2 \text{ A}$ $V_{GS} = 2.5 \text{ V}, I_D = 3.7 \text{ A}$			32 64	mΩ
g <sub>fs</sub>	Forward Tranconductance <sup>A</sup>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 5.2 A		40		S
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> = 2.3 V, V <sub>GS</sub> = 0 V		0.7		V

Dynamic <sup>b</sup>						
Symbol	Parameter	Test Conditions	Min	Тур.	Max.	Units
$Q_g$	Total Gate Charge	$V_{DS} = 15 \text{ V}, I_{D} = 4.5 \text{ A},$ $V_{GS} = 5.2 \text{ V}$		6.0		nC
$Q_{gs}$	Gate-Source Charge			1.0		nC
$Q_{gd}$	Gate-Drain Charge			1.5		nC
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 25 \text{ V}, R_L = 25 \Omega,$ $V_{GEN} = 10 \text{ V}, I_D = 1 \text{ A}$		7.8		ns
t <sub>r</sub>	Rise Time			5.2		ns
$t_{d(off)}$	Turn-Off Delay Time			31.2		ns
tf	Fall Time			8		ns

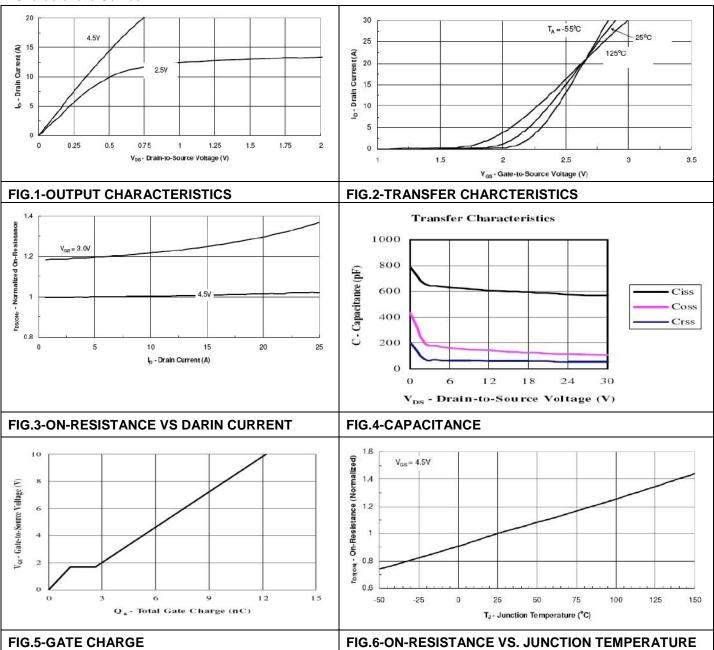
### Notes:

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.



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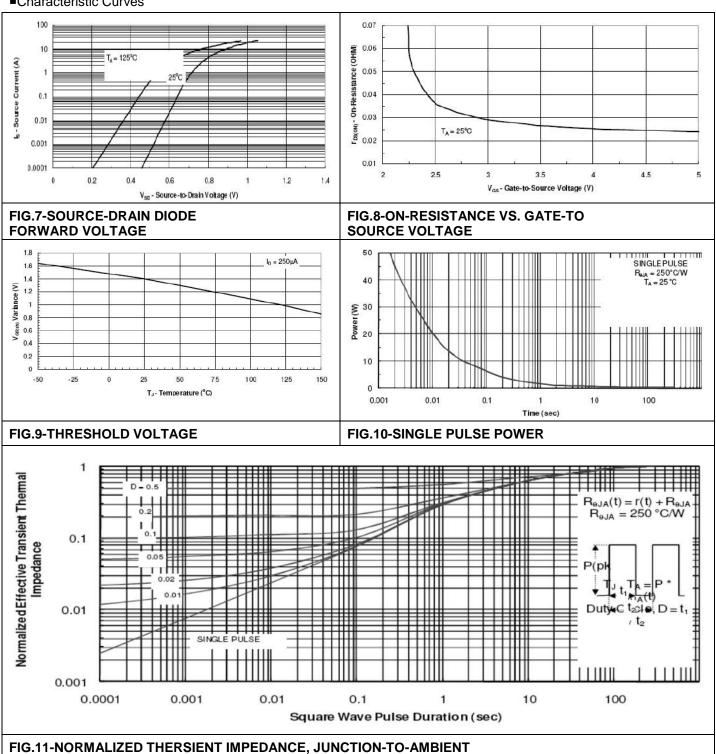
#### ■Characteristic Curves





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#### ■Characteristic Curves





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