

PS06N30SA 30V Single Channel NMOSEFT

Revision : 1.0

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ProsPower Microelectronics Co., Ltd

1. General Description

The PS06N30SA uses advanced trench technology and design to provide excellent Rds(on) with low gate charge. This device is suitable for use as a load switch or in PWM applications. Standard Product PS06N30SA is Pb-free (meets ROHS & Sony 259 specifications). It is offered in the very popular SOT23 package

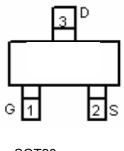
2. Applications

- PWM applications
- · Load switch
- Power management
- DC-DC convert

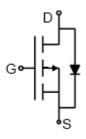
3. Features

- Vds=30V, Id=5.8A
- Rds(on)=28mohm (Vgs=4.5V)
- Rds(on)=37mohm (Vgs=2.5V)
- High Power and current handing capability
- Low capacitance minimizes driver loss
- Optimized gate charge minimizes switching loss

Pin Configuration



SOT23



Schematic

Pin Descriptions

Pin Name	Symbol	Function			
Gate(1)	G	Device Gate terminal			
Drain(3)	D	Device drain terminal			
Source(2)	S	Device source terminal			



Absolute Maximum Ratings

Stress greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These stress ratings only, and functional operation of the device at these or any conditions beyond those indicated under recommended Operating Conditions is not implied. Exposure to "Absolute Maximum Rating" for extended periods may affect device reliability. Use of standard ESD handling precautions is required..

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _G S	±12	V
Continuous Drain Current	I _D	5.8	Α
Pulsed Drain Current (Note 1)	I _{DM}	30	Α
Junction Temperature	TJ	150	°C
Power Dissipation T _C =25°C	P _D	1.4	W
Storage Temperature Range	T _{STG}	-65 to 150	°C

Thermal Characteristics

Parameter	Symbol	Тур.	Units
Maximum Junction-to-Ambient (Note2)	$R_{\theta JA}$	125	°C/W

Electrical Specifications

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Units
STATIC PARAMETERS							
Drain-Source Breakdown Voltage	BVDss	I _D =250uA, V _{GS} =0V		30	35		V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =24V, V _{GS} =0V	T _J =25°C			1	uA
Gate-Body leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V				±0.1	μA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA		0.7	0.88	1.4	V
		V _{GS} =4.5V, I _D =5A			28	33	
Static Drain-Source On-Resistance	RDS(ON)	V _{GS} =2.5V, I _D =4A			37	50	mΩ
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =5A		10	15		S
Maximum Body-Diode Continuous Current	Is					2.5	Α
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V			0.72	1.0	V
DYNAMIC PARAMETERS							
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =15V,			823	1030	pF
Output Capacitance	Coss	f=1MHz			99		pF
Reverse Transfer Capacitance	Crss				77		pF



SWITCHING PARAMETERS					
Total Gate Charge	Qg	\/ -45\/ \/ -45\/	11	14.3	nC
Gate Source Charge	Qgs	V _{GS} =4.5V, V _{DD} =15V,	1.6	2.08	nC
Gate Drain Charge	Qgd	I _D =5.8A (Note 3)	2.8	3.64	nC
Turn-On Delay Time	t _{D(on)}	457	7	14	ns
Turn-On Rise Time	t _r	V _{DD} =15V,	15	30	ns
Turn-Off Delay Time	t _{D(off)}	V_{GEN} =10V, R_L =2.7 Ω	38	76	ns
Turn-Off Fall Time	t _f	$R_G=3\Omega(Note 3)$	3	6	ns

Notes

- 1. Pulse width limited by max. junction temperature
- 2. Surface mounted on 1 in² copper pad of FR4 board, t <= 5sec; 180°C/W when mounted on min. copper pad.
- 3. Pulse Width <= 300us, Duty Cycle <=2%



Typical Performance Characteristics

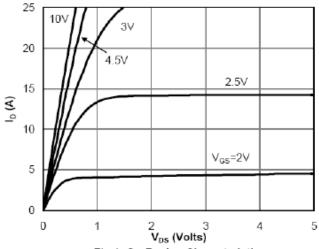


Fig 1: On-Region Characteristics

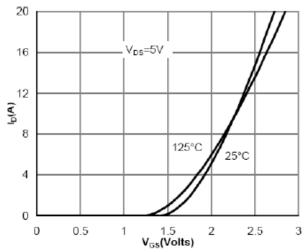


Figure 2: Transfer Characteristics

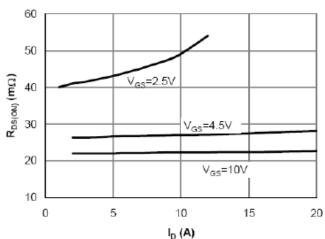


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

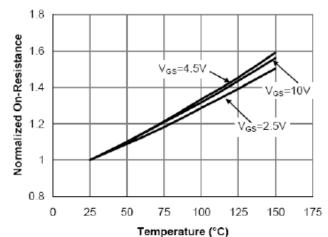


Figure 4: On-Resistance vs. Junction Temperature

Typical Performance Characteristics (contd.)

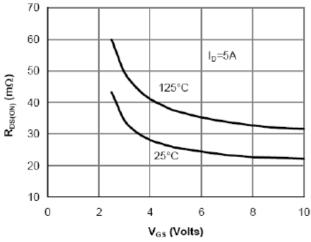


Figure 5: On-Resistance vs. Gate-Source Voltage

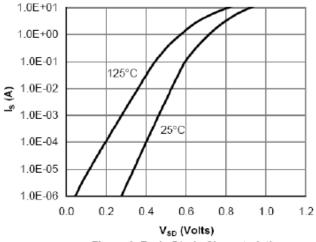


Figure 6: Body-Diode Characteristics

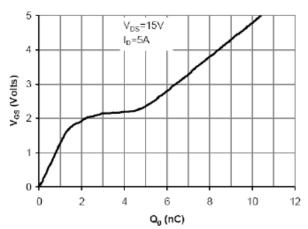


Figure 7: Gate-Charge Characteristics

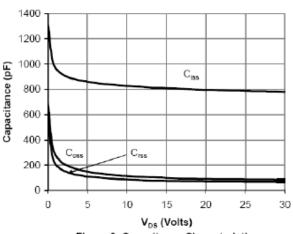


Figure 8: Capacitance Characteristics

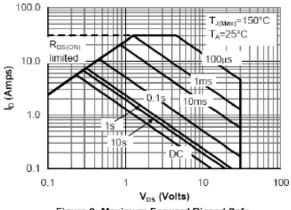


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

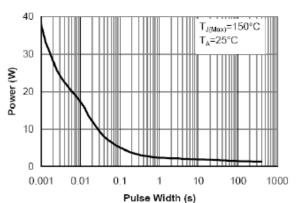


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

Typical Performance Characteristics (contd.)

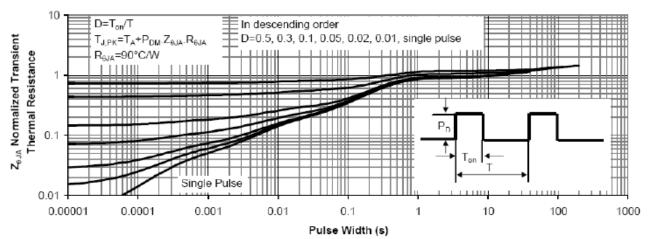
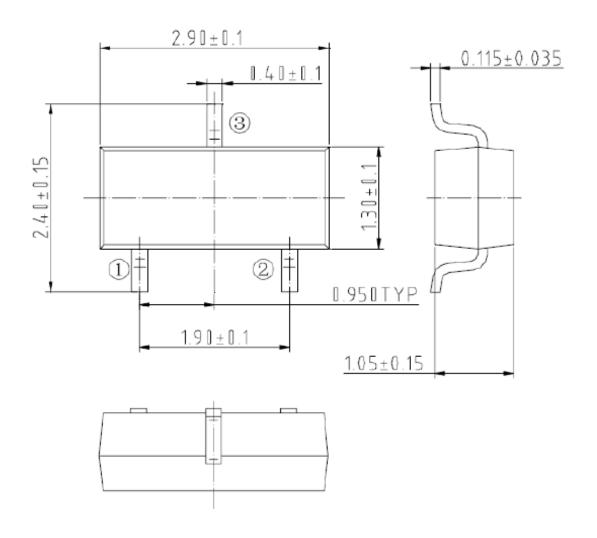


Figure 11: Normalized Maximum Transient Thermal Impedance



Package Dimensions SOT23





Ordering Information

Device	Operating T _j	PKG Type	Wrap	Order Number
PS06N30SA	-65C° ≤150C°	SOT23	T&R	PS06N30SA-S2-TL

Note: Lead Free and RoHS compliant.

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