

GSM4599

40V N & P Pair Enhancement Mode MOSFET

Product Description

GSM4599, N & P Pair enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge.

These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

N-Channel

- 40V/8A, $R_{DS(ON)}=22m\Omega@V_{GS}=10V$
- 40V/6A, $R_{DS(ON)}=36m\Omega@V_{GS}=4.5V$

P-Channel

- -40V/-7.2A, $R_{DS(ON)}=37m\Omega@V_{GS}=-10V$
- -40V/-6.2A, $R_{DS(ON)}=54m\Omega@V_{GS}=-4.5V$

Applications

- Low Current DC/DC Conversion
- Load Switch
- CCFL Inverter
- Power Management in Notebook Computer

Packages & Pin Assignments

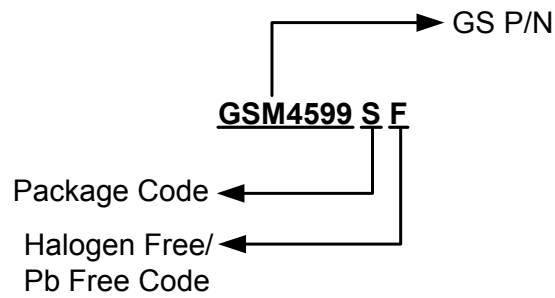
GSM4599SF(SOP-8P)

Pin	Description	Pin	Description
1	Source1	5	Drain2
2	Gate1	6	Drain2
3	Source2	7	Drain1
4	Gate2	8	Drain1

N-Channel MOSFET

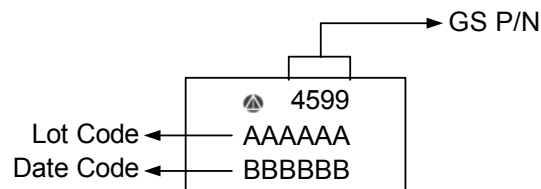
P-Channel MOSFET

Ordering Information



Part Number	Package	Quantity Reel
GSM4599SF	SOP-8P	3000 PCS

Marking Information



Absolute Maximum Ratings (N-Channel)

($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Typical	Unit
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate –Source Voltage	± 20	V
I_D	Continuous Drain Current($T_J=150^\circ\text{C}$)	$T_A=25^\circ\text{C}$	8.0
		$T_A=70^\circ\text{C}$	6.0
I_{DM}	Pulsed Drain Current	25	A
I_S	Continuous Source Current(Diode Conduction)	1.5	A
P_D	Power Dissipation	$T_A=25^\circ\text{C}$	2.8
		$T_A=70^\circ\text{C}$	1.8
T_J	Operating Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55/150	$^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62.5	$^\circ\text{C}/\text{W}$

Electrical Characteristics (N-Channel)

(T_A=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	40			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	1.5		3.0	
I _{GSS}	Gate Leakage Current	V _{DS} =0V, V _{GS} =±20V			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V			1	uA
		V _{DS} =40V, V _{GS} =0V, T _J =85°C			10	
I _{D(on)}	On-State Drain Current	V _{DS} ≥5V, V _{GS} =10V	20			A
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =10V, I _D =8A		16	22	mΩ
		V _{GS} =4.5V, I _D =6A		28	36	
g _{FS}	Forward Transconductance	V _{DS} =15V, I _D =5.0A		25		S
V _{SD}	Diode Forward Voltage	I _S =2A, V _{GS} =0V		0.85	1.2	V
Dynamic						
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz		850		pF
C _{oss}	Output Capacitance			110		
C _{rss}	Reverse Transfer Capacitance			75		
Q _g	Total Gate Charge	V _{DS} =20V, V _{GS} =4.5V, I _D =5A		10	14	nC
Q _{gs}	Gate-Source Charge			2.8		
Q _{gd}	Gate-Drain Charge			3.2		
t _{d(on)}	Turn-On Time	V _{DD} =20V, R _L =4Ω, I _D =5.0A, V _{GEN} =10V, R _G =1Ω		6	12	ns
T _r				10	20	
t _{d(off)}	Turn-Off Time			20	36	
T _f				6	12	

Absolute Maximum Ratings (P-Channel)

(T_A=25°C unless otherwise noted)

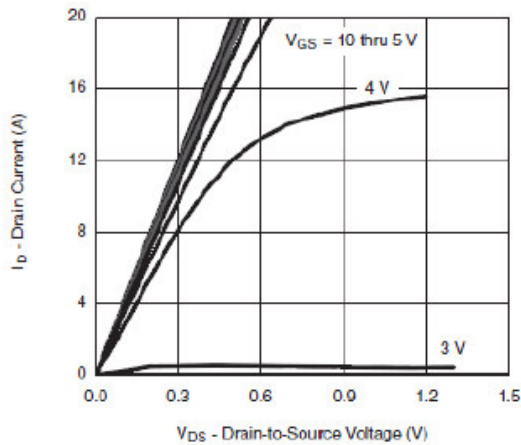
Symbol	Parameter	Typical	Unit
V _{DSS}	Drain-Source Voltage	-40	V
V _{GSS}	Gate –Source Voltage	±20	V
I _D	Continuous Drain Current(T _J =150°C)	T _A =25°C	-7.2
		T _A =70°C	-6.2
I _{DM}	Pulsed Drain Current	-25	A
I _S	Continuous Source Current(Diode Conduction)	-1.7	A
P _D	Power Dissipation	T _A =25°C	2.8
		T _A =70°C	1.8
T _J	Operating Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-55/150	°C
R _{θJA}	Thermal Resistance-Junction to Ambient	62.5	°C/ W

Electrical Characteristics (P-Channel)

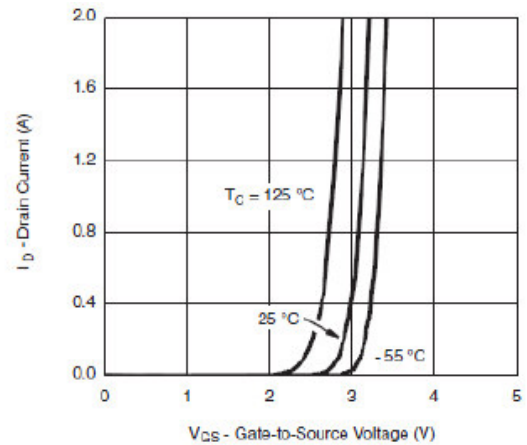
(T_A=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-40			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	-1.0		-3.0	
I _{GSS}	Gate Leakage Current	V _{DS} =0V, V _{GS} =±20V			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-40V, V _{GS} =0V			-1	uA
		V _{DS} =-40V, V _{GS} =0V, T _J =85°C			-20	
I _{D(on)}	On-State Drain Current	V _{DS} ≤ -5V, V _{GS} =-10V	-20			A
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =-10V, I _D =-7.2A		30	37	mΩ
		V _{GS} =-4.5V, I _D =-6.2A		38	54	
g _{FS}	Forward Transconductance	V _{DS} =-15V, I _D =-5A		20		S
V _{SD}	Diode Forward Voltage	I _S =-2A, V _{GS} =0V		-0.8	-1.2	V
Dynamic						
C _{iss}	Input Capacitance	V _{DS} =-20V, V _{GS} =0V, f=1MHz		1100		pF
C _{oss}	Output Capacitance			145		
C _{rss}	Reverse Transfer Capacitance			115		
Q _g	Total Gate Charge	V _{DS} =-20V, V _{GS} =-4.5V, I _D =-5.0A		13	20	nC
Q _{gs}	Gate-Source Charge			4.5		
Q _{gd}	Gate-Drain Charge			6.5		
t _{d(on)}	Turn-On Time	V _{DD} =-20V, R _L =4Ω, I _D =-5.0A, V _{GEN} =-4.5V, R _G =1Ω		40	80	ns
T _r				55	100	
t _{d(off)}	Turn-Off Time			30	60	
T _f				12	20	

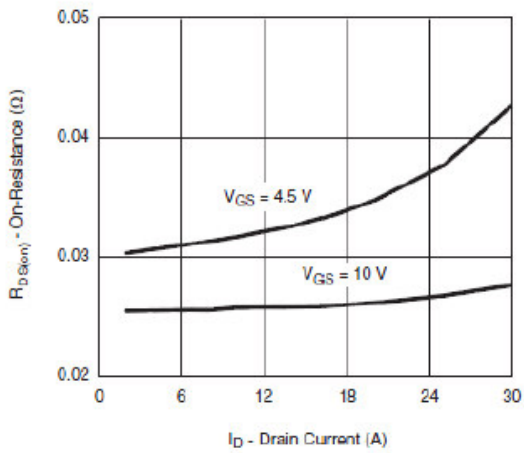
Typical Performance Characteristics (N-Channel)



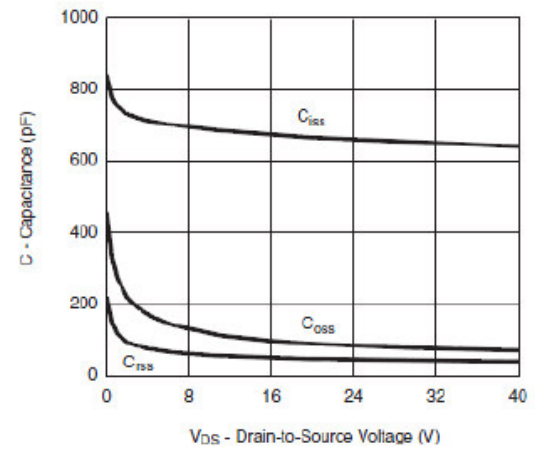
Output Characteristics



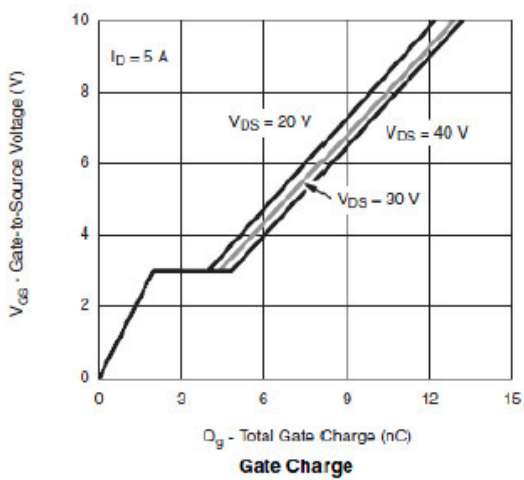
Transfer Characteristics



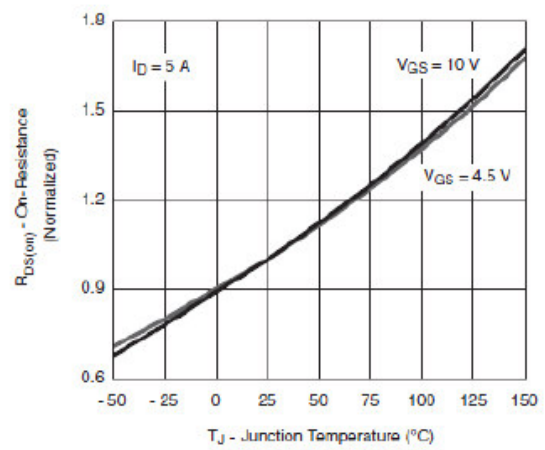
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

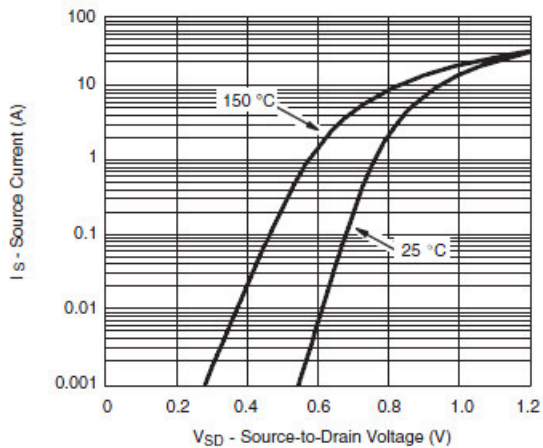


Gate Charge

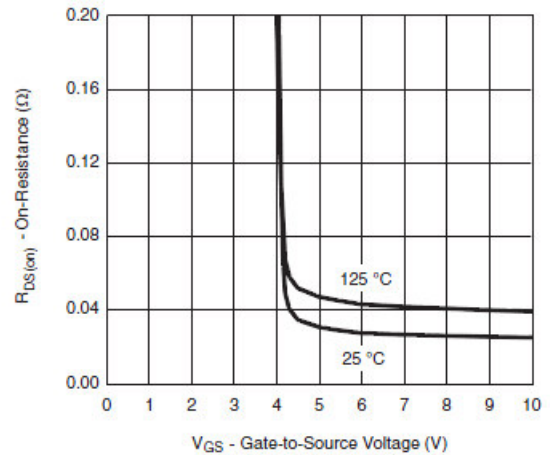


On-Resistance vs. Junction Temperature

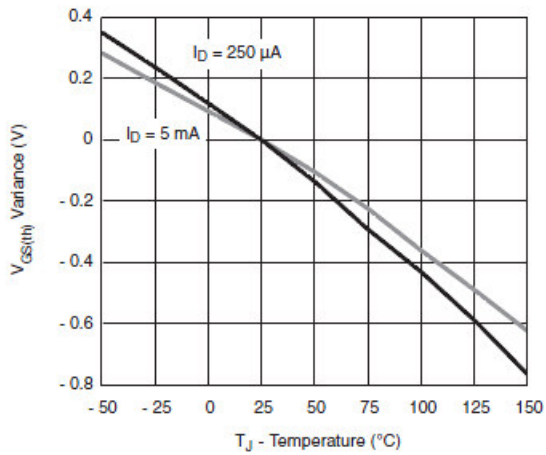
Typical Performance Characteristics (N-Channel)



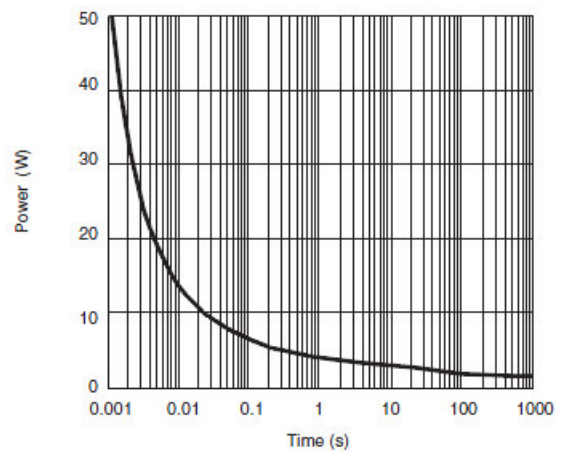
Source-Drain Diode Forward Voltage



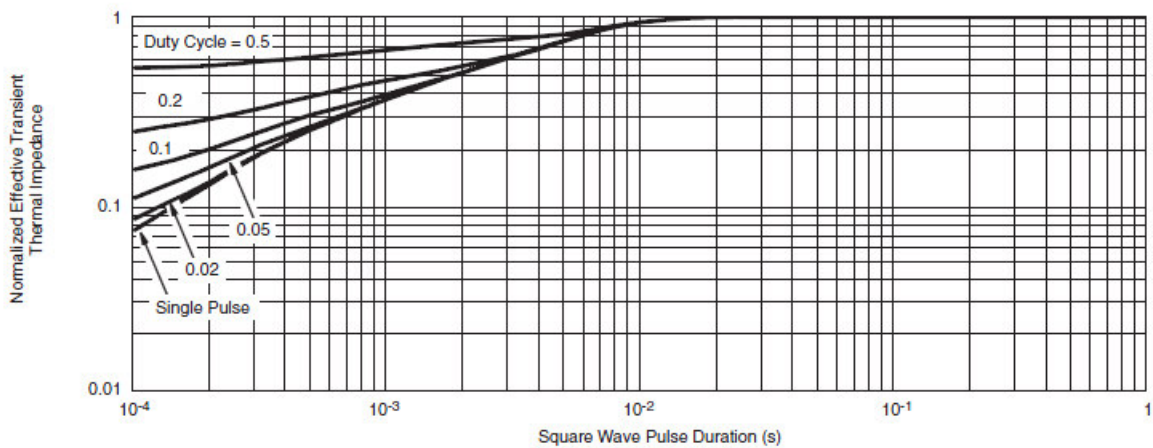
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

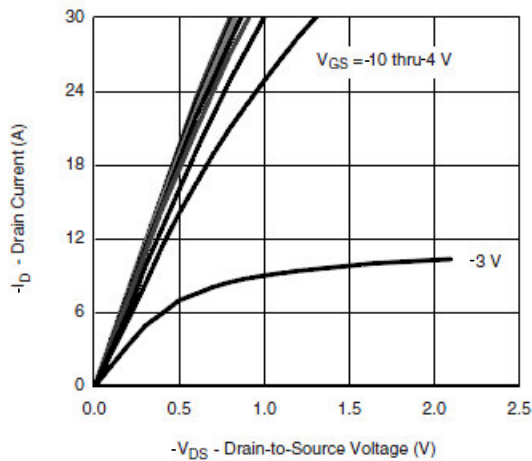


Single Pulse Power, Junction-to-Ambient

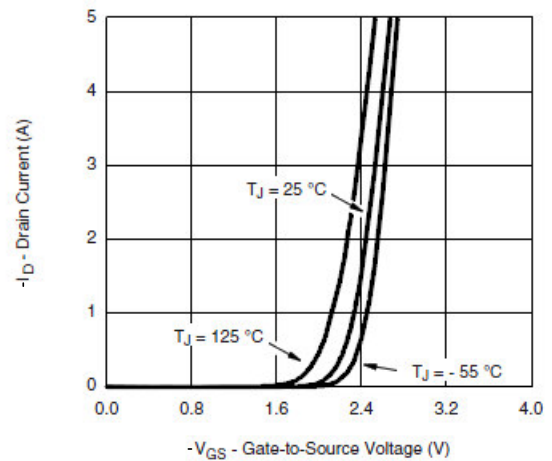


Normalized Thermal Transient Impedance, Junction-to-Case

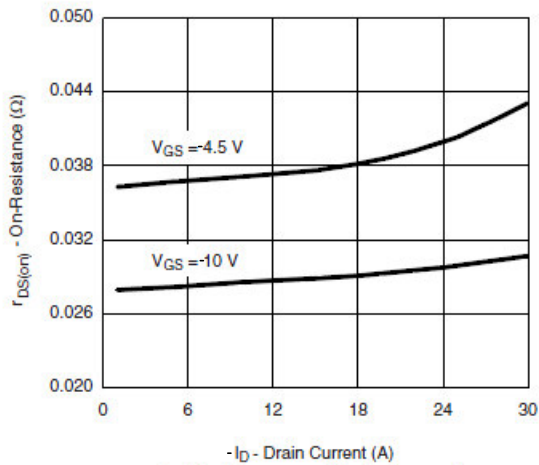
Typical Performance Characteristics (P-Channel)



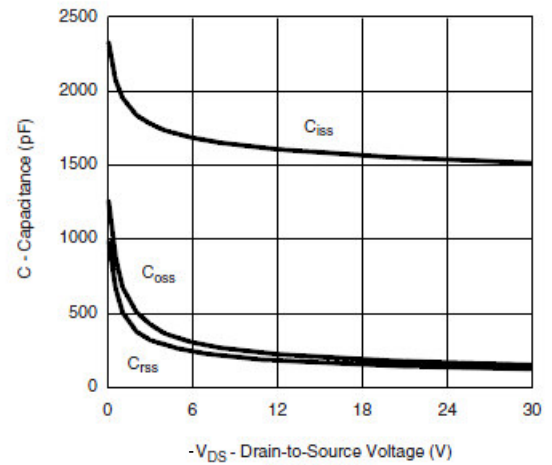
Output Characteristics



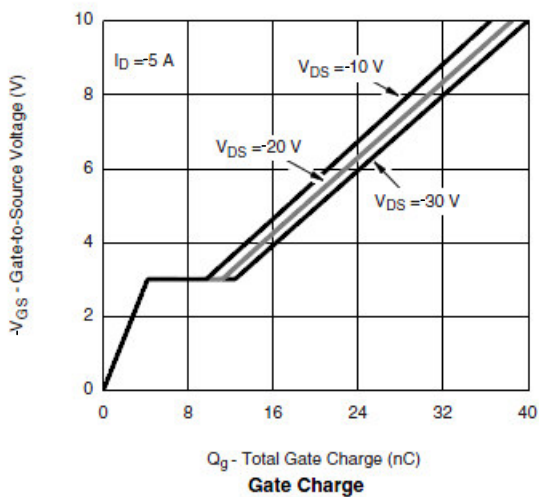
Transfer Characteristics



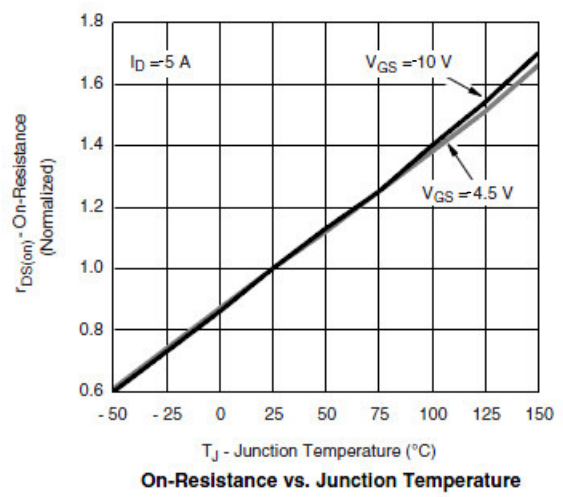
On-Resistance vs. Drain Current



Capacitance

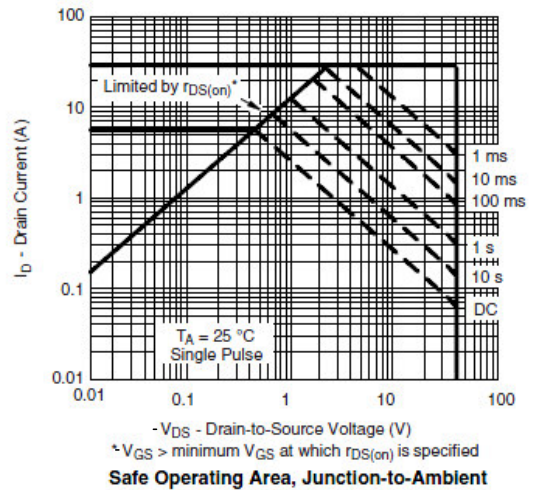
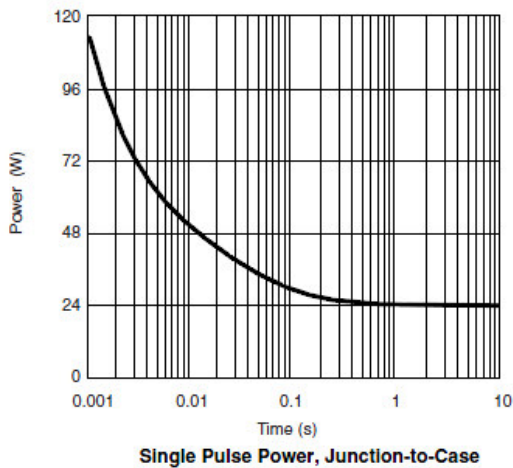
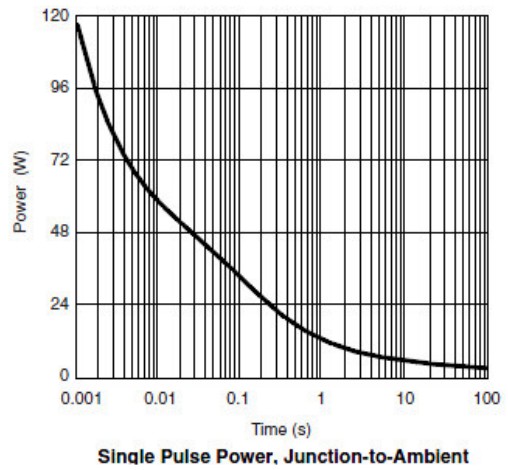
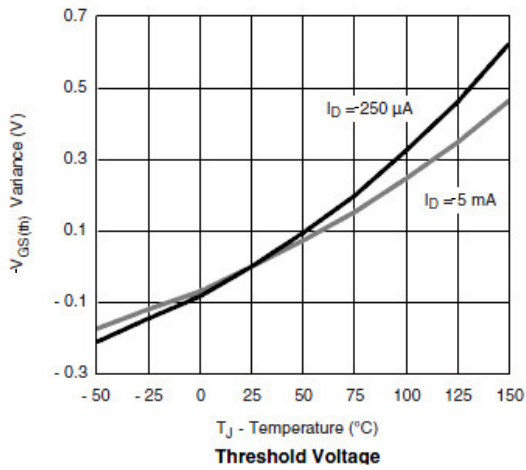
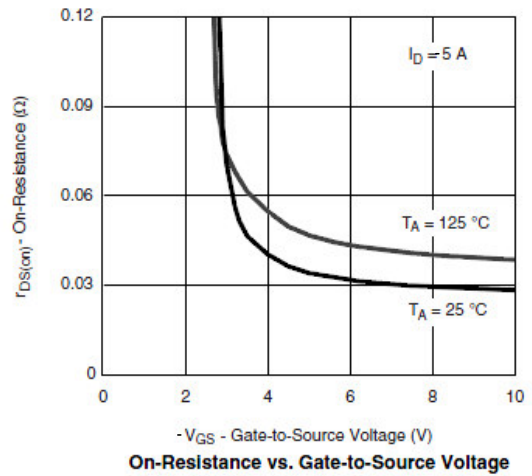
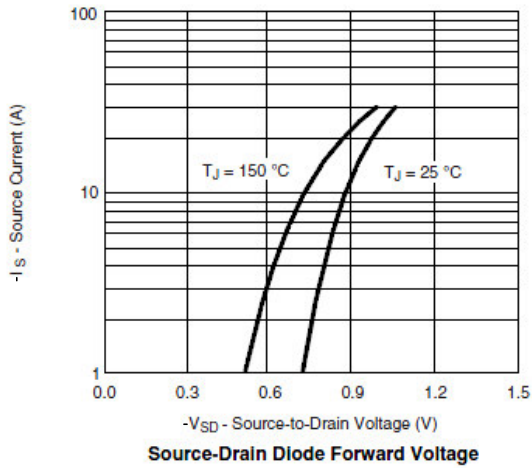


Gate Charge



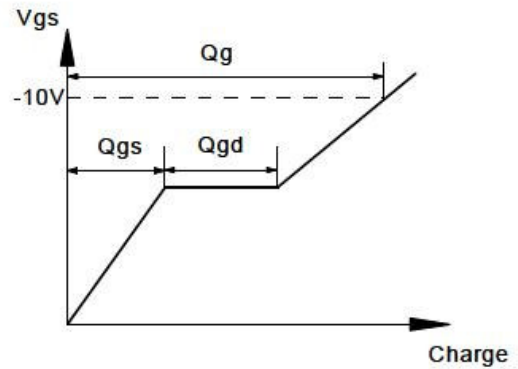
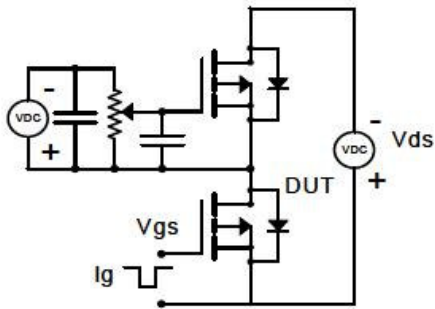
On-Resistance vs. Junction Temperature

Typical Performance Characteristics (P-Channel)

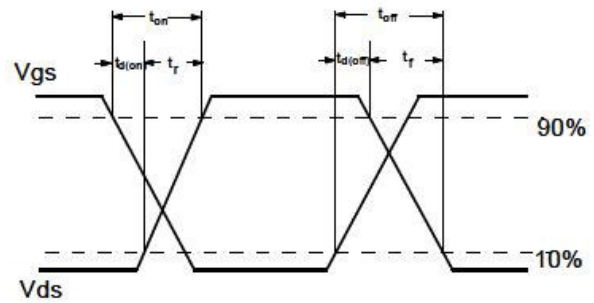
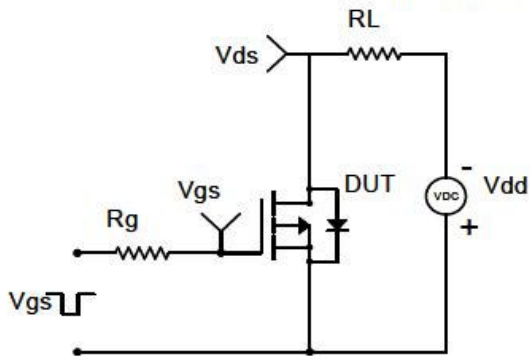


Typical Performance Characteristics

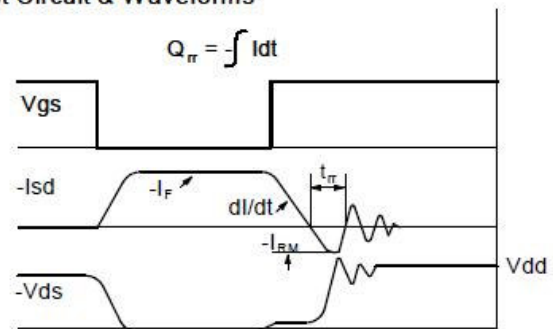
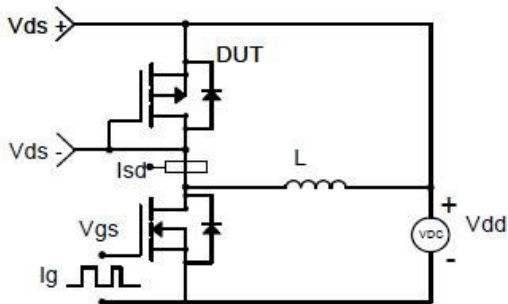
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

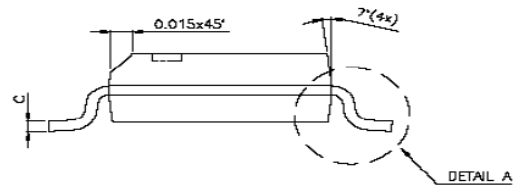
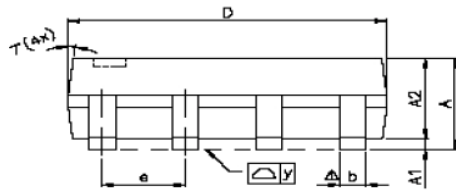
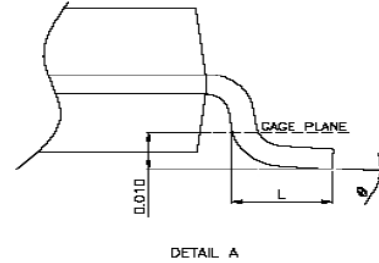
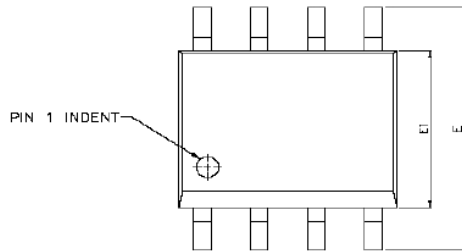


Diode Recovery Test Circuit & Waveforms



Package Dimension

SOP-8P PLASTIC PACKAGE











Dimensions						
SYMBOL	Millimeters			Inches		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10	-	0.25	0.004	-	0.010
A2	-	1.45	-	-	0.057	-
b	0.33	0.41	0.51	0.013	0.016	0.020
C	0.19	0.20	0.25	0.0075	0.008	0.0098
D	4.80	4.85	4.95	0.189	0.191	0.195
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
e	-	1.27	-	-	0.050	-
L	0.38	0.71	1.27	0.015	0.028	0.050
Δy	-	-	0.076	-	-	0.003
θ	0°	-	8°	0°	-	8°



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