

GSMBSS84

50V P-Channel Enhancement Mode MOSFET

Product Description

GSMBSS84, P-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge.

These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

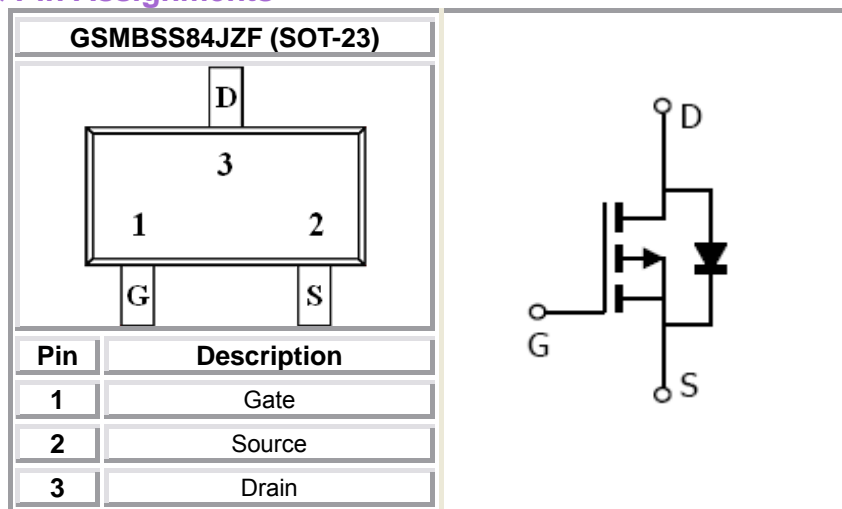
Features

- -50V/-0.1A, $R_{DS(ON)}=10\Omega@V_{GS}=-5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- SOT-23 package design

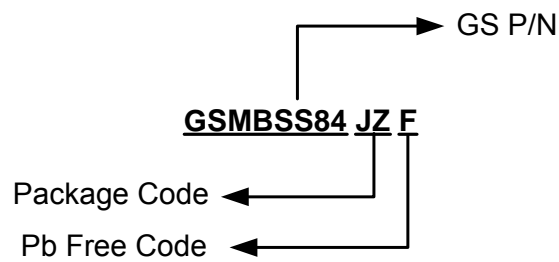
Applications

- DC to DC Converter
- Cellular & PCMCIA Card
- Cordless Telephone
- Power Management in Portable and Battery etc.

Packages & Pin Assignments

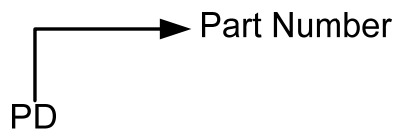


Ordering Information



Part Number	Package	Quantity
GSMBSS84JZF	SOT-23	3000 PCS

Marking Information



Absolute Maximum Ratings

($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Typical	Unit
V_{DSS}	Drain-Source Voltage	-50	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current ($T_A=25^\circ\text{C}$)	-130	mA
I_{DM}	Pulsed Drain Current ($t_p \leq 10\mu\text{s}$)	-520	mA
I_S	Continuous Current	-0.13	A
P_D	Power Dissipation ($T_A=25^\circ\text{C}$)	225	mW
T_J	Operating Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$R_{\theta JA}$	Maximax Junction to Ambient	556	$^\circ\text{C}/\text{W}$

Note 1: Pulse Test: $PW \leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

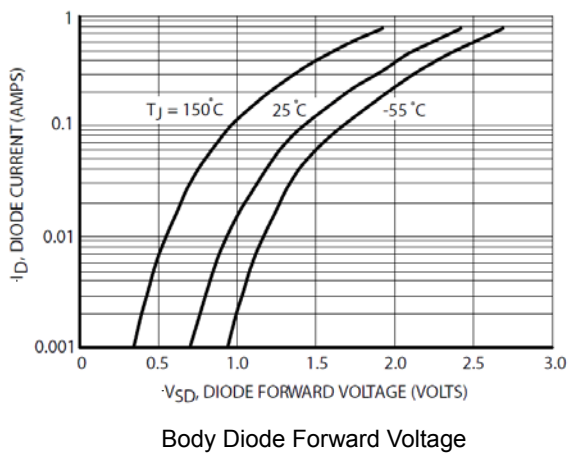
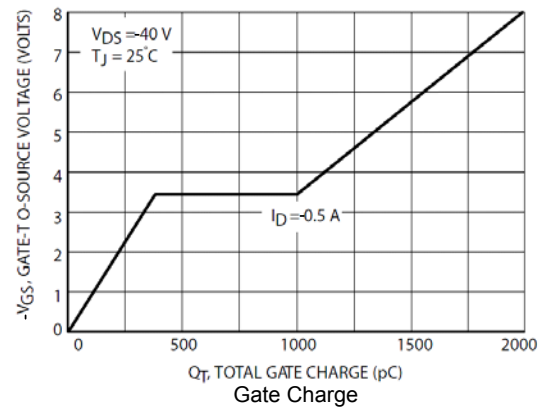
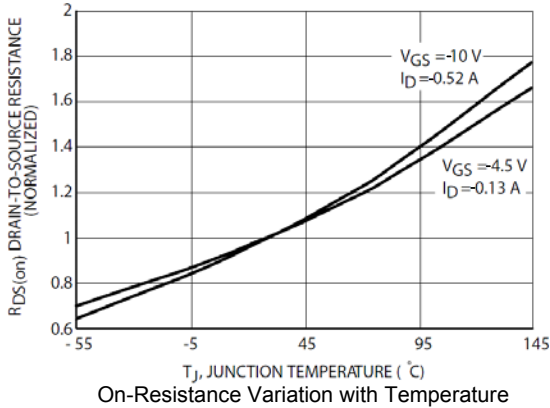
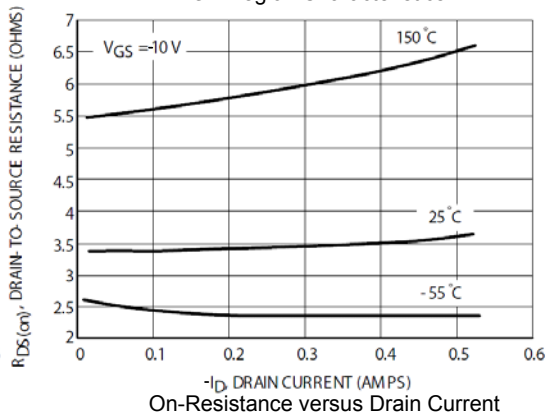
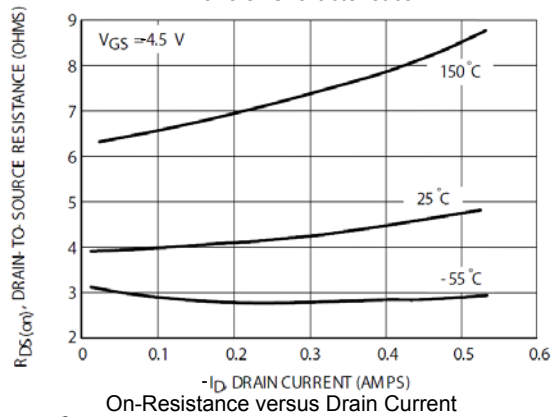
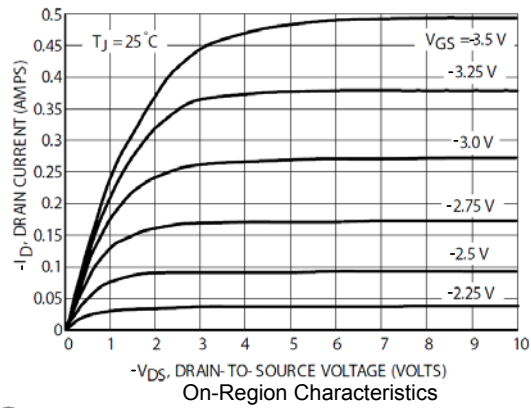
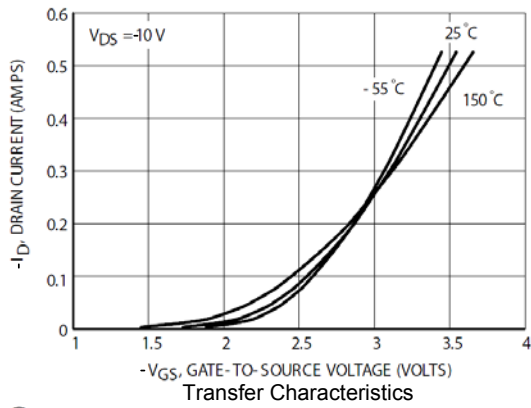
Note 2: Switching Time is Essentially Independent of Operating Temperature.

Electrical Characteristics

(T_A=25°C unless otherwise noted)

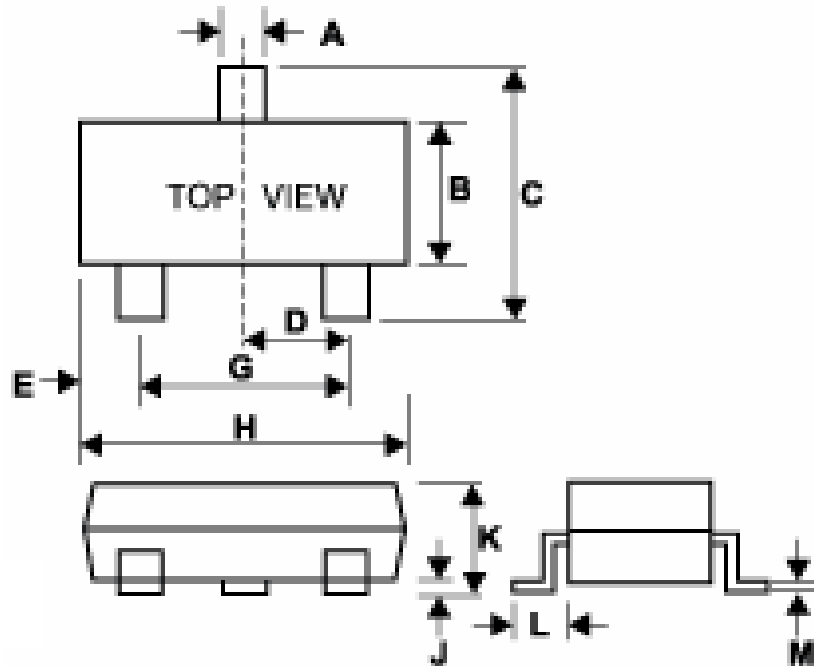
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-50			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-1.0mA	-0.8		-2.0	
I _{GSS}	Gate-Source Leakage Current	V _{DS} =0V, V _{GS} =±20V			±60	uA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -25V, V _{GS} =0V			-0.1	uA
		V _{DS} = -50V, V _{GS} =0V			-15	
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =-5V, I _D =-100mA		5.0	10	Ω
g _{fs}	Forward Transconductance	V _{DS} =-25V, I _D =-100mA, f=1.0KHz	50			mS
V _{SD}	Forward Voltage			-2.5		V
Dynamic						
C _{iss}	Input Capacitance	V _{DS} =-5V, V _{GS} =0V, f=1MHz		30		pF
C _{oss}	Output Capacitance			10		
C _{rss}	Reverse Transfer Capacitance			5.0		
Q _G	Gate Charge			6		nC
t _{d(on)}	Turn-On Time	V _{DD} =-15V, R _L =50Ω, I _D =-2.5A		25		ns
t _r				1.0		
t _{d(off)}	Turn-Off Time			16		
t _f				8.0		

Typical Performance Characteristics



Package Dimension

SOT-23








Dimensions				
Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.35	0.51	0.013	0.020
B	1.19	1.80	0.046	0.070
C	2.10	3.00	0.082	0.118
D	0.85	1.05	0.033	0.041
E	0.46	1.00	0.018	0.039
G	1.70	2.10	0.066	0.082
H	2.70	3.10	0.106	0.122
J	0.01	0.13	0.0003	0.005
K	0.89	1.60	0.035	0.062
L	0.30	0.61	0.011	0.024
M	0.076	0.25	0.002	0.009


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