

GSM3406S

30V N-Channel Enhancement Mode MOSFET

Product Description

GSM3406S, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge.

These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

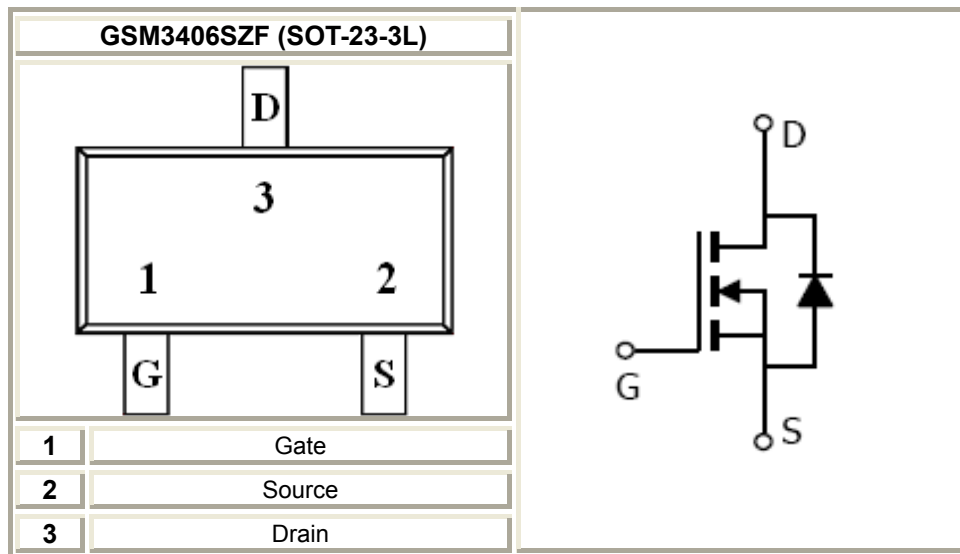
Features

- 30V/4.0A, $R_{DS(ON)}=40m\Omega@V_{GS}=10V$
- 30V/3.5A, $R_{DS(ON)}=50m\Omega@V_{GS}=4.5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- SOT-23-3L package design

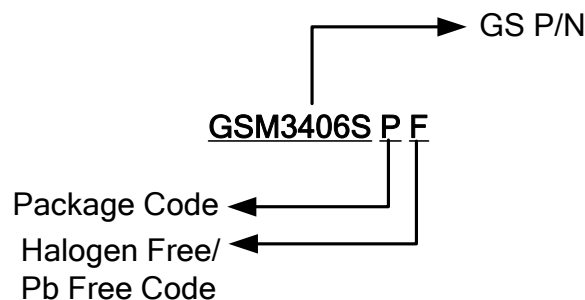
Applications

- Power Management in Note book
- LED Display
- DC-DC System
- LCD Panel

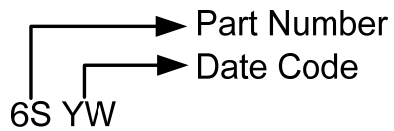
Packages & Pin Assignments



Ordering Information



Marking Information



Part Number	Package	Part Marking
GSM3406SZF	SOT-23-3L	6SYW

Absolute Maximum Ratings

($T_A=25^\circ\text{C}$ unless otherwise noted)

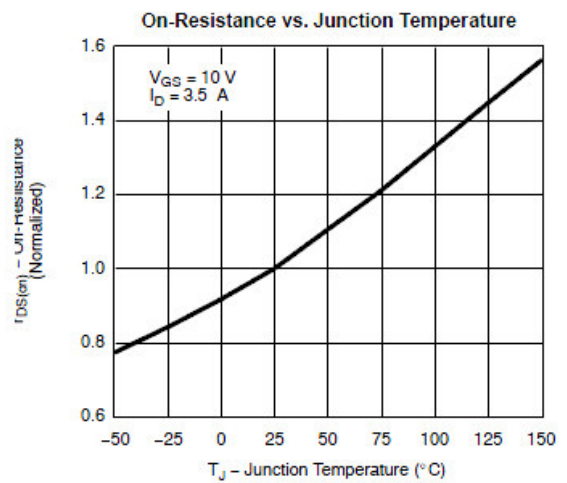
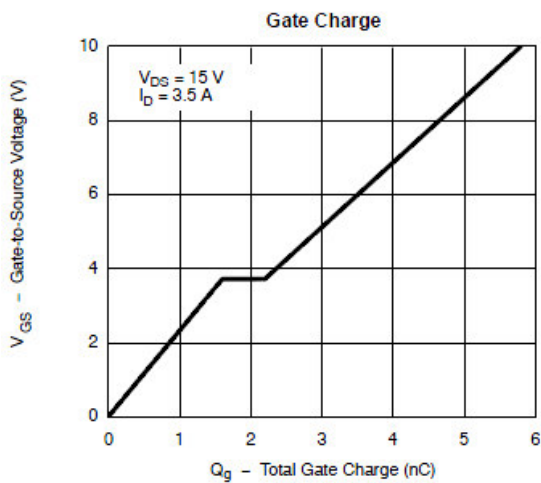
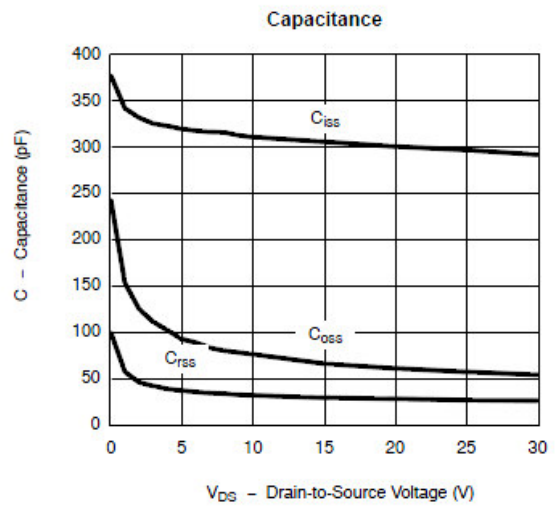
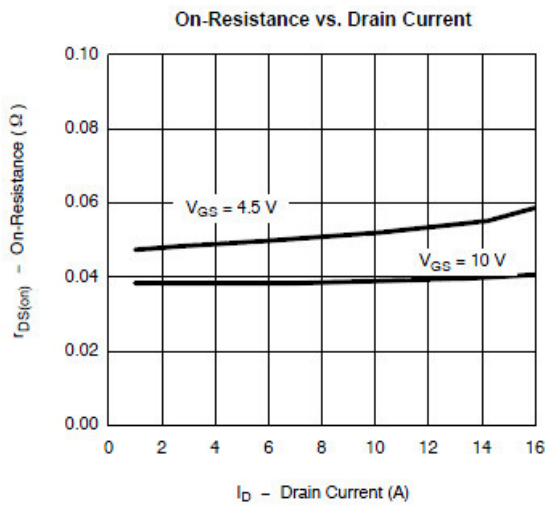
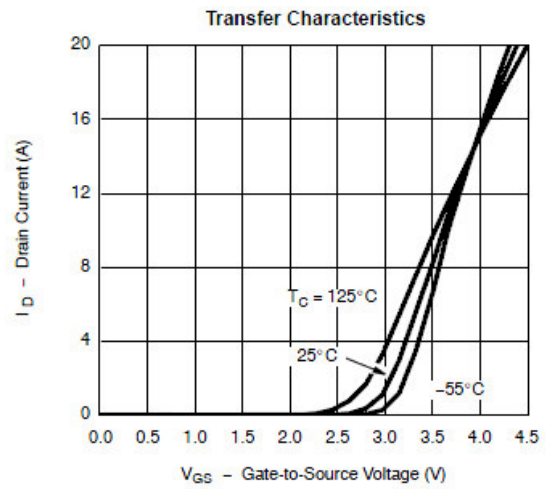
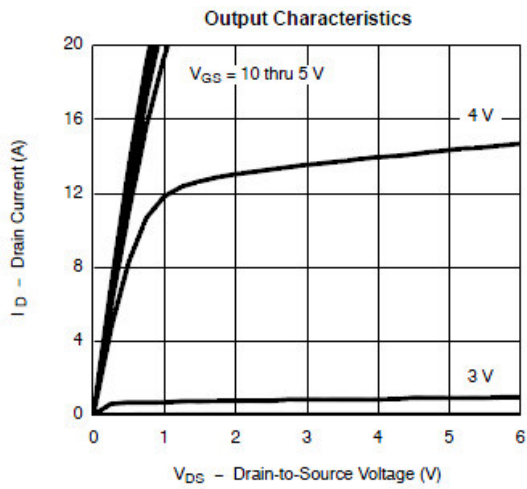
Symbol	Parameter	Typical	Unit	
V_{DSS}	Drain-Source Voltage	30	V	
V_{GSS}	Gate –Source Voltage	± 20	V	
I_D	Continuous Drain Current($T_J=150^\circ\text{C}$)	$T_A=25^\circ\text{C}$	4.0	A
		$T_A=70^\circ\text{C}$	3.5	
I_{DM}	Pulsed Drain Current	20	A	
I_S	Continuous Source Current(Diode Conduction)	1.5	A	
P_D	Power Dissipation	$T_A=25^\circ\text{C}$	1.25	W
		$T_A=70^\circ\text{C}$	0.8	
T_J	Operating Junction Temperature	150	$^\circ\text{C}$	
T_{STG}	Storage Temperature Range	-55/150	$^\circ\text{C}$	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	120	$^\circ\text{C}/\text{W}$	

Electrical Characteristics

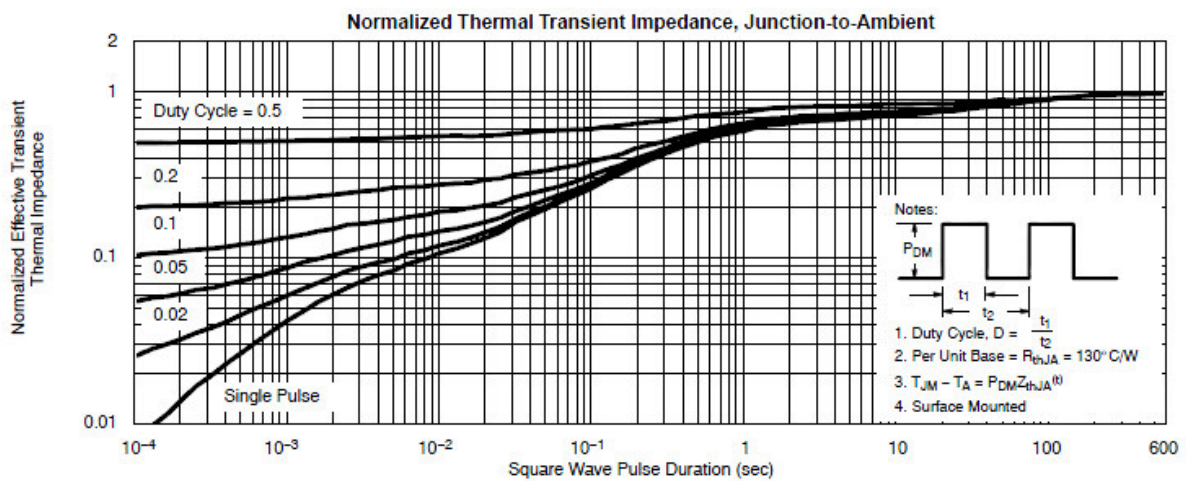
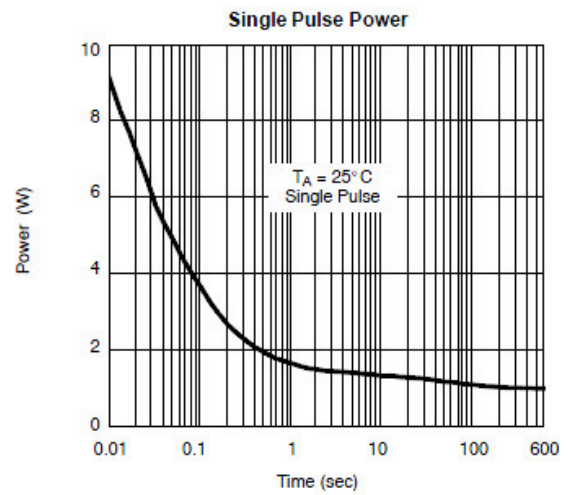
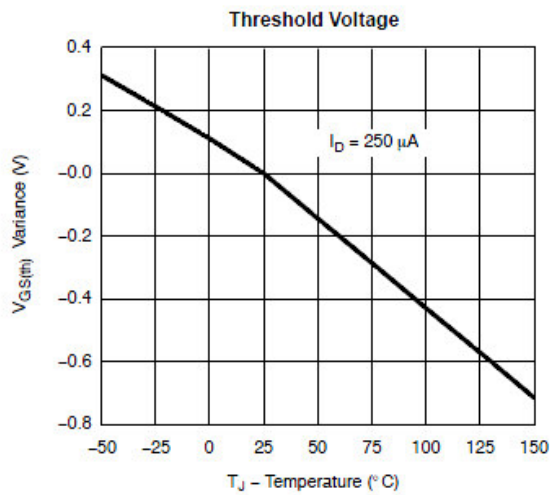
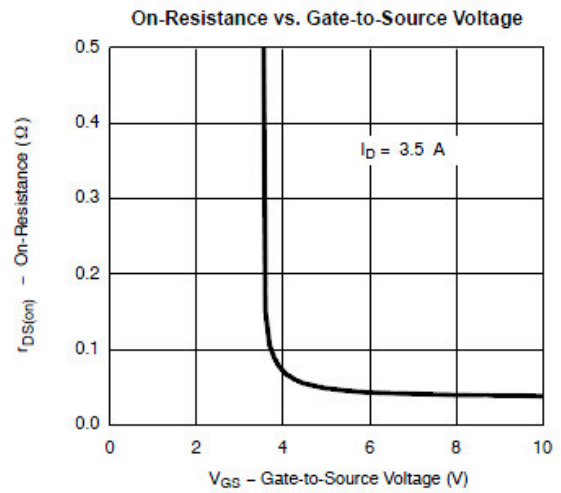
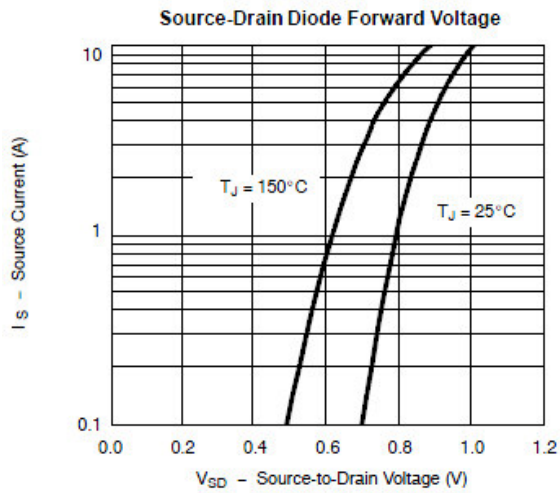
($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ	Max.	Unit
Static						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.3		2.1	
I_{GSS}	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$			1	μA
		$V_{DS}=24V, V_{GS}=0V, T_J=85^\circ\text{C}$			30	
$I_{D(on)}$	On-State Drain Current	$V_{DS}\geq 4.5V, V_{GS}=10V$	6			A
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=10V, I_D=4.0A$		25	40	m Ω
		$V_{GS}=4.5V, I_D=3.5A$		35	50	
g_{fs}	Forward Transconductance	$V_{DS}=4.5V, I_D=2.5A$		8		S
V_{SD}	Diode Forward Voltage	$I_S=1.6A, V_{GS}=0V$		0.8	1.2	V
Dynamic						
C_{iss}	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1\text{MHz}$		320		pF
C_{oss}	Output Capacitance			70		
C_{riss}	Reverse Transfer Capacitance			30		
Q_g	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V, I_D=2.6A$		3.0	4.5	nC
Q_{gs}	Gate-Source Charge			1.6		
Q_{gd}	Gate-Drain Charge			0.6		
$t_{d(on)}$	Turn-On Time	$V_{DD}=15V, R_L=15\Omega, I_D=1.0A, V_{GEN}=10V, R_G=6\Omega$		8	12	ns
t_r				12	18	
$t_{d(off)}$	Turn-Off Time			15	30	
t_f				8	15	

Typical Performance Characteristics

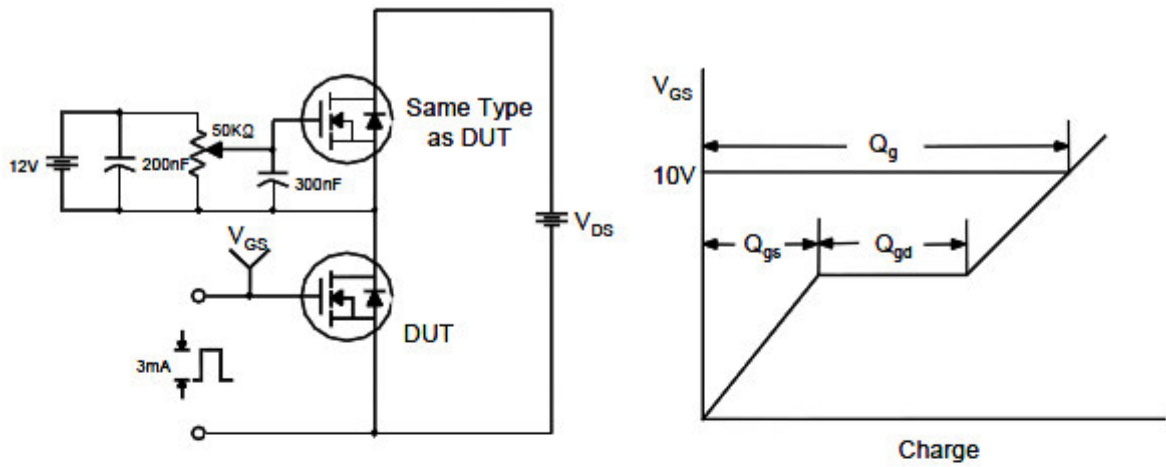


Typical Performance Characteristics (continue)

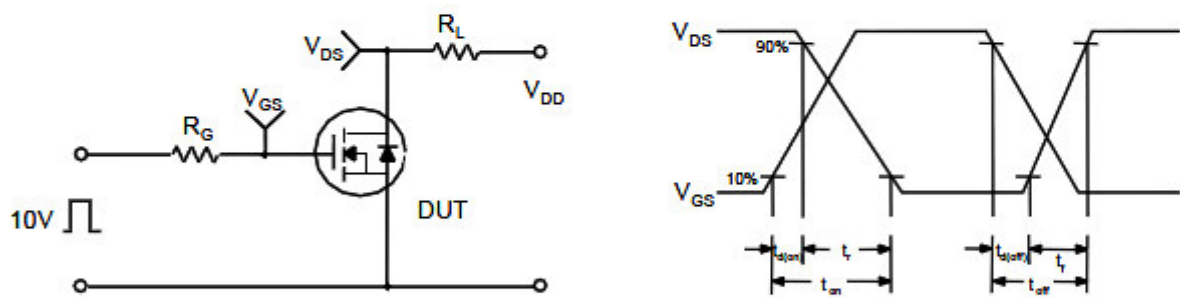


Typical Characteristics

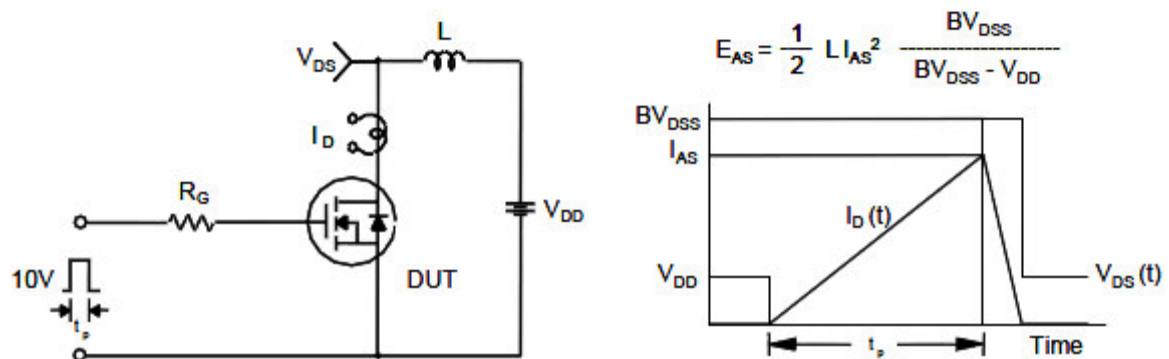
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

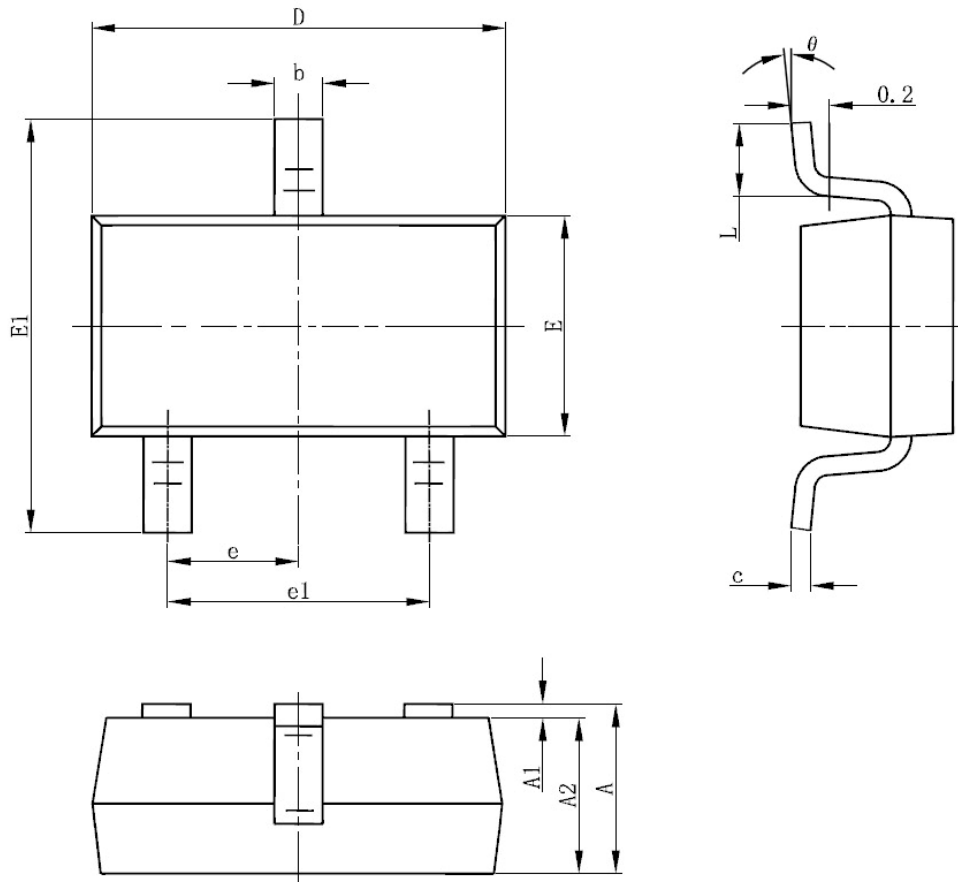


Unclamped Inductive Switching Test Circuit & Waveforms



Package Dimension

SOT-23-3L







Dimensions





SYMBOL	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	1.05	1.25	0.041	0.049
A1	0	0.1	0	0.004
A2	1.05	1.15	0.041	0.045
b	0.3	0.4	0.012	0.016
c	0.1	0.2	0.004	0.008
D	2.82	3.02	0.111	0.119
E	1.5	1.7	0.059	0.067
E1	2.65	2.95	0.104	0.116
e	0.950 TYP		0.037 TYP	
e1	1.8	2	0.071	0.079
L	0.700 REF		0.028 REF	
L1	0.3	0.6	0.012	0.024
θ	0°	8°	0°	8°



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