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20V P-Channel Enhancement Mode MOSFET – ESD Protected SOT-563 Unit : inch(mm) Voltage -20 V Current -0.6A 0.006(0.17) Features 0.044(1.10)0.035(0.90)0.067(1.70) 0.059(1.50) RDS(ON), VGS@-4.5V, ID@-0.6A<340mΩ RDS(ON), VGS@-2.5V, ID@-0.4A<420mΩ 0.052(1.30) 0.024(0.60) 0.043(1.10) 0.067(1.70) 0.019(0.50) RDS(ON), VGS@-1.8V, ID@-0.2A<600mΩ . 0.059(1.50) Advanced Trench Process Technology 0.007(0.17) Specially Designed for Switch Load, PWM Application, etc. ESD Protected • Lead free in compliance with EU RoHS 2011/65/EU directive Green molding compound as per IEC61249 Std. 0.012(0.30) (Halogen Free) D1 G2 **S**2 **Mechanical Data** 6 5 4 • Case: SOT-563 Package Terminals: Solderable per MIL-STD-750, Method 2026 • Approx. Weight: 0.00009 ounces, 0.0026 grams 3

Marking: X03

Maximum Ratings and Thermal Characteristics (T_A=25[°]C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 8	V
Continuous Drain Current		I _D	-0.6	А
Pulsed Drain Current		I _{DM}	-2.4	А
Power Dissipation	T _a =25°C	P _D	300	mW
	Derate above 25°C		2.4	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient ^(Note 3)		R _{eJA}	417	°C/W

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D2



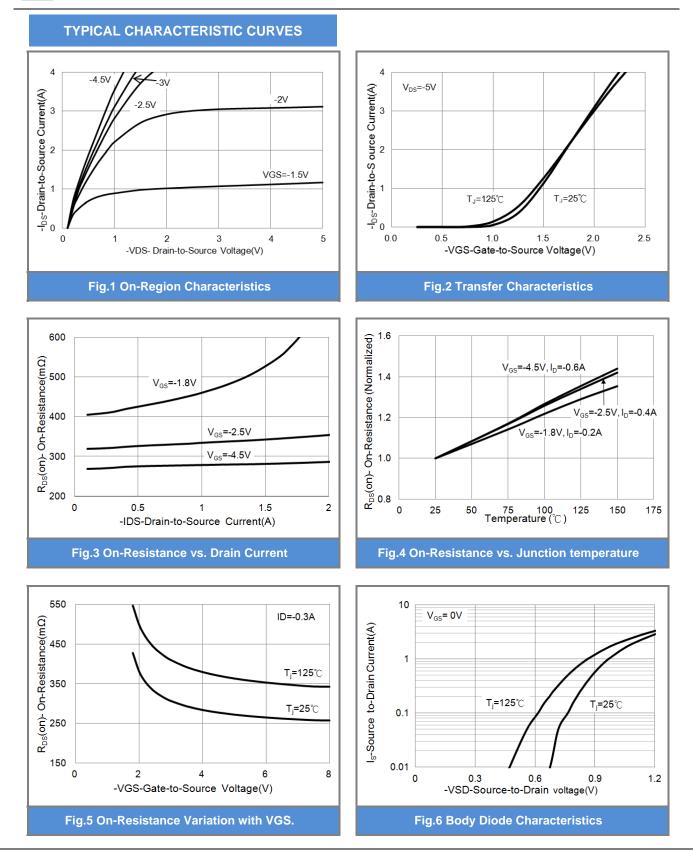
Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static					L	
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250$ uA	-0.4	-0.64	-1.0	V
		V _{GS} =-4.5V, I _D =-0.6A	-	280	340	
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	V _{GS} =-2.5V, I _D =-0.4A	-	330	420	mΩ
		V _{GS} =-1.8V, I _D =-0.2A	-	420	600	-
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	<u>+</u> 3.5	<u>+</u> 10	uA
Dynamic						
Total Gate Charge	Qg		-	2.2	-	nC
Gate-Source Charge	Q_gs	V _{DS} =-10V, I _D =-0.6A, V _{GS} =-4.5V ^(Note 1,2)	-	0.4	-	
Gate-Drain Charge	Q_gd	V _{GS} 4.5V	-	0.5	-	
Input Capacitance	Ciss		-	151	-	pF
Output Capacitance	Coss	V_{DS} =-10V, V_{GS} =0V,	-	27	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	9	-	
Switching						
Turn-On Delay Time	td _(on)		-	9	-	
Turn-On Rise Time	tr	V_{DD} =-10V, I_{D} =-0.6A, V_{GS} =-4.5V, R_{G} =6 Ω ^(Note 1,2)	-	37	-	ns
Turn-Off Delay Time	td _(off)		-	128	-	
Turn-Off Fall Time	tf	R _G =012	-	72	-	
Drain-Source Diode						
Maximum Continuous Drain-Source					-0.4	A
Diode Forward Current	I _S		-	-	-0.4	A
Diode Forward Voltage	V_{SD}	I _S =-1A, V _{GS} =0V		-0.95	-1.2	v

NOTES :

- 1. Pulse width300us, Duty cycle2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Reja is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited







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TYPICAL CHARACTERISTIC CURVES

Fig.7 Gate-Charge Characteristics

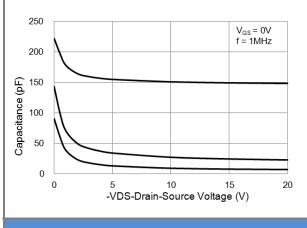
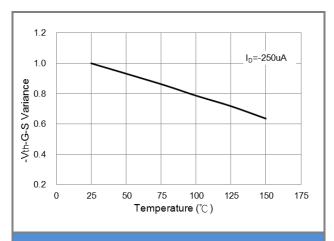


Fig.9 Capacitance vs. Drain-Source Voltage.





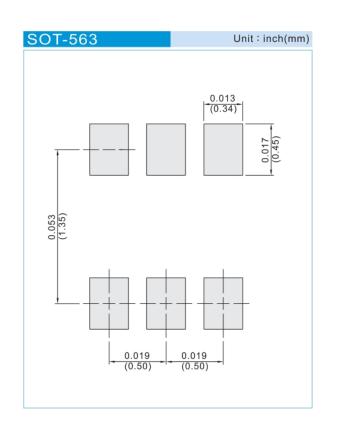




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJX8803_R1_00001	SOT-563	4K pcs / 7" reel	X03	Halogen free
PJX8803_R2_00001	SOT-563	10K pcs / 13" reel	X03	Halogen free

MOUNTING PAD LAYOUT







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