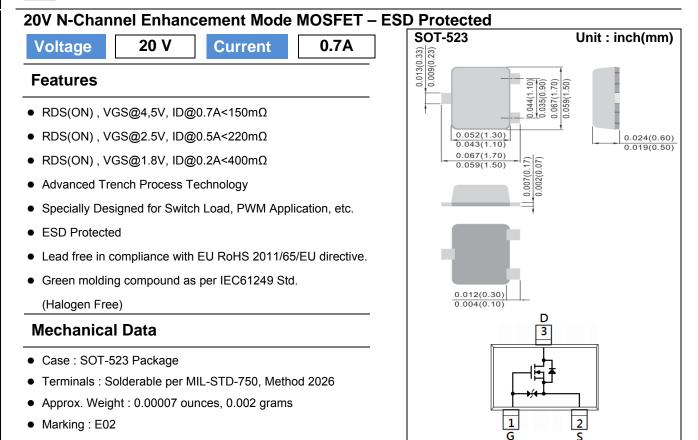
ΡΛΝ	ĴΪΤ
	SEMI CONDUCTOR



Maximum Ratings and Thermal Characteristics (T_A=25[°]C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 8	V
Continuous Drain Current		I _D	0.7	А
Pulsed Drain Current		I _{DM}	2.8	А
Power Dissipation	T _a =25°C		300	mW
	Derate above 25°C	P _D	2.4	mW/°C
Operating Junction and Storage T	perating Junction and Storage Temperature Range		-55~150	°C
Typical Thermal resistance - Junction to Ambient ^(Note 3)		R _{θJA}	417	°C/W



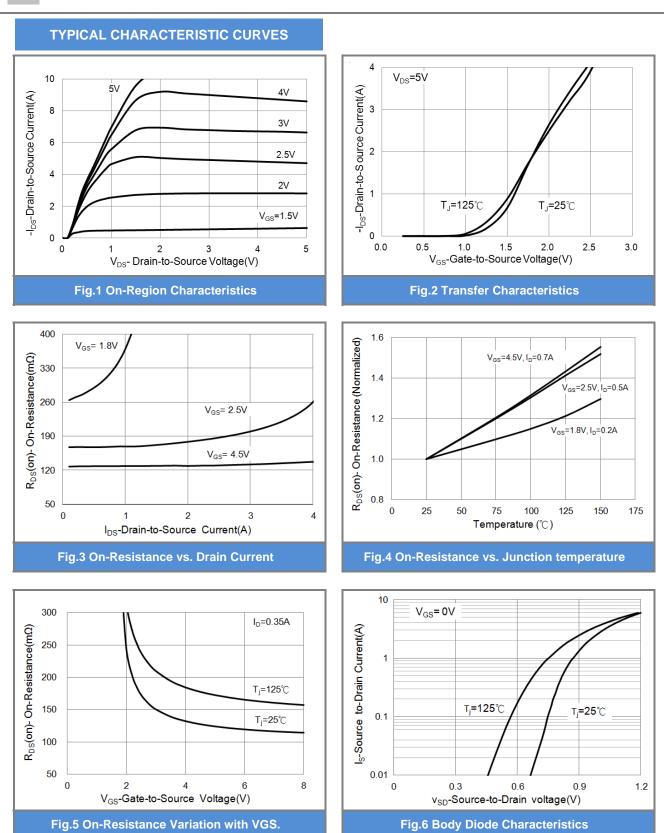
Electrical Characteristics (T_A=25[°]C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250$ uA	0.5	0.78	1.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =0.7A	-	129	150	mΩ
		V _{GS} =2.5V, I _D =0.5A	-	167	220	
		V _{GS} =1.8V, I _D =0.2A	-	260	400	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V	-	0.01	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	<u>+</u> 2	<u>+</u> 10	uA
Dynamic						
Total Gate Charge	Qg		_	1.6	-	nC
Gate-Source Charge	Q_gs	V _{DS} =10V, I _D =0.7A, V _{GS} =4.5V ^(Note 1,2)	_	0.3	-	
Gate-Drain Charge	Q_gd		-	0.4	-	
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V, f=1.0MHZ	_	92	-	pF
Output Capacitance	Coss		-	25	-	
Reverse Transfer Capacitance	Crss	I=1.0IVIHZ	-	9	-	
Switching						
Turn-On Delay Time	td _(on)	X 40X L 0 7A	-	6	-	
Turn-On Rise Time	tr	V_{DD} =10V, I _D =0.7A,	-	26	-	ns
Turn-Off Delay Time	td _(off)	V_{GS} =4.5V, R _G =6Ω ^(Note 1,2)	-	41	-	
Turn-Off Fall Time	tf	R _G =012	-	31	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	I				0.4	А
Diode Forward Current	I _S		-	-	0.4	A
Diode Forward Voltage	V_{SD}	I _S =1A, V _{GS} =0V		0.89	1.2	v

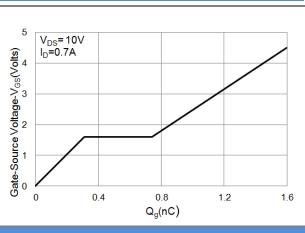
NOTES :

- 1. Pulse width200us, Duty cycle
- 2. Essentially independent of operating temperature typical characteristics.
- 3. ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited









TYPICAL CHARACTERISTIC CURVES

Fig.7 Gate-Charge Characteristics

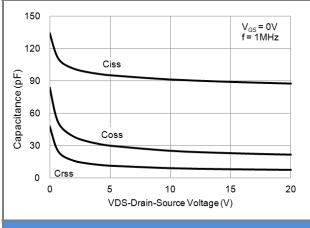
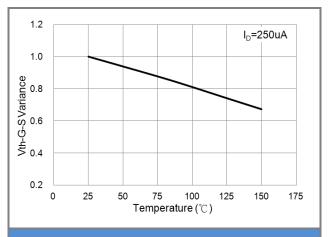


Fig.9 Capacitance vs. Drain-Source Voltage.





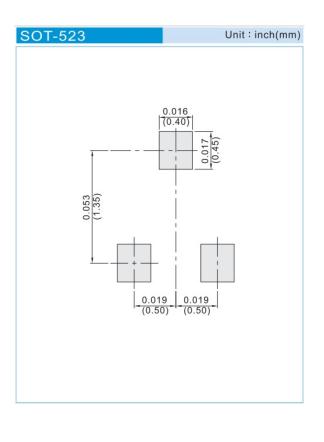




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJE8402_R1_00001	SOT-523	4K pcs / 7" reel	E02	Halogen free

MOUNTING PAD LAYOUT







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