



#### 20V N-Channel Enhancement Mode MOSFET

Voltage 20 V Current 5.8A

#### **Features**

- RDS(ON) , VGS@4.5V, ID@5.8A<27mΩ</li>
- RDS(ON) , VGS@2.5V, ID@3.2A<40mΩ</li>
- RDS(ON) , VGS@1.8V, ID@1.6A<80mΩ</li>
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc..
- Lead free in Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

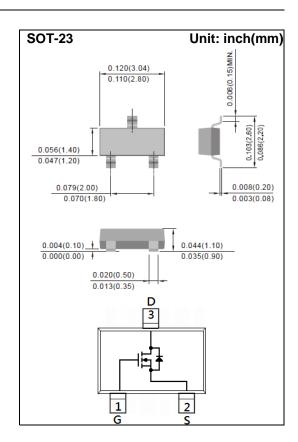
### **Mechanical Data**

Case: SOT-23 Package

Terminals : Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.0003 ounces, 0.0084 grams

Marking: A16



### **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	20	V
Gate-Source Voltage		$V_{GS}$	<u>+</u> 12	V
Continuous Drain Current		I <sub>D</sub>	5.8	Α
Pulsed Drain Current		I <sub>DM</sub>	23.2	Α
Power Dissipation	T <sub>a</sub> =25°C	P <sub>D</sub>	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		$T_{J}, T_{STG}$	-55~150	°C
Typical Thermal resistance				
- Junction to Ambient (Note 3)		$R_{\theta JA}$	100	°C/W





# **Electrical Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	$BV_{DSS}$	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250uA$	0.5	0.77	1.2	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5.8A	-	23	27	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =3.2A	-	32	40	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =1.6A	-	61	80	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V	-	0.01	1	uA
Gate-Source Leakage Current	$I_{GSS}$	V <sub>GS</sub> = <u>+</u> 12V, V <sub>DS</sub> =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	$Q_g$	V <sub>DS</sub> =10V, I <sub>D</sub> =5.8A, V <sub>GS</sub> =4.5V <sup>(Note 1,2)</sup>	-	6.7	-	nC
Gate-Source Charge	$Q_gs$		_	1.2	-	
Gate-Drain Charge	$Q_{gd}$		-	2	-	
Input Capacitance	Ciss	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V,	-	513	-	pF
Output Capacitance	Coss		-	75	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	59	-	
Switching						
Turn-On Delay Time	td <sub>(on)</sub>	\/ 40\/   5.0A	-	6	-	
Turn-On Rise Time	tr	$V_{DD}$ =10V, $I_{D}$ =5.8A, $V_{GS}$ =4.5V, $R_{G}$ =6 $\Omega$ (Note 1,2)		56		ns
Turn-Off Delay Time	td <sub>(off)</sub>		_	23	-	us
Turn-Off Fall Time	tf		-	13	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	-				1 5	А
Diode Forward Current	I <sub>S</sub>		-	_	1.5	
Diode Forward Voltage	$V_{\text{SD}}$	I <sub>S</sub> =1.0A, V <sub>GS</sub> =0V	-	0.71	1.2	V

#### NOTES:

- 1. Pulse width<a></a>300us, Duty cycle<a></a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Reja is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





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# **PJA3416**

#### **TYPICAL CHARACTERISTIC CURVES**

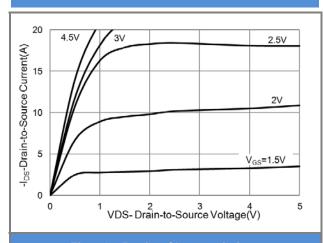
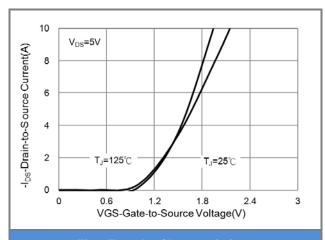


Fig.1 On-Region Characteristics



**Fig.2 Transfer Characteristics** 

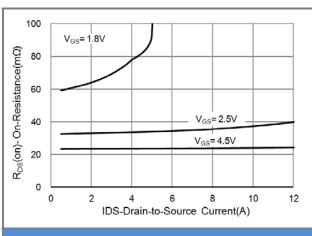


Fig.3 On-Resistance vs. Drain Current

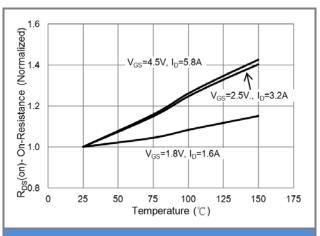
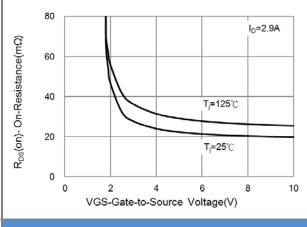
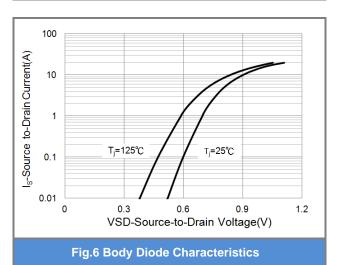


Fig.4 On-Resistance vs. Junction temperature





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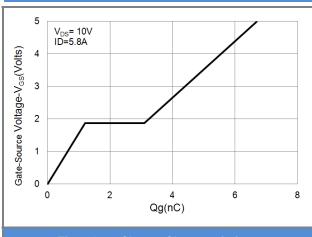


Fig.7 Gate-Charge Characteristics

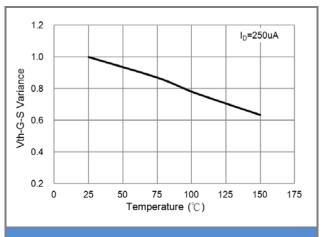


Fig.8 Threshold Voltage Variation with Temperature.

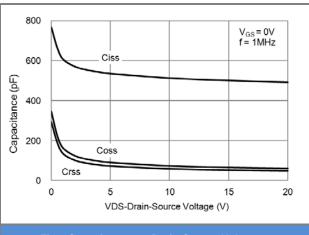


Fig.9 Capacitance vs. Drain-Source Voltage.

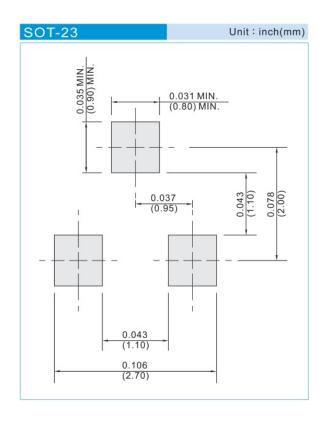




### PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJA3416_R1_00001	SOT-23	3K pcs / 7" reel	A16	Halogen free
PJA3416_R2_00001	SOT-23	12K pcs / 13" reel	A16	Halogen free

### **MOUNTING PAD LAYOUT**







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