# PT40QPx45

**KEY PARAMETERS** 

4500V

13000A

**5000A/**μs

760A



# **Pulse Power Thyristor Switch**

**Preliminary Information** 

 $V_{DRM}$ 

I<sub>T(AV)</sub>

I<sub>TSM</sub> dl/dt

DS5267-2 July 2014 (LN31779)

## **APPLICATIONS**

- Pulse Power
- Crowbars
- Ignitron Replacement

# **FEATURES**

- Double Side Cooling
- Fast Turn-on
- Low Turn-on Losses

### **VOLTAGE RATINGS**

Type Number	Repetitive Peak Voltages V <sub>DRM</sub> /V <sub>RRM</sub> V	Conditions
PT40QPx45	4500/16	$T_{vj} = 0^{\circ} \text{ to } 125^{\circ}\text{C},$ $I_{DRM} = I_{RRM} = 50\text{mA},$ $V_{DRM}, V_{RRM} t_{p} = 10\text{ms}$

Lower voltage grades available.

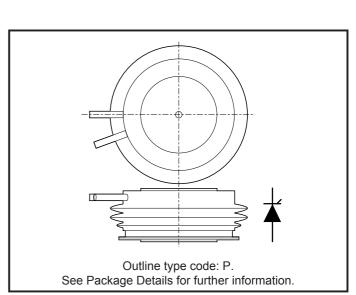


Fig.1 Package outline

## **CURRENT RATINGS**

Symbol	Parameter	Conditions	Max.	Units
Double Side Cooled				
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load, T <sub>case</sub> = 80°C	760	А
I <sub>T(RMS)</sub>	RMS value	T <sub>case</sub> = 80°C	1190	Α

# PT40QPx45

# **SURGE RATINGS**

Symbol	Parameter	Conditions	Max.	Units
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine; T <sub>case</sub> = 125°C	10.4	kA
l²t	I <sup>2</sup> t for fusing	V <sub>R</sub> = 50% V <sub>RRM</sub> - 1/4 sine	541 x 10 <sup>3</sup>	A²s
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine; T <sub>case</sub> = 125°C	13.0	kA
l²t	I <sup>2</sup> t for fusing	V <sub>R</sub> = 0	845 x 10 <sup>3</sup>	A <sup>2</sup> s

# THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions		Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance - junction to case	Double side cooled	dc	-	0.033	°C/W
R <sub>th(c-h)</sub>	Thermal resistance - case to heatsink	Clamping force 12.0kN with mounting compound	Double side	-	0.008	°C/W
_	Vintual impetion to manage trans	On-state (conducting)		-	135	°C
T <sub>vj</sub> Virtual junction temperature		Reverse (blocking)		-	125	°C
T <sub>stg</sub>	Storage temperature range			-55	125	°C
-	Clamping force			11.0	15.0	kN

# **DYNAMIC CHARACTERISTICS**

Symbol	Parameter	Conditions	i	Тур.	Max.	Units
I <sub>RRM</sub> /I <sub>DRM</sub>	Peak reverse and off-state current	At V <sub>RRM</sub> /V <sub>DRM</sub> , T <sub>case</sub> = 125°C		-	50	mA
dV/dt	Maximum linear rate of rise of off-state voltage	To 67% $V_{DRM}$ $T_j$ = 125°C. $R_{gk} \le 1.5Ω$		-	200	V/μs
dI/dt	Rate of rise of on-state current	From 67% $V_{DRM}$ to 20kA Gate source 30A $t_i = 1.5 \mu s$ , $T_j = 25 ^{\circ} C$	Non-repetitive	-	5000	A/μs
V <sub>T(TO)</sub>	Threshold voltage	At T <sub>vj</sub> = 125°C		-	1.4	٧
r <sub>T</sub>	On-state slope resistance	At T <sub>vj</sub> = 125°C		-	0.152	mΩ

# **GATE TRIGGER CHARACTERISTICS AND RATINGS**

Symbol	Parameter	Conditions	Тур.	Max.	Units
$V_{\rm GT}$	Gate trigger voltage	V <sub>DRM</sub> = 5V, T <sub>case</sub> = 25°C	1.0	4.0	V
I <sub>GT</sub>	Gate trigger current	V <sub>DRM</sub> = 5V, T <sub>case</sub> = 25°C	-	1.5	А

# **ORDERING INFORMATION**

PT Pulse Power Thyristor

40Q Device type

P Package outline type code x lead length (see table, right)

45 Voltage x100

Lead length (x)				
0	No lead			
С	8"	200mm		
D	10"	250mm		
E	12"	300mm		
F	16"	400mm		
G	18"	450mm		
Н	20"	500mm		
J	24"	600mm		
K	30"	750mm		
L	40"	1000mm		

## **CURVES**

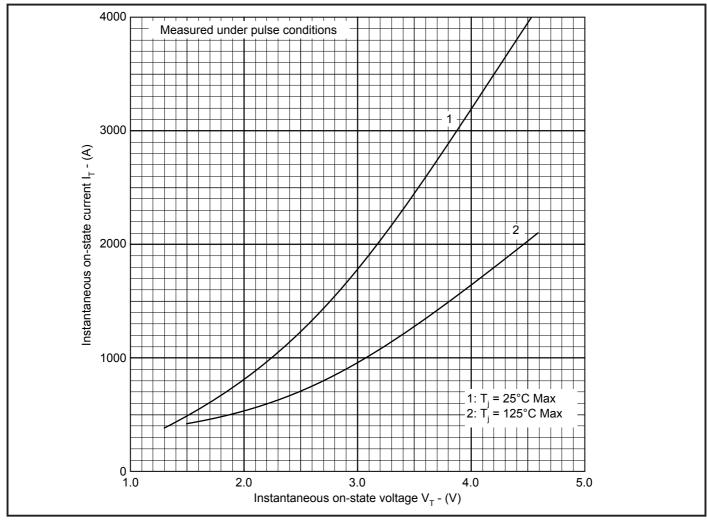


Fig.2 Maximum (limit) on-state characteristics

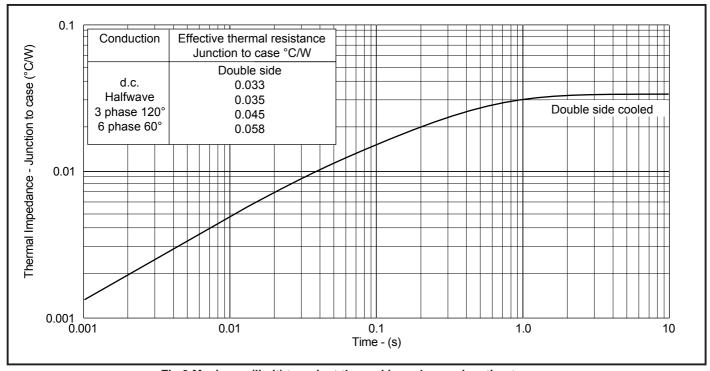
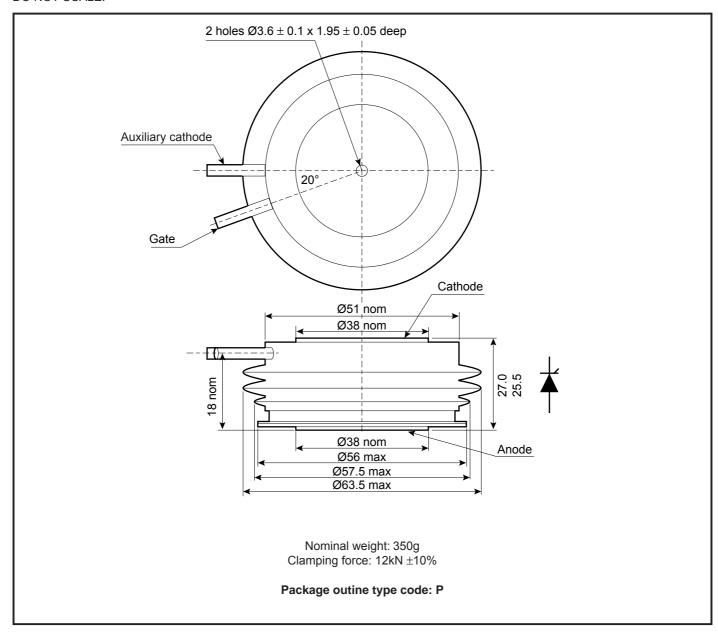


Fig.3 Maximum (limit) transient thermal impedance - junction to case

# **PACKAGE DETAILS**

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





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