

# DIGITRON SEMICONDUCTORS

## MAC320(A) SERIES

## SILICON BIDIRECTIONAL THYRISTORS

Available Non-RoHS (standard) or RoHS compliant (add PBF suffix).

Available as "HR" (high reliability) screened per MIL-PRF-19500, JANTX level. Add "HR" suffix to base part number.

### MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
<b>Peak repetitive off-state voltage</b> <sup>(1)</sup> ( $T_J = -40$ to $+125^\circ\text{C}$ , $\frac{1}{2}$ sine wave, 50 to 60Hz, gate open) MAC320-4, MAC320A-4 MAC320-6, MAC320A-6 MAC320-8, MAC320A-8 MAC320-10, MAC320A-10	$V_{\text{DRM}}$	200 400 600 800	Volts
<b>Peak gate voltage</b>	$V_{\text{GM}}$	10	Volts
<b>RMS on-state current</b> (Full cycle sine wave, 50 to 60Hz, $T_C = 75^\circ\text{C}$ )	$I_{\text{T(RMS)}}$	20	Amps
<b>Peak non-repetitive surge current</b> (1 cycle, 60Hz, $T_C = 75^\circ\text{C}$ , preceded and followed by rated current)	$I_{\text{TSM}}$	150	Amps
<b>Peak gate power</b> ( $T_C = 75^\circ\text{C}$ , $t \leq 2\mu\text{s}$ )	$P_{\text{GM}}$	20	Watts
<b>Average gate power</b> ( $T_C = 75^\circ\text{C}$ , $t \leq 8.3\text{ms}$ )	$P_{\text{G(AV)}}$	0.5	Watts
<b>Peak gate current</b>	$I_{\text{GM}}$	2	Amps
<b>Operating junction temperature range</b>	$T_J$	-40 to +125	$^\circ\text{C}$
<b>Storage temperature range</b>	$T_{\text{stg}}$	-40 to +150	$^\circ\text{C}$

Note 1:  $V_{\text{DRM}}$  for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Maximum	Unit
<b>Thermal resistance, junction to case</b>	$R_{\theta\text{JC}}$	1.8	$^\circ\text{C}/\text{W}$

### ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$ and either polarity of MT2 to MT1 voltage unless otherwise noted)

Characteristic	Symbol	Min	Typ.	Max	Unit
<b>Peak blocking current</b> ( $V_D = \text{Rated } V_{\text{DRM}}$ , gate open, $T_J = 25^\circ\text{C}$ ) ( $V_D = \text{Rated } V_{\text{DRM}}$ , gate open, $T_J = 125^\circ\text{C}$ )	$I_{\text{DRM}}$	-	-	10 2	$\mu\text{A}$ mA
<b>Peak on-state voltage</b> (either direction) ( $I_{\text{TM}} = 28\text{A}$ peak, pulse width $\leq 2\text{ms}$ , duty cycle $\leq 2\%$ .)	$V_{\text{TM}}$	-	1.4	1.7	Volts
<b>Gate trigger current</b> (continuous dc) ( $V_D = 12\text{V}$ , $R_L = 100\Omega$ ) MT2(+),G(+); MT2(+),G(-); MT2(-),G(-) MT2(-),G(+) "A" suffix only	$I_{\text{GT}}$	-	-	50 75	mA
<b>Gate trigger voltage</b> (continuous dc) ( $V_D = 12\text{V}$ , $R_L = 100\Omega$ ) MT2(+),G(+); MT2(+),G(-); MT2(-),G(-) MT2(-),G(+) "A" suffix only ( $V_D = \text{Rated } V_{\text{DRM}}$ , $R_L = 10\text{k}\Omega$ , $T_J = 110^\circ\text{C}$ ) MT2(+),G(+); MT2(+),G(-); MT2(-),G(-) MT2(-),G(+) "A" suffix only	$V_{\text{GT}}$	-	0.9 1.4	2.0 2.5	Volts
<b>Holding current</b> (either direction) ( $V_D = 12\text{V}$ , $I_{\text{TM}} = 200\text{mA}$ , gate open)	$I_{\text{H}}$	-	6	40	mA
<b>Gate controlled turn-on time</b> ( $V_D = \text{Rated } V_{\text{DRM}}$ , $I_{\text{TM}} = 28\text{A}$ , $I_{\text{G}} = 120\text{mA}$ , rise time = $0.1\mu\text{s}$ , pulse width = $2\mu\text{s}$ )	$t_{\text{gt}}$	-	1.5	-	$\mu\text{s}$
<b>Critical rate of rise of commutation voltage</b> ( $V_D = \text{Rated } V_{\text{DRM}}$ , $I_{\text{TM}} = 28\text{A}$ peak, commutating $di/dt = 10\text{A/ms}$ , gate unenergized, $T_C = 75^\circ\text{C}$ )	$dv/dt(c)$	-	5	-	$\text{V}/\mu\text{s}$

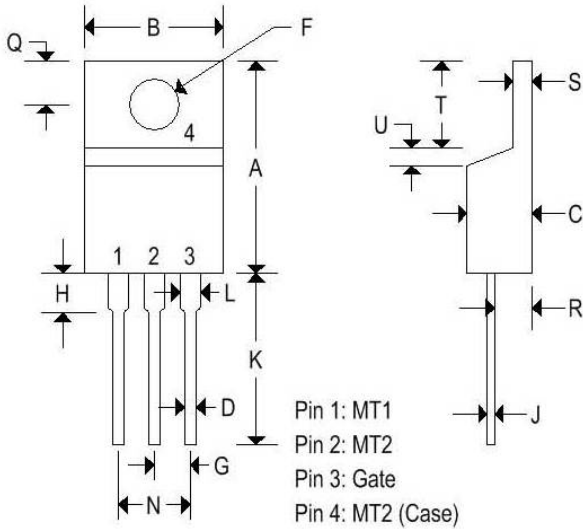
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### MECHANICAL CHARACTERISTIC

Case	TO-220AB
Marking	Alpha-numeric
Pin out	See below



	TO-220AB			
	Inches		Millimeters	
	Min	Max	Min	Max
A	0.575	0.620	14.600	15.750
B	0.380	0.405	9.650	10.290
C	0.160	0.190	4.060	4.820
D	0.025	0.035	0.640	0.890
F	0.142	0.147	3.610	3.730
G	0.095	0.105	2.410	2.670
H	0.110	0.155	2.790	3.930
J	0.014	0.022	0.360	0.560
K	0.500	0.562	12.700	14.270
L	0.045	0.055	1.140	1.390
N	0.190	0.210	4.830	5.330
Q	0.100	0.120	2.540	3.040
R	0.080	0.110	2.040	2.790
S	0.045	0.055	1.140	1.390
T	0.235	0.255	5.970	6.480
U	-	0.050	-	1.270
V	0.045	-	1.140	-
Z	-	0.080	-	2.030

FIGURE 1 — RMS CURRENT DERATING

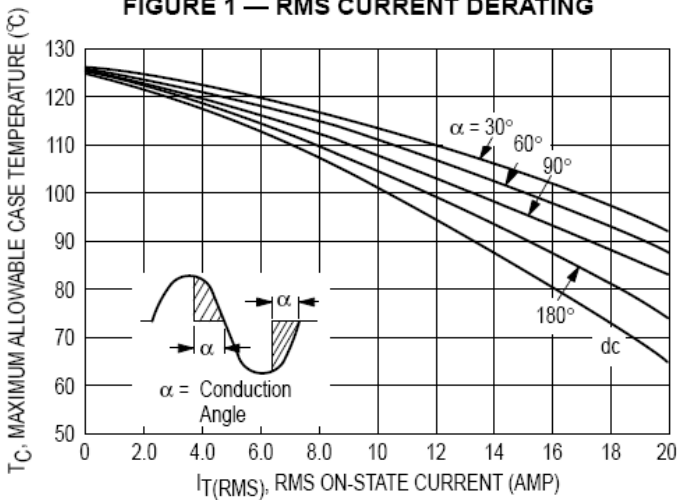
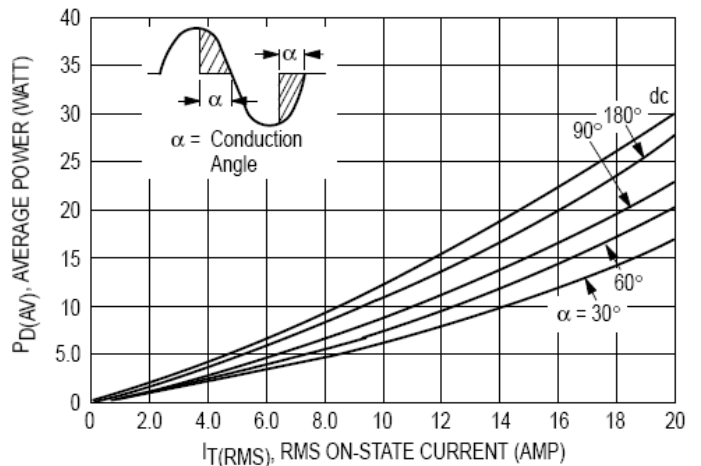


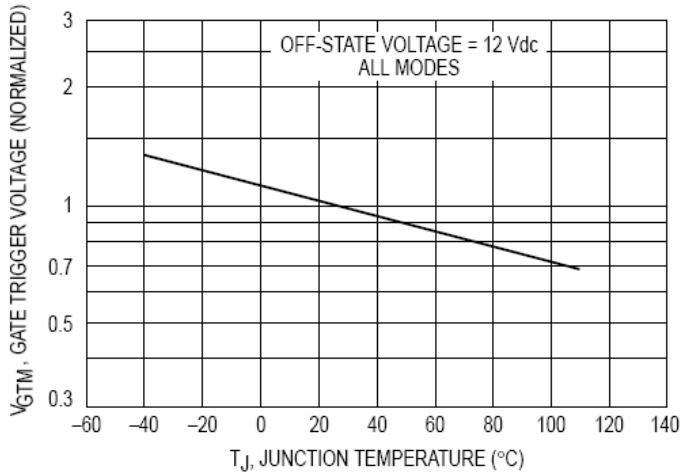
FIGURE 2 — ON-STATE POWER DISSIPATION



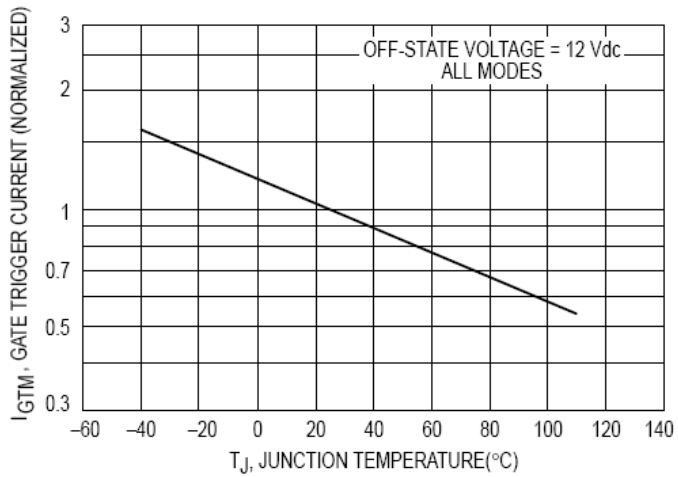
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## MAC320(A) SERIES SILICON BIDIRECTIONAL THYRISTORS

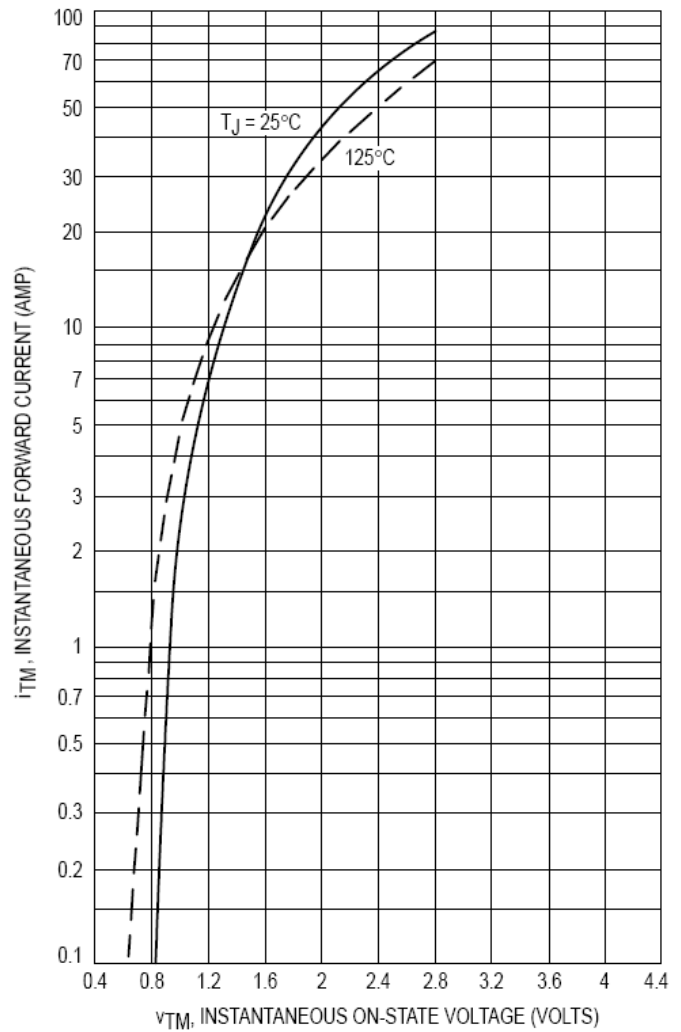
**FIGURE 3 — TYPICAL GATE TRIGGER VOLTAGE**



**FIGURE 4 — TYPICAL GATE TRIGGER CURRENT**



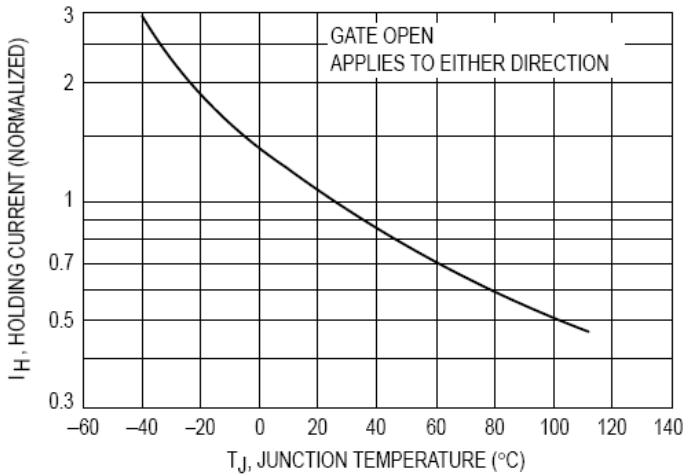
**FIGURE 5 — MAXIMUM ON-STATE CHARACTERISTICS**



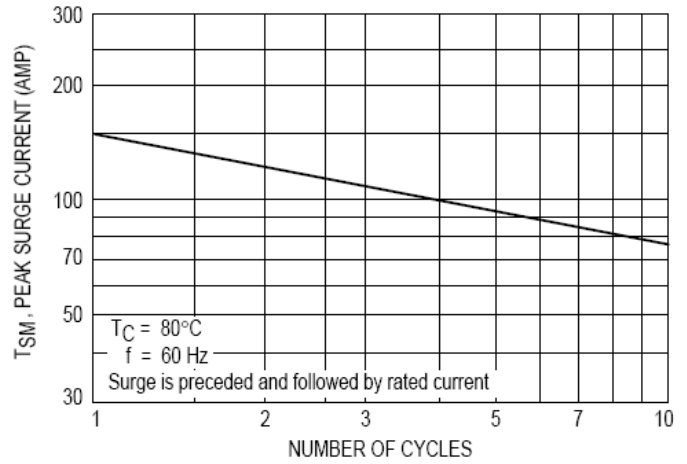
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**FIGURE 6 — TYPICAL HOLDING CURRENT**



**FIGURE 7 — MAXIMUM ON-REPETITIVE SURGE CURRENT**



**FIGURE 8 — THERMAL RESPONSE**

