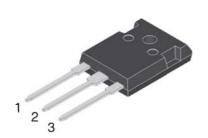
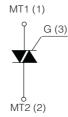
RoHS



HIGH COMMUTATION TRIAC

TO-3P / TO-247AD





On-State Current

Gate Trigger Current

40 Amp

 \leq 50 mA (16)

Off-State Voltage

600 V ÷ 800 V

FEATURES

- Glass/passivated die junctions
- High current Triac
- Low thermal resistance with clip bonding
- High commutation
- High surge current capability
- Low forward voltage drop
- Solder dip 260°C, 10s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC
- Meets MSL level 3, per J-STD-020, LF maximum peak of 260° C

MECHANICAL DATA

- Case: TO-3P / TO-247AD. Epoxy meets UL 94V-0 flammability rating.
- Polarity: As marked on the body.
- **Terminals:** Matte tin plated leads, solderable per MIL-STD-750 Method 2026, J-STD-002 and JESD22-B102. Consumer grade, meets JESD 201 class 1A whisker test.

TYPICAL APPLICATIONS

 Used on inductive loads, thanks to their high commutation performances.

Maximun Ratings and Electrical Characteristics at 25°C

SYMBOL	PARAMETER	CONDITIONS	Value	Unit
I _{T(RMS)}	RMS On-state Current (full sine wave)	All Conduction Angle, T _c = 80 °C	40	А
I _{TSM}	Non-repetitive On-State Current	Full Cycle, 60 Hz (t = 16.7 ms)	420	А
I _{TSM}	Non-repetitive On-State Current	Full Cycle, 50 Hz (t = 20 ms)	400	А
I ² t	Fusing Current	tp = 10 ms, Half Cycle	1000	A ² s
I_{GM}	Peak Gate Current	20 μs max. Tj = 125 °C	8	А
$P_{G(AV)} \\$	Average Gate Power Dissipation	Tj = 125 °C	1	W
dl/dt	Critical rate of rise of on-state current	$I_G = 2x I_{GT}, t_r \le 100 \text{ns}$	50	A/µs
		f = 120 Hz, T _j = 125 °C		
T _j	Operating Temperature		(-40 +125)	°C
T_{stg}	Storage Temperature		(-40 +150)	°C
T _{sld}	Soldering Temperature	10s max	260	°C

SYMBOL	PARAMETER	VOLTAGE		
OTNIBOL		M	N	
V _{DRM} /V _{RRM}	Repetitive Peak Off State Voltage	600	800	V

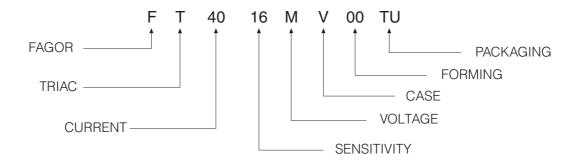


Electrical Characteristics at Tamb = 25 °C

SYMBOL	PARAMETER	CONDITIONS	Quadrant		SENSITIVITY	Unit
					16	
I _{GT} ⁽¹⁾	Gate Trigger Current	$V_D = 12 V_{DC}, R_L = 33\Omega, T_j = 25 °C$	Q1÷Q3	MAX	50	mA
V _{GT}	Gate Trigger Voltage	$V_D = 12 V_{DC}, R_L = 33\Omega, T_j = 25 °C$	Q1÷Q3	MAX	1.5	V
V _{GD}	Gate Non Trigger Voltage	$V_D = V_{DRM}, R_L = 3.3 \text{ K}\Omega, T_j = 125 \text{ °C}$	Q1÷Q3	MIN	0.2	V
I _H ⁽²⁾	Holding Current	$I_T = 100 \text{ mA,Gate open, } T_j = 25 ^{\circ}\text{C}$		MAX	80	mA
IL	Latching Current	I _G = 1.2 I _{GT} , T _j = 25 °C	Q1, Q3	MAX	80	mA
			Q2	MAX	160	mA
dV/dt (2)	Critical Rate of Voltage Rise	$V_D = 0.67 \times V_{DRM}$, Gate open		MIN	500	V/µs
		T _j = 125 °C				
V _{TM} ⁽²⁾	On-state Voltage	$I_{TM} = 60 \text{ Amp, tp} = 380 \ \mu \text{s,T}_j = 25 \ ^{\circ}\text{C}$		MAX	1.55	V
V _{t (o)} (2)	Threshold Voltage	T _j = 125 °C		MAX	0.85	V
r _d ⁽²⁾	Dynamic resistance	T _j = 125 °C		MAX	10	mΩ
I _{DRM} /I _{RRM}	Off-State Leakage Current	$V_D = V_{DRM},$ $T_j = 125 °C$		MAX	5	mA
		$V_R = V_{RRM},$ $T_j = 25 °C$		MAX	20	μΑ
R _{th(j-c)}	Thermal Resistance	for AC 360° conduction angle			1.1	°C/W
	Junction-Case					

⁽¹⁾ Minimum I_{GT} is guaranted at 5% of I_{GT} max.

Part Number Information



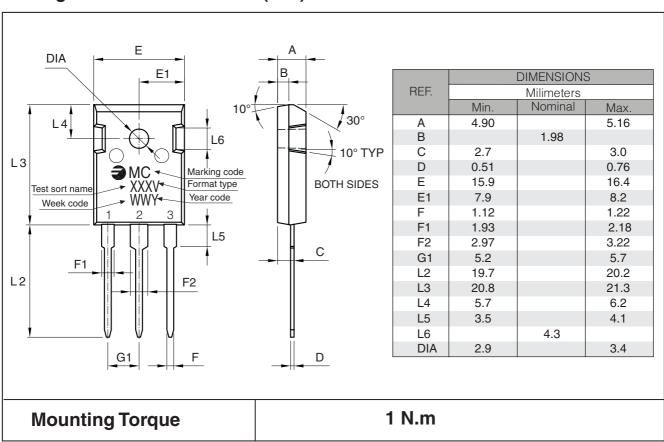
⁽²⁾ For either polarity of electrode MT2 voltage with reference to electrode MT1.



Ordering information

PREFERRED P/N	PACKAGE CODE	DELIVERY MODE	BASE QUANTITY	UNIT WEIGHT (g)
FT4016MV 00TUC	TU	TUBE	450	5.6

Package Outline Dimensions: (mm) TO-3P/TO-247AD





Ratings and Characteristics (Ta 25 °C unless otherwise noted)

Fig. 1: Maximum power dissipation versus RMS on-state current (full cycle).

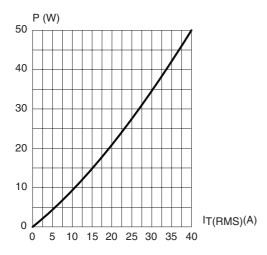


Fig. 3: On-state characteristics (maximum values)

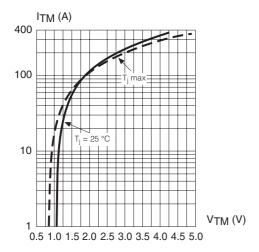


Fig. 5: Non repetitive surge peak on-state current for a sinusoidal pulse with width: tp < 10 ms, and corresponding value of l²t.

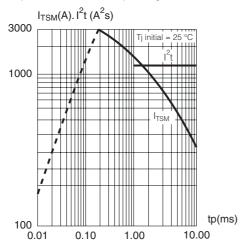


Fig. 2: RMS on-state current versus case temperature (full cycle).

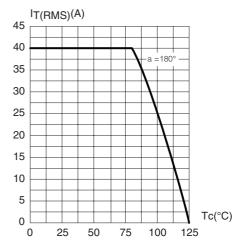


Fig. 4: Surge peak on-state current versus number of cycles

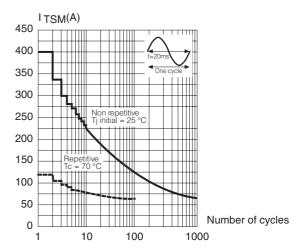
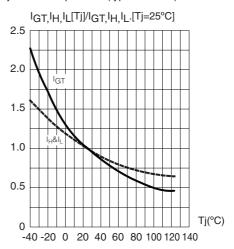


Fig. 7: Relative variation of gate trigger current, holding current and latching versus junction temperature (typical values)





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