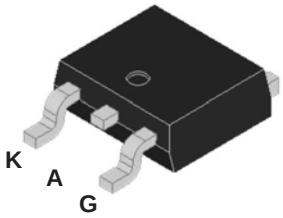
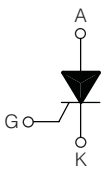
SENSITIVE GATE SCR

DPAK
(Plastic)





On-State Current
8 Amp

Gate Trigger Current
< 200 µA

Off-State Voltage
200 V ÷ 800 V

These series of **S**ilicon **C**ontrolled **R**ectifier use a high performance PNPN technology.

These parts are intended for general purpose applications where high gate sensitivity is required.

Absolute Maximum Ratings, according to IEC publication No. 134

SYMBOL	PARAMETER	CONDITIONS	Value	Unit
$I_{T(RMS)}$	On-state Current	180° Conduction Angle, T_c = 110 °C	8	A
$I_{T(AV)}$	Average On-state Current	Half Cycle, Θ = 180 °, T_C = 110 °C	5	A
I_{TSM}	Non-repetitive On-State Current	Half Cycle, 60 Hz	73	A
I_{TSM}	Non-repetitive On-State Current	Half Cycle, 50 Hz	70	A
I^2t	Fusing Current	t_p = 10ms, Half Cycle	24.5	A^2s
I_{GM}	Peak Gate Current	20 µs max.	4	A
P_{GM}	Peak Gate Dissipation	20 µs max.	5	W
$P_{G(AV)}$	Gate Dissipation	20 ms max.	1	W
T_j	Operating Temperature		(-40 to + 125)	°C
T_{stg}	Storage Temperature		(-40 to + 150)	°C
T_{sld}	Soldering Temperature	10s max.	260	°C
V_{RGM}	Reverse Gate Voltage		5	V

SYMBOL	PARAMETER	CONDITIONS	VOLTAGE					Unit
			B	D	M	S	N	
V_{DRM} V_{RRM}	Repetitive Peak Off State Voltage	R_{GK} = 1 kΩ	200	400	600	700	800	V

SENSITIVE GATE SCR

Electrical Characteristics

SYMBOL	PARAMETER	CONDITIONS		SENSITIVITY	Unit
				02	
I_{GT}	Gate Trigger Current	V_D = 12 V_{DC}, R_L = 140Ω. T_j = 25 °C	MIN		µA
			MAX	200	
V_{GT}	Gate Trigger Voltage	V_D = 12 V_{DC}, R_L = 140Ω, T_j = 25 °C	MAX	1.6	V
V_{GD}	Gate Non Trigger Voltage	V_D = V_{DRM}, R_L = 3.3kΩ, R_{GK} = 220Ω T_j = 125 °C	MIN	0.1	V
V_{RGM}	Reverse Gate Voltage	I_{RG} = 10µA,	MIN	8	V
I_H	Holding Current	I_T = 50 mA, R_{GK} = 1kΩ T_j = 25 °C	MAX	5	mA
I_L	Latching Current	I_G = 1 mA, R_{GK} = 1 kΩ	MAX	6	mA
dV/dt	Critical Rate of Voltage Rise	V_D = 0.67 x V_{DRM}, R_{GK} =1 kΩ, T_j = 125 °C	MIN	5	V/µs
dI/dt	Critical Rate of Current Rise	I_G = 2 x I_{GT} tr ≤ 100 ns, f = 60 Hz, T_j = 125 °C	MIN	50	A/µs
V_{TM}	On-state Voltage	at I_T = 16 Amp, tp = 380 µs, T_j = 25 °C	MAX	1.6	V
$V_{t(0)}$	Threshold Voltage	T_j = 125 °C	MAX	0.85	V
r_d	Dynamic resistance	T_j = 125 °C	MAX	46	mΩ
I_{DRM}/I_{RRM}	Off-State Leakage Current	V_D = V_{DRM}, R_{GK} = 1kΩ T_j = 125 °C	MAX	2	m A
		V_R = V_{RRM}, T_j = 25 °C	MAX	10	m A
$R_{th(j-c)}$	Thermal Resistance Junction-Amb for DC	for AC 360° conduction angle		1.8	°C/W
$R_{th(j-a)}$	Thermal Resistance Junction-Amb for DC	S = 1 cm^2		70	°C/W

PART NUMBER INFORMATION

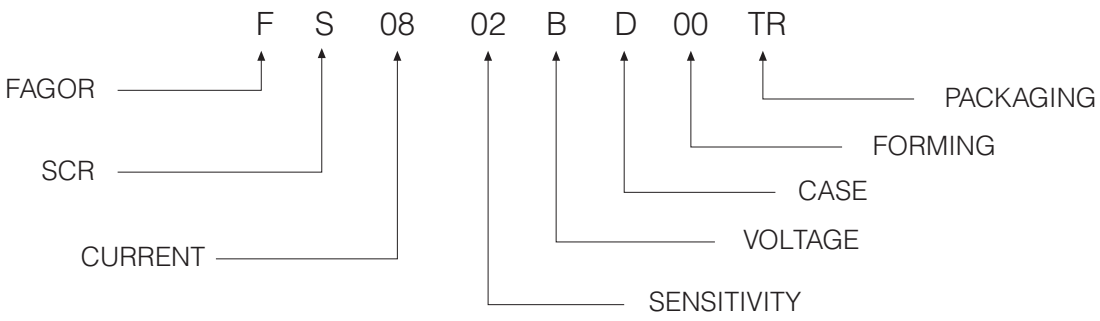


Fig. 1: Maximum average power dissipation versus average on-state current.

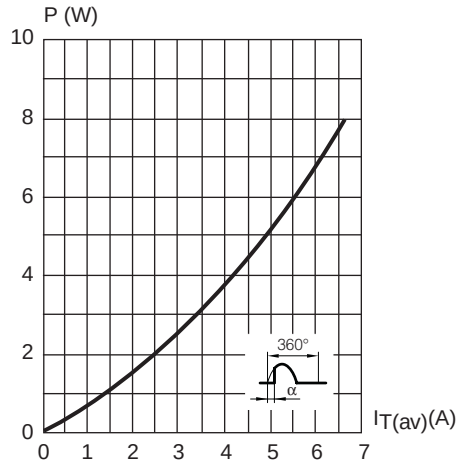


Fig. 2: Average and D.C. on-state current versus case temperature.

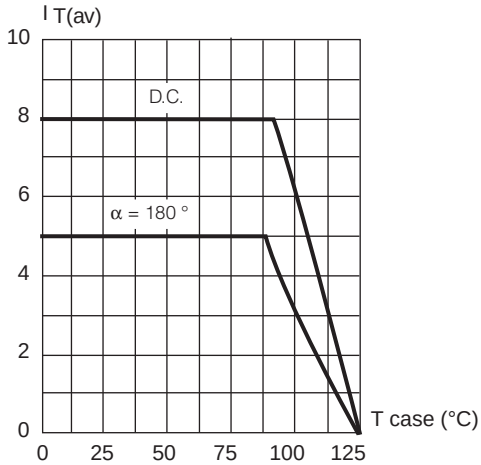


Fig. 3: Relative variation of thermal impedance junction to case versus pulse duration.

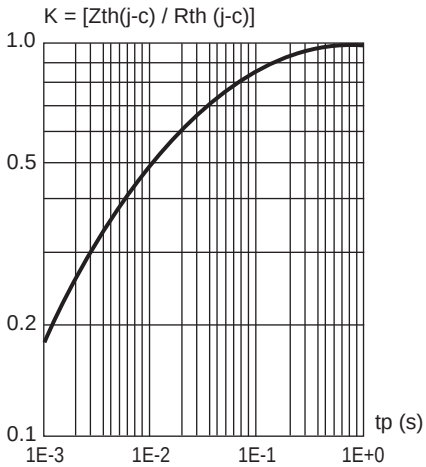


Fig. 4: Relative variation of gate trigger current, holding and latching current versus junction temperature.

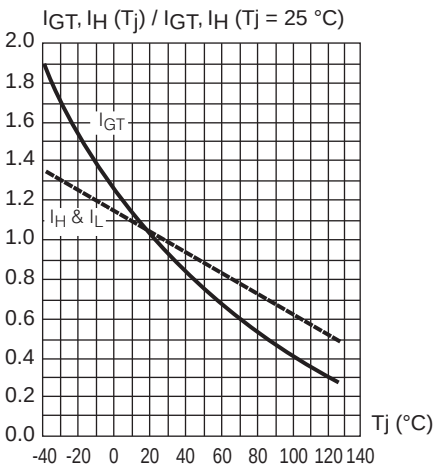


Fig. 5: Relative variation of holding current versus gate-cathode resistance (typical values).

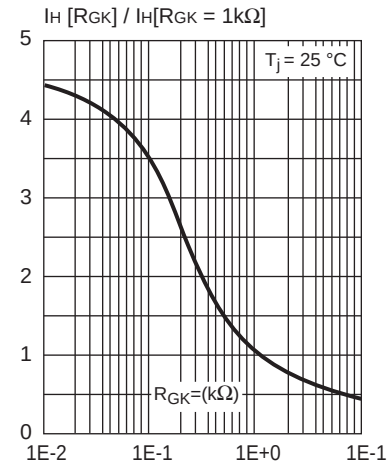


Fig. 6: Relative variation of dV/dt immunity versus gate-cathode resistance (typical values).

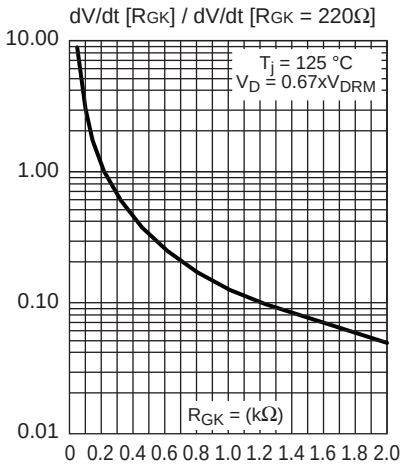


Fig. 7: Relative variation of dV/dt immunity versus gate-cathode resistance (typical values).

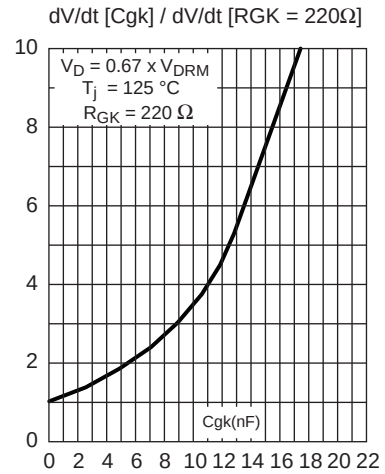


Fig. 8: Non repetitive surge peak on-state current versus number of cycles.

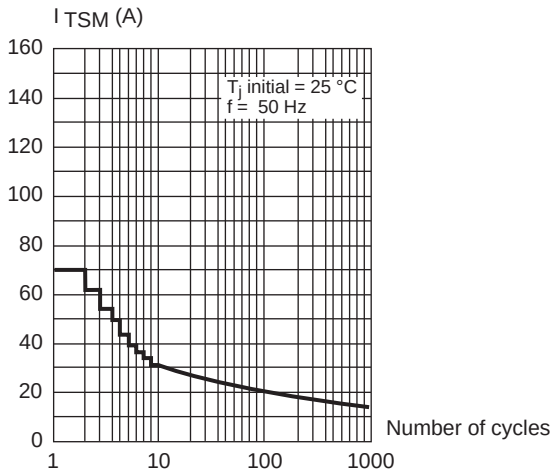


Fig. 9: Non repetitive surge peak on-state current for a sinusoidal pulse with width: tp < 10 ms, and corresponding value of I^2t.

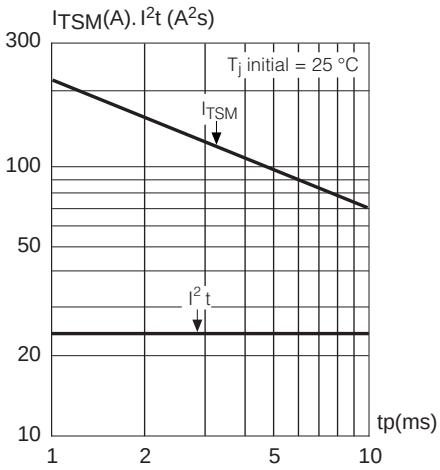
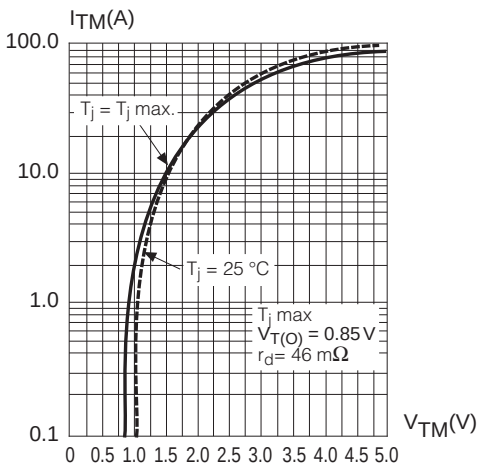
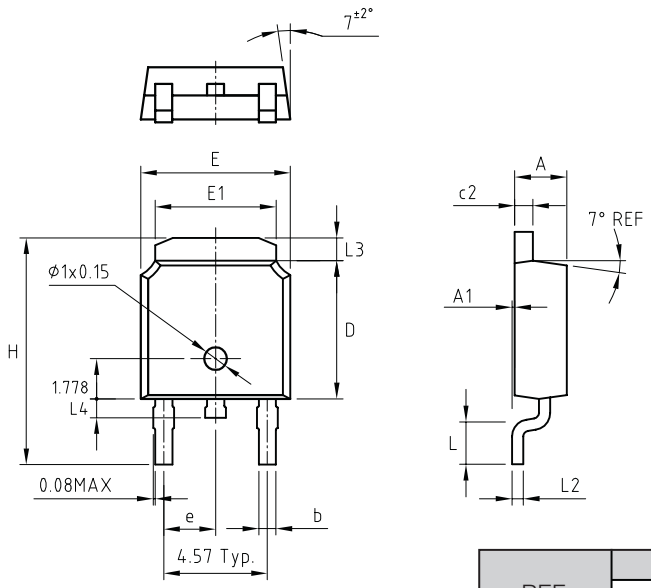


Fig. 10: On-state characteristics (maximum values).

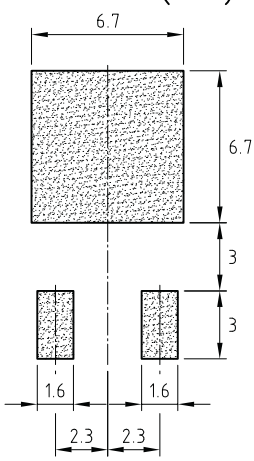


SENSITIVE GATE SCR

PACKAGE MECHANICAL DATA DPAK / TO252-AA



FOOT PRINT (mm)



REF.	DIMENSIONS		
	Milimeters		
	Min.	Nominal	Max.
A	2.18	2.3	2.39
A1	0	0.127	0.127
b	0.64	0.75	0.89
c2	0.46	0.51	0.56
D	5.97	6.1	6.22
E	6.47	6.6	6.73
E1	5.20	5.34	5.46
e		2.28BSC	
H	9.77	10.03	10.28
L	1.31	1.44	1.57
L2	0.46	0.51	0.56
L3	0.89	1.02	1.14
L4	0.51	0.76	1.02

Marking: type number
Weight: 0.2 g