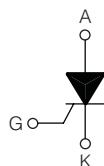
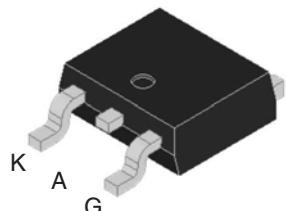


## SENSITIVE GATE SCR

### DPAK (Plastic)


**On-State Current**

4 Amp

**Gate Trigger Current**

< 200  $\mu$ A

**Off-State Voltage**

200 V ÷ 800 V

These series of **Silicon Controlled Rectifier** use a high performance PNPN technology.

These parts are intended for general purpose applications where high gate sensitivity is required.

### Absolute Maximum Ratings, according to IEC publication No. 134

SYMBOL	PARAMETER	CONDITIONS	Value	Unit
$I_{T(RMS)}$	On-state Current	180° Conduction Angle, $T_c = 110^\circ C$	4	A
$I_{T(AV)}$	Average On-state Current	Half Cycle, $\Theta = 180^\circ$ , $T_c = 110^\circ C$	2.5	A
$I_{TSM}$	Non-repetitive On-State Current	Half Cycle, 60 Hz	33	A
$I_{TSM}$	Non-repetitive On-State Current	Half Cycle, 50 Hz	30	A
$I^2t$	Fusing Current	$t_p = 10ms$ , Half Cycle	4.5	$A^2s$
$I_{GM}$	Peak Gate Current	20 $\mu$ s max.	1.2	A
$P_{GM}$	Peak Gate Dissipation	20 $\mu$ s max.	3	W
$P_{G(AV)}$	Gate Dissipation	20 ms max.	0.2	W
$T_j$	Operating Temperature		(-40 to + 125)	$^\circ C$
$T_{stg}$	Storage Temperature		(-40 to + 150)	$^\circ C$
$T_{sld}$	Soldering Temperature	10s max.	260	$^\circ C$
$V_{RGM}$	Reverse Gate Voltage		5	V

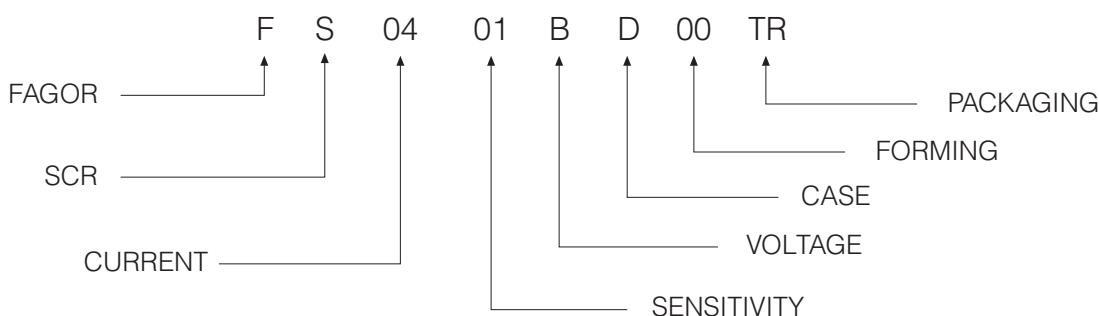
SYMBOL	PARAMETER	CONDITIONS	VOLTAGE					Unit
			B	D	M	S	N	
$V_{DRM}$	Repetitive Peak Off State Voltage	$R_{GK} = 1 k\Omega$	200	400	600	700	800	V
$V_{RRM}$								

## SENSITIVE GATE SCR

### Electrical Characteristics

SYMBOL	PARAMETER	CONDITIONS	SENSITIVITY				Unit			
			01	02	03	04				
$I_{GT}$	Gate Trigger Current	$V_D = 12 \text{ V}_{DC}, R_L = 140\Omega, T_j = 25^\circ\text{C}$	MIN	1		20	15	$\mu\text{A}$		
			MAX	20	200	200	50			
$V_{GT}$	Gate Trigger Voltage	$V_D = 12 \text{ V}_{DC}, R_L = 140\Omega, T_j = 25^\circ\text{C}$	MAX	0.8			$\text{V}$			
			MIN	0.1						
$V_{GD}$	Gate Non Trigger Voltage	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega, R_{GK} = 220 \Omega$ $T_j = 125^\circ\text{C}$		0.1			$\text{V}$			
		MIN	8							
$V_{RGM}$	Reverse Gate Voltage		$I_{RG} = 10\mu\text{A},$ $I_T = 50 \text{ mA}, R_{GK} = 1 \text{ k}\Omega, T_j = 25^\circ\text{C}$		MIN	5			$\text{mA}$	
					MAX	6				
$dV/dt$	Critical Rate of Voltage Rise	$V_D = 0.67 \times V_{DRM}, R_{GK} = 1 \text{ k}\Omega,$ $T_j = 125^\circ\text{C}$	MIN	10	5	10	10	$\text{V}/\mu\text{s}$		
			MIN	50			$\text{A}/\mu\text{s}$			
$dI/dt$	Critical Rate of Current Rise	$I_G = 2 \times I_{GT}, t_r \leq 100 \text{ ns}, f = 60 \text{ Hz},$ $T_j = 125^\circ\text{C}$			1.6					
		MAX	0.85			$\text{V}$				
$V_{TM}$	On-state Voltage		at $I_T = 8 \text{ Amp}, t_p = 380 \mu\text{s}, T_j = 25^\circ\text{C}$		90					
		MAX	1			$\text{mA}$				
$r_d$	Dynamic resistance		$T_j = 125^\circ\text{C}$		5					
		MAX	1.6			$\text{m}\Omega$				
$I_{DRM}/I_{RRM}$	Off-State Leakage Current		$V_D = V_{DRM}, R_{GK} = 1 \text{ k}\Omega$   $T_j = 125^\circ\text{C}$ $V_R = V_{RRM}, T_j = 25^\circ\text{C}$		1			$\text{mA}$		
		MAX	70							
$R_{th(j-c)}$	Thermal Resistance Junction-Amb for DC		for AC 360° conduction angle		1.6			$^\circ\text{C/W}$		
		MIN	0.85							
$R_{th(j-a)}$	Thermal Resistance Junction-Amb for DC		$S = 1 \text{ cm}^2$		70			$^\circ\text{C/W}$		
		MAX	1.6							

### PART NUMBER INFORMATION



## SENSITIVE GATE SCR

Fig. 1: Maximum average power dissipation versus average on-state current.

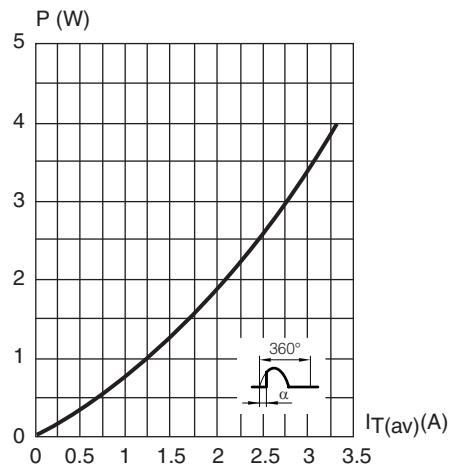


Fig. 3: Relative variation of thermal impedance junction to case versus pulse duration.

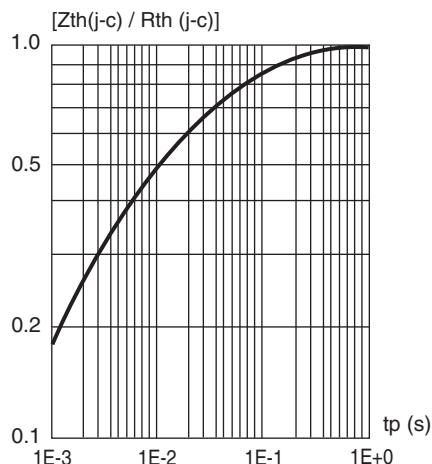


Fig. 5: Relative variation of holding current versus gate-cathode resistance (typical values).

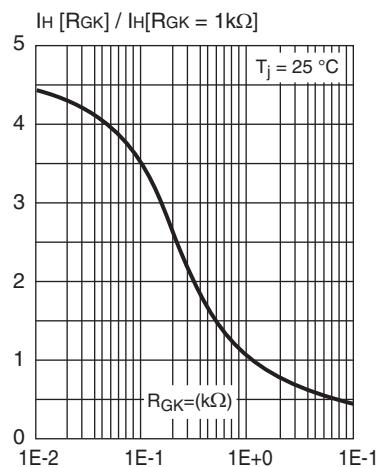


Fig. 2: Average and D.C. on-state current versus case temperature.

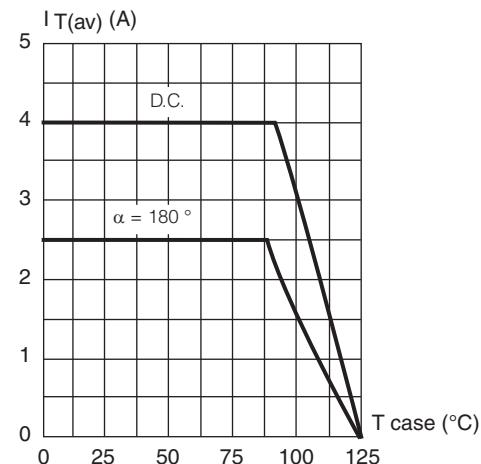


Fig. 4: Relative variation of gate trigger current, holding and latching current versus junction temperature.

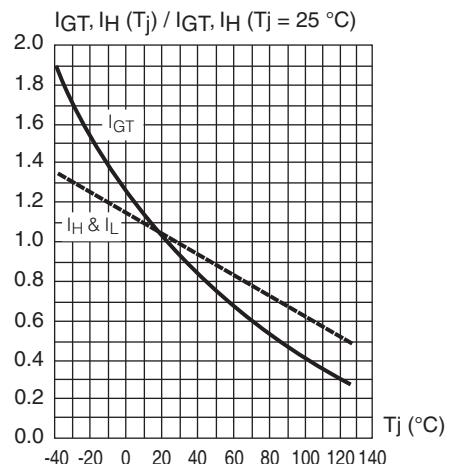
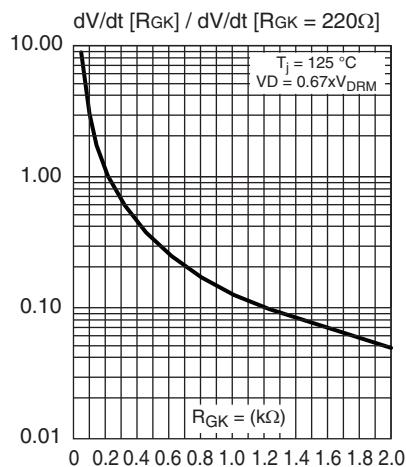


Fig. 6: Relative variation of dV/dt immunity versus gate-cathode resistance (typical values).



## SENSITIVE GATE SCR

Fig. 7: Relative variation of dV/dt immunity versus gate-cathode resistance (typical values).

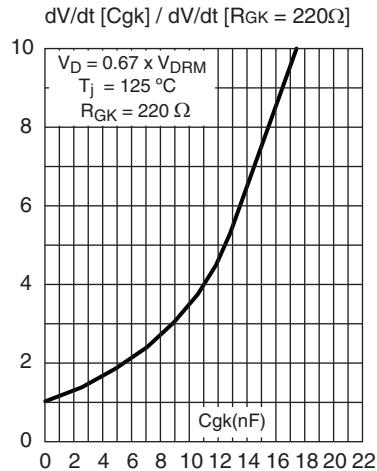


Fig. 9: Non repetitive surge peak on-state current for a sinusoidal pulse with width:  $t_p < 10$  ms, and corresponding value of  $I^2 t$ .

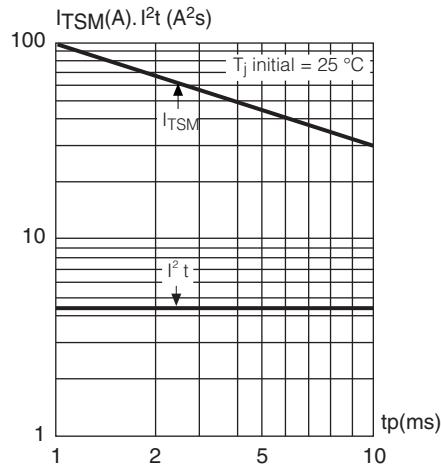


Fig. 8: Non repetitive surge peak on-state current versus number of cycles.

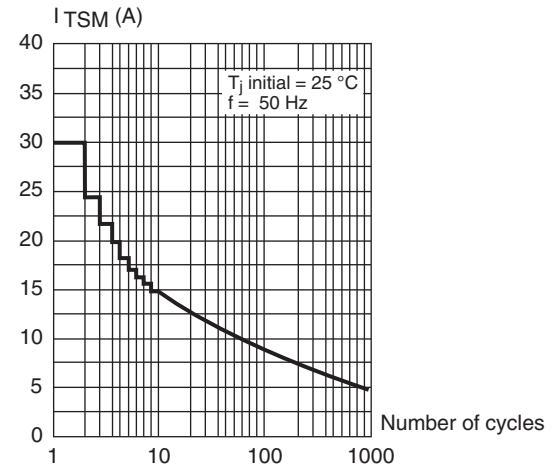
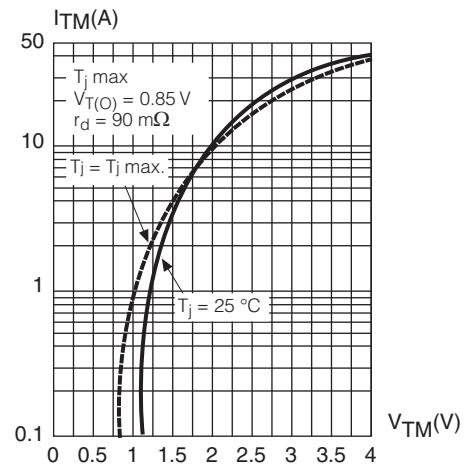


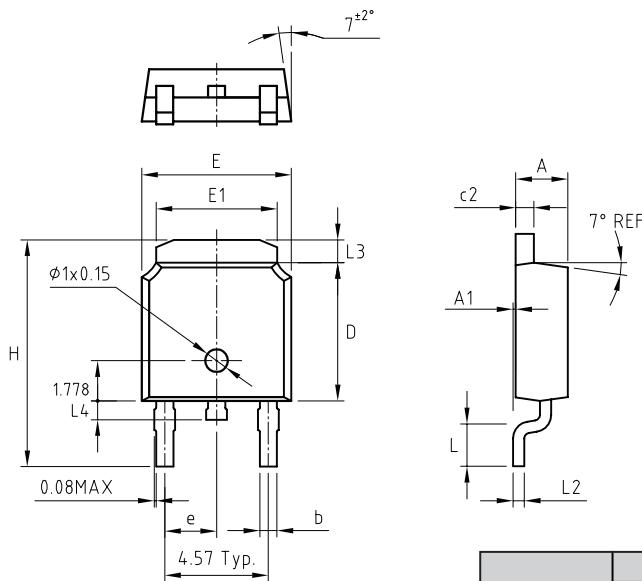
Fig. 10: On-state characteristics (maximum values).



## SENSITIVE GATE SCR

### PACKAGE MECHANICAL DATA

DPAK / TO252-AA



REF.	DIMENSIONS		
	Milimeters		
	Min.	Nominal	Max.
A	2.18	2.3	2.39
A1	0	0.127	0.127
b	0.64	0.75	0.89
c2	0.46	0.51	0.56
D	5.97	6.1	6.22
E	6.47	6.6	6.73
E1	5.20	5.34	5.46
e		2.28BSC	
H	9.77	10.03	10.28
L	1.31	1.44	1.57
L2	0.46	0.51	0.56
L3	0.89	1.02	1.14
L4	0.51	0.76	1.02

Marking: type number

Weight: 0.2 g