

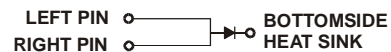
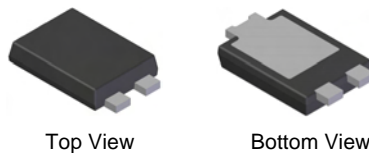
Features

- Designed as Bypass Diodes for Solar Panels
- Selectively Rated for 200°C Maximum Junction Temperature for High Thermal Reliability
- Patented Super Barrier Rectifier Technology
- Low Forward Voltage Drop
- Excellent High Temperature Stability
- **Lead Free Finish, RoHS Compliant (Note 1)**
- **Halogen and Antimony free "Green" Molding Compound (No Br, Sb)**

Mechanical Data

- Case: POWERDI[®]5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 [Ⓔ]
- Weight: 0.093 grams (approximate)

POWERDI[®]5



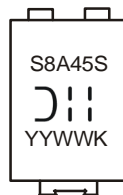
Note: Pins Left & Right must be electrically connected at the printed circuit board.

Ordering Information (Note 2)

Part Number	Case	Packaging
SBR8A45SP5-13	POWERDI [®] 5	5000/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2). All applicable RoHS exemptions applied.
 2. For packaging details, go to our website at <http://www.diodes.com>.

Marking Information



S8A45S = Product Type Marking Code
 ⌋|| = Manufacturers' code marking
 K = Factory designator
 YYWW = Date Code Marking
 YY = Last two digits of year (ex: 09 for 2009)
 WW = Week code (01 ~ 53)

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	45	V
Working Peak Reverse Voltage	V_{RWM}		
DC Blocking Voltage	V_{RM}		
Average Rectified Output Current	I_O	8	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I_{FSM}	180	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance Junction to Lead	$R_{\theta JL}$	3	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction to Case (Note 3)	$R_{\theta JC}$	8	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction to Ambient (Note 3)	$R_{\theta JA}$	102	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction to Ambient (Note 4)	$R_{\theta JA}$	60	$^\circ\text{C}/\text{W}$
Operating Temperature Range	T_J	$V_R \leq 80\% V_{RRM}$	-65 to +150
		$V_R \leq 50\% V_{RRM}$	≤ 180
		DC Forward Mode	≤ 200
Storage Temperature Range	T_{STG}	-65 to +175	$^\circ\text{C}$

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop	V_F	-	-	0.60	V	$I_F = 8\text{A}, T_J = 25^\circ\text{C}$
		-	0.52	0.57		$I_F = 8\text{A}, T_J = 125^\circ\text{C}$
Leakage Current (Note 5)	I_R	-	0.03	0.30	mA	$V_R = 45\text{V}, T_J = 25^\circ\text{C}$
		-	10	75		$V_R = 45\text{V}, T_J = 125^\circ\text{C}$

- Notes:
3. FR-4 PCB, 2oz. Copper, minimum recommended pad layout per <http://www.diodes.com>
 4. Polyimide PCB, 2oz. Copper, minimum recommended pad layout per <http://www.diodes.com>.
 5. Short duration pulse test used to minimize self-heating effect.

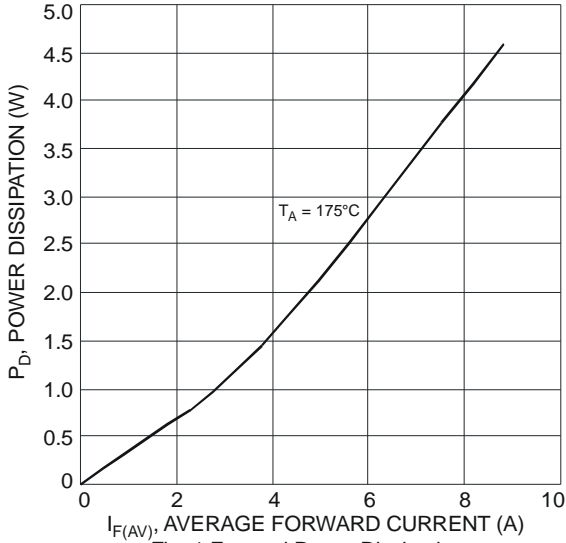


Fig. 1 Forward Power Dissipation

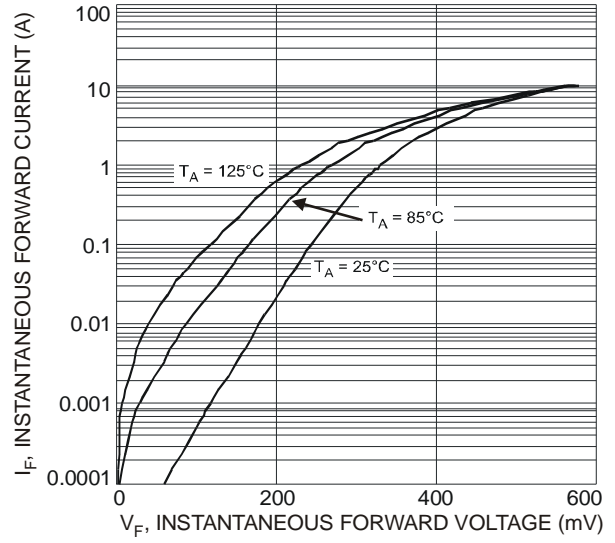


Fig. 2 Typical Forward Characteristics

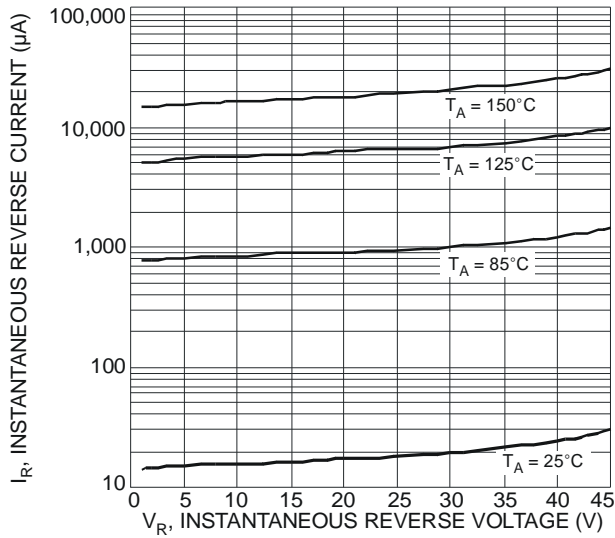


Fig. 3 Typical Reverse Characteristics

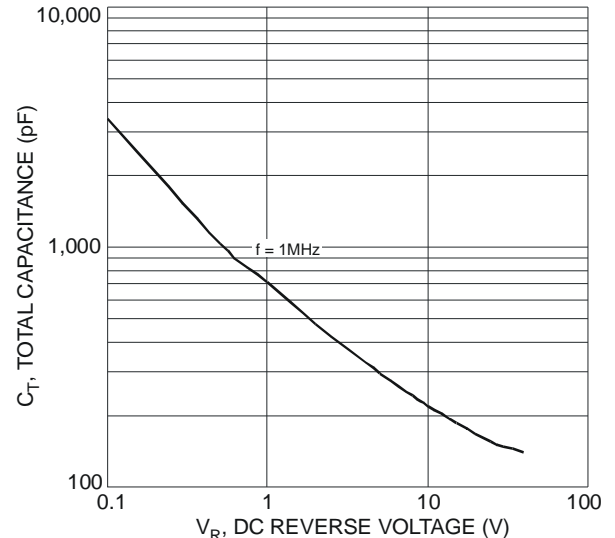


Fig. 4 Total Capacitance vs. Reverse Voltage

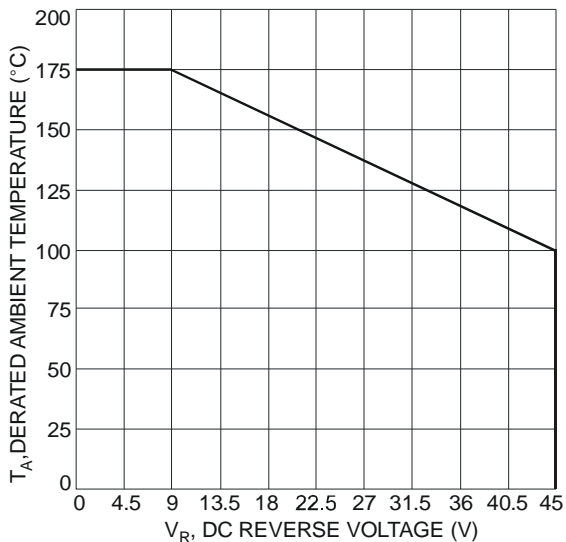


Fig. 5 Operating Temperature Derating

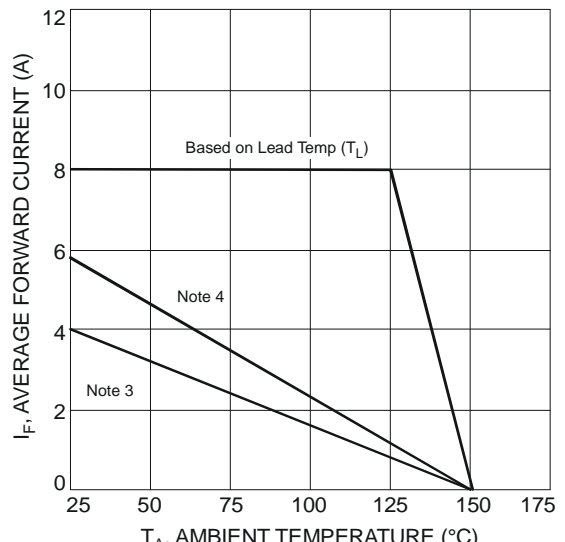
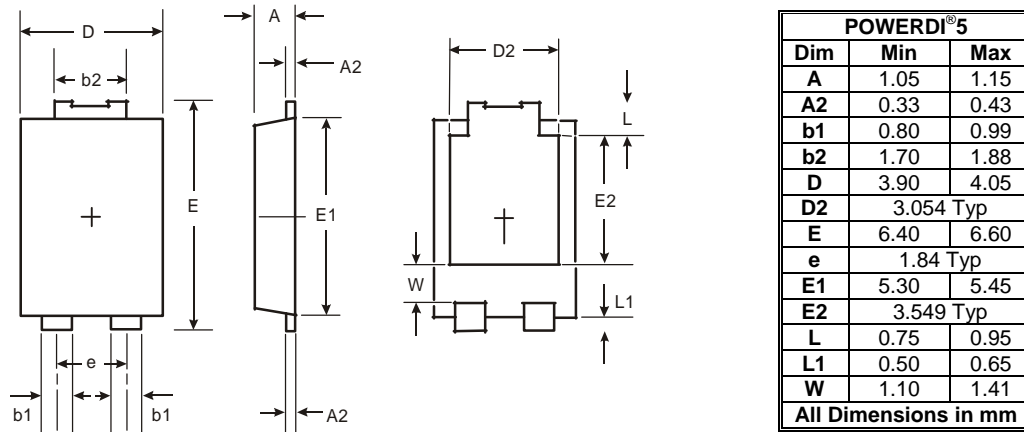
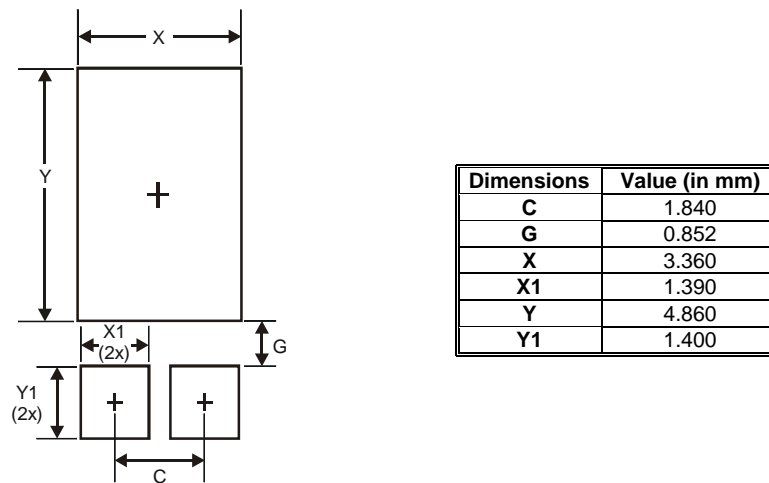


Fig. 6 Forward Current Derating Curve

Package Outline Dimensions



Suggested Pad Layout



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