



SBR2A40SA

2.0A SBR[®] SUPER BARRIER RECTIFIER SMA

Features

- Low Leakage Current
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- Lead Free Finish, RoHS Compliant (Note 1)
- Green Molding Compound (No Halogen and Antimony)
 (Note 2)

Mechanical Data

- Case: SMA
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Polarity Indicator: Cathode Band
- Weight: 0.064 grams (approximate)



Top View



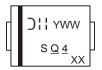
Ordering Information (Note 3)

Part Number	Case	Packaging
SBR2A40SA-13	SMA	5000/Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes. 2. No purposefully added lead. Halogen and Antimony Free.

3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



 $\begin{array}{l} S \ \underline{Q} \ \underline{4} = \mbox{Product Type Marking Code} \\ O(1) = \ \mbox{Manufacturers' code marking} \\ YWW = \ \mbox{Date Code Marking} \\ Y = \ \mbox{Last digit of year (ex: 9 for 2009)} \\ WW = \ \mbox{Week code (01 - 53)} \end{array}$



Maximum Ratings @T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.			
Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} Vrwm V _{RM}	40	V
Average Rectified Output Current (See Figure 1)	lo	2	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	15	А

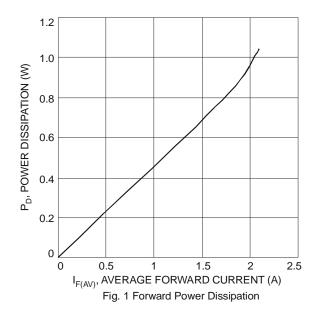
Thermal Characteristics

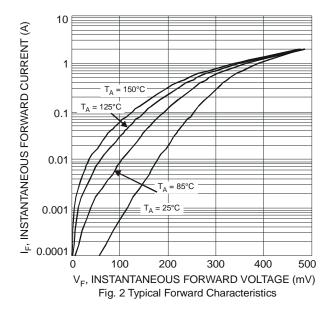
Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 4)	R _{θJA}	110	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop	V _F	-	-	0.55 0.50	V	I _F = 2.0A, T _J = 25°C I _F = 1.0A, T _J = 25°C
Leakage Current (Note 5)	I _R	-	-	500 100	· ·	V _R = 40V, T _J = 25°C V _R = 40V, T _J = 125°C

Notes: 4. Device mounted on Polymide substrate, with 1" x 1", 2 oz. Copper, double-sided PCB board. 5. Short duration pulse test used to minimize self-heating effect.



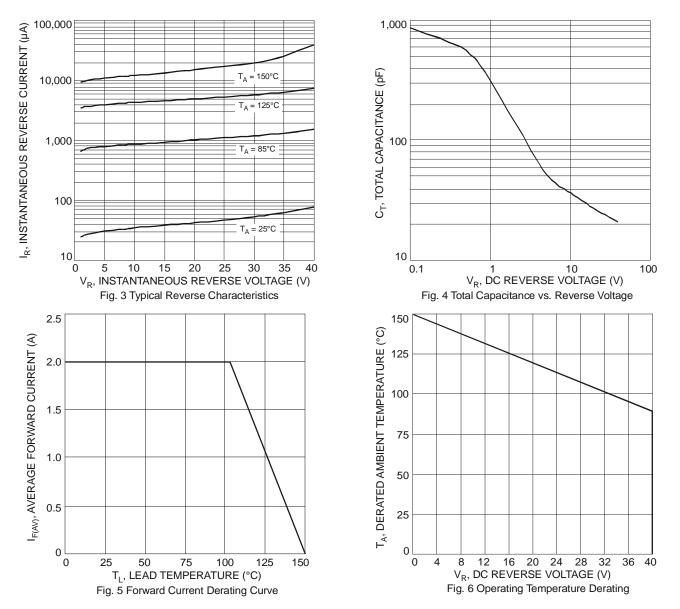


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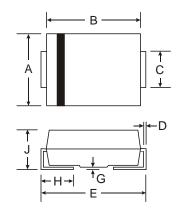
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Package Outline Dimensions



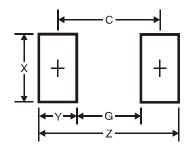
SMA				
Dim	Min	Max		
Α	2.29	2.92		
В	4.00	4.60		
С	1.27	1.63		
D	0.15	0.31		
E	4.80	5.59		
G	0.05	0.20		
Н	0.76	1.52		
J	2.01	2.30		
All Dim	All Dimensions in mm			

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Suggested Pad Layout



Dimensions	Value (in mm)
Z	6.5
G	1.5
Х	1.7
Y	2.5
С	4.0

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