

UTC UNISONIC TECHNOLOGIES CO., LTD

BTB20 **Preliminary TRIAC**

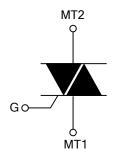
20A TRIACS

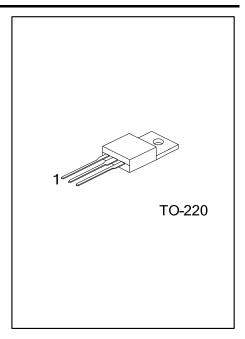
DESCRIPTION

The UTC BTB20 is a 20A triacs, it uses UTC's advanced technology to provide customers with glass passivation, a superior performance in surge current handling and voltage insulated tab,

The UTC BTB20 is suitable for static switching on inductive or resistive load and phase control application.

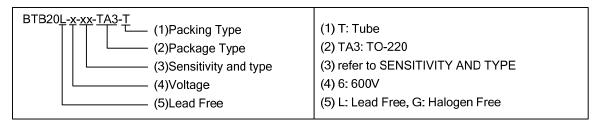






ORDERING INFORMATION

| Ordering | Dookogo | Pin | Assignn | Doolsing | | | |
|-------------------|-------------------|---------|---------|----------|---|---------|--|
| Lead Free | Halogen Free | Package | 1 | 2 | 3 | Packing | |
| BTB20L-x-xx-TA3-T | BTB20G-x-xx-TA3-T | TO-220 | MT1 | MT2 | G | Tube | |



SENSITIVITY AND TYPE

| PART NUMBER | VOLTAGE | SENSITIVITY | TYPE | | |
|-------------|---------|-------------|-------------|--|--|
| CW | 600V | 35mA | SNUBBERLESS | | |

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ABSOLUTE MAXIMUM RATINGS

| PARAMETER | | | SYMBOL | RATINGS | UNIT |
|---|---|-------------------------------|---------------------|---|--------|
| RMS On-State Current (Full Sine Wave) | | T _C =70°C | I _{T(RMS)} | 20 | Α |
| Non Repetitive Surge Peak On-State Current | F=50 Hz | t=10ms | I _{TSM} | 210 | Α |
| (Full Cycle, T _J initial=25°C) | F=60 Hz | t=8.3ms | | 200 | Α |
| I ² t Value for Fusing | t _P =10ms | | l ² t | 200 | A^2s |
| Critical Rate of Rise of | Repetitive, F=50 Hz | | | 50 | A/μs |
| On-State Current I _G =500mA, dI _G /dt =1A/µs | Non Repetitive | T _J =125°C dl/dt | | 100 | A/μs |
| Non Repetitive Surge Peak Off-State Voltage | t _P =10ms T _J =25°C | | V_{DSM}/V_{RSM} | V _{DSM} /V _{RSM} +100 | V |
| Peak Gate Current | t _P =20µs | T _J =125°C | I_{GM} | 4 | Α |
| Peak Positive Gate Voltage t _P =20µs | | V_{GM} | 16 | V | |
| Average Gate Power Dissipation T _J =125°C | | $P_{G(AV)}$ | 1 | W | |
| Operating Junction Temperature | | | T_J | -40~+125 | °C |
| Storage Junction Temperature | | | T_{STG} | -40~+150 | °C |

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

| PARAMETER | SYMBOL | RATINGS | UNIT |
|-----------------------|---------------|---------|------|
| Junction to Ambient | θ_{JA} | 60 | °C/W |
| Junction to Case (AC) | | 1.3 | °C/W |
| Junction to Case (DC) | θ_{JC} | 1.7 | °C/W |

■ ELECTRICAL CHARACTERISTICS (T_J =25°C unless otherwise specified.)

| DARAMETER | 0) (1 1 1 0 1 | TEST CONDITIONS | | BW | | CW | | | | |
|--|----------------|---|----------|-----|-----|-----|-----|-----|-----|------|
| PARAMETER | SYMBOL | | | MIN | TYP | MAX | MIN | TYP | MAX | UNIT |
| Gate Trigger Current (Note 1) | I_{GT} | V _D =12V, R _L =33Ω | ALL | 2 | | 50 | 1 | | 35 | mA |
| Gate Trigger Voltage | V_{GT} | | ALL | | | 1.5 | | | 1.5 | V |
| Gate Non-Trigger Voltage | V_{GD} | $V_D = V_{DRM}$, $R_L = 3.3k\Omega$, $T_J = 125^{\circ}C$ | ALL | 0.2 | | | 0.2 | | | > |
| Holding Current (Note 2) | I _H | I _T =500mA, Gate Open | | | | 75 | | | 50 | mA |
| | | | 1-111 | | 50 | | | | | mA |
| Latching Current | IL | I _G =1.2I _{GT} | II | | 90 | | | | | mA |
| | | | 1-11-111 | | | | | | 80 | mA |
| Critical Rate of Rise of Off-State Voltage (Note 2) | dV/dt | V _D =67%V _{DRM} , Gate Open, T _J =125°C | | 500 | 750 | | 250 | 500 | | V/µs |
| Critical Rate of Rise of Off-State Voltage at Commutation (Note 2) | (dV/dt)c | (dl/dt)c=20A/ms, T _J =125°C | | 18 | 36 | | 11 | 22 | | V/µs |

■ STATIC CHARACTERISTICS

| PARAMETER | SYMBOL | TEST CONDITIONS | | MIN | TYP | MAX | UNIT |
|--------------------------------|------------------|---|-----------------------|-----|-----|------|------|
| Peak On-State Voltage (Note 2) | V _{TM} | I _{TM} =28A, t _p =380μs | T _J =25°C | | | 1.70 | ٧ |
| Repetitive Peak Off-State | I_{DRM} | \/ -\/ | T _J =25°C | | | 10 | μΑ |
| Current | I _{RRM} | V _{DRM} =V _{RRM} | T _J =125°C | | | 3 | mA |

Notes: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

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