



PRELIMINARY

SOLID STATE DEVICES, INC.

14005 Stage Road * Santa Fe Springs, Ca 90670
Phone: (562) 404-4474 * Fax: (562) 404-1773

SFS2004/59

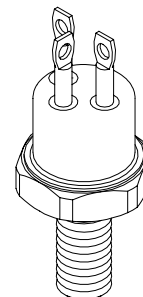
**10 AMP
800 VOLTS
HIGH VOLTAGE THYRISTOR**

DESIGNER'S DATA SHEET

FEATURES:

- **Designed for Pulse Modulators in Radar Applications.**
- **High Surge Current, 100A.**
- **High Blocking Voltage, 600V min.**
- **High dv/dt, 200V/us min.**
- **di/dt = 100A/us.**
- **Fast Switching Time.**
- **Isolated Stud.**
- **Hermetically sealed.**

TO-59



MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	VALUE	UNIT
Peak Repetitive Forward Blocking Voltage	V _{DRM}	800	Volts
Peak Repetitive Reverse Blocking Voltage	V _{RDM}	600	Volts
RMS On-State Current (All Condition Angles, T _C = 85°C max)	I _{T(RMS)}	10	Amps
Peak Repetitive Surge Current (One Cycle, 60Hz, Pulse width 2µsec, Duty Cycle 0.6%, T _C = 85°C max)	I _{TFM (REP)}	100	Amps
Peak Gate Power	P _{GM}	20	Watts
Average Gate Power (Pulse width 2µsec)	P _{G (AV)}	1.0	Watts
Peak Gate Current	I _{GM}	5.0	Amps
Peak Gate Voltage	V _{GM}	10	Volts
Operating Junction Temperature Range	T _J	-65 TO +105	°C
Storage Temperature Range	T _{STG}	-65 TO +200	°C
Thermal Resistance Junction to Case	Θ _{JC}	3.0	°C/W

NOTE: All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: SCR0002A

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ELECTRICAL CHARACTERISTICS @ T_J=25°C (Unless Otherwise Specified)

RATING		SYMBOL	MIN	MAX	UNIT
Peak Reverse Blocking Current (Rated V _R RM)	T _J = 25°C T _J = 105°C	I_{RRM}	-	0.5 2.0	mA
Peak Forward Blocking Current (Rated V _D RM)	T _J = 25°C T _J = 105°C	I_{DRM}	-	0.5 2.0	mA
Forward On-State Voltage (I _F = 2.0A Peak, t = 1ms, Duty Cycle ≤ 1%)		V_F	-	1.50	Volts
Gate Trigger Current (V _A = 7Vdc, R _L = 100Ω)	T _J = 25°C T _J = 105°C	I_{GT}	- -	50 100	mA
Gate Trigger Voltage (V _A = 7Vdc, R _L = 100Ω)	T _J = 25°C T _J = -65°C T _J = 105°C	V_{GT}	- - 0.2	1.5 2.5 -	Volts
Holding Current (V _A = 7Vdc, R _{KG} - Open, T _J = 105°C)		I_H	0.2	-	mA
Switch Time (I _F = 30A min Pulse; I _R = 5A; T _C = 85°C; dV/dt = 250V/μs to 600V; V _{RA(OFF)} = 0V; V _{RG(OFF)} = 6V)	Delay Time Rise Time Turn Off Time	t_d t_r t_{off}	- - 0.2	1.5 2.5 -	μsec

PACKAGE OUTLINE: TO-59

PIN OUT:

1. CATHODE
2. GATE
3. ANODE

