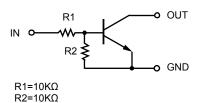


## **Digital Transistor(built-in resistors)**

#### **Feature**

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the device design easy.



### **Applications**

- Inverter
- Interface
- Driver

#### **Mechanical Characteristics**

Lead finish:100% matte Sn(Tin)

Mounting position: Any

➤ Qualified max reflow temperature:260 °C

Device meets MSL 1 requirements

➤ Pure tin plating: 7 ~ 17 um

Pin flatness:≤3mil

#### **Structure**

NPN epitaxial planar silicon transistor (Resistor built-in type)

#### Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
logut voltage	$V_{I(off)}$	V <sub>CC</sub> =5V,I <sub>O</sub> =100μA	-	-	0.5	V
Input voltage	V <sub>I(on)</sub>	V <sub>O</sub> =0.3V,I <sub>O</sub> =10mA	3	-	-	V
Output voltage	$V_{O(off)}$	I <sub>O</sub> /I <sub>I</sub> =10mA/0. 5mA	-	0.1	0.3	V
Input current	I <sub>I</sub>	V <sub>I</sub> =5V	-	-	0.88	mA
Output current	I <sub>O(off)</sub>	V <sub>CC</sub> =50V, V <sub>I</sub> =0V	-	-	0.5	μA
DC current gain	G <sub>1</sub>	V <sub>O</sub> =5V, I <sub>O</sub> =5mA	30	-	-	-
Input resistance	R <sub>1</sub>	-	7	10	13	ΚΩ
Resistance ration	R <sub>2</sub> /R <sub>1</sub>	-	0.8	1	1.2	-
Transition frequency	f⊤	V <sub>CE</sub> =10V, I <sub>E</sub> = -5mA, f=100MHz	-	250	-	MHz

## Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Supply voltage	Vcc	50	V
Input voltage	V <sub>IN</sub>	-10 to +40	V
Output surrent	Io	50	mA
Output current	I <sub>C(MAX.)</sub>	100	mA
Power dissipation	P <sub>d</sub>	150	mW
Junction temperature	Tj	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	$^{\circ}$ C

## **Typical Characteristics**

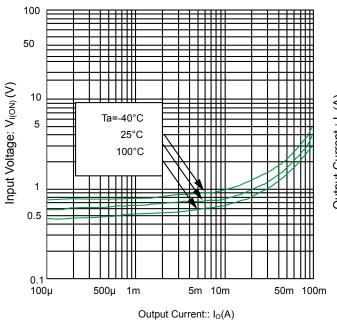


Fig 1.Input Voltage vs. output current @V<sub>C</sub>=0.3V (ON characteristics)

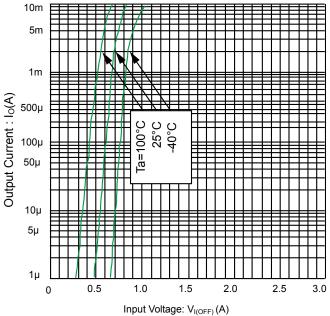
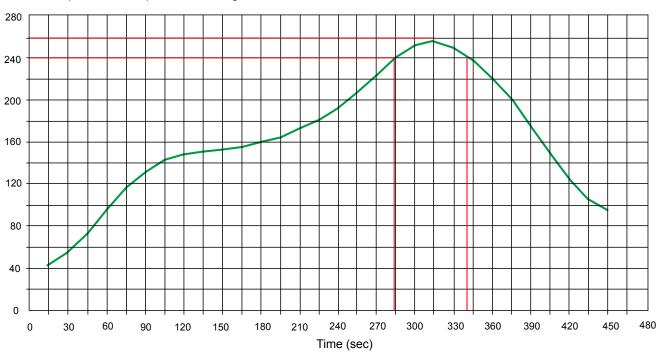


Fig 2.Output current vs. input voltage @V<sub>CC</sub>=5V(OFF characteristics)

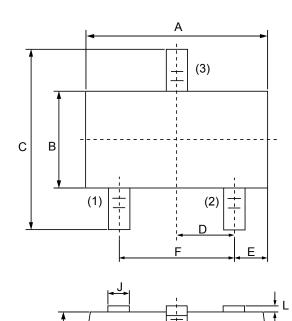
### **Solder Reflow Recommendation**

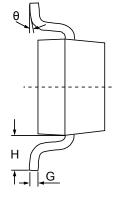
Peak Temp=257℃, Ramp Rate=0.802deg. ℃/sec



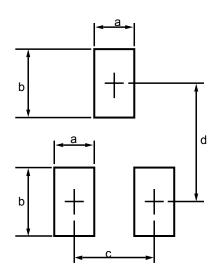
## **Product dimension (SOT-523)**

Κ





Dim	Millimeters		Inches		
Dim	MIN	MAX	MIN	MAX	
А	1.50	1.70	0.059	0.067	
В	0.75	0.85	0.030	0.033	
С	1.450	1.750	0.057	0.069	
D	0.50BSC		0.020BSC		
E	0.30	0.33	0.012	0.015	
F	0.900	1.100	0.035	0.043	
G	0.100	0.200	0.004	0.008	
Н	0.550		0.022		
J	0.150	0.250	0.006	0.010	
К	0.700	0.900	0.028	0.038	
L	0.024	0.027	0.600	0.700	
θ	0°	4°	0°	4°	



Dim	Millimeters			
Dilli	MIN	MAX		
а	-	0.4		
b	1	0.6		
С	-1	1.0		
d	ı	1.24		

# Ordering information

Device	Package	Shipping
PDTC114EE	SOT-523 (Pb-Free)	3000 / Tape & Reel

Rev.06 4 www.prisemi.com

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