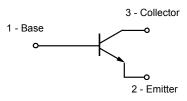


Feature





PT23T5551

Transistor

Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- > Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness:≤3mil

Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

Electrical characteristics per line@25°C(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Collector-Emitter Breakdown Voltage	V (BR)CEO	I _C =1.0mA,I _B =0	160			V
Collector-Base Breakdown Voltage	V (BR)CBO	I _C =100uA,I _E =0	180			V
Emitter -Base Breakdown Voltage	V (BR)EBO	I _E =10uA,I _C =0	6.0			V
Collector Cutoff Current	I _{CBO}	V _{CB} =120V			50	nA
Emitter Cutoff Current	I _{EBO}	V _{EB} =4V			50	nA
Collector Current	Ι _C			0.6		А

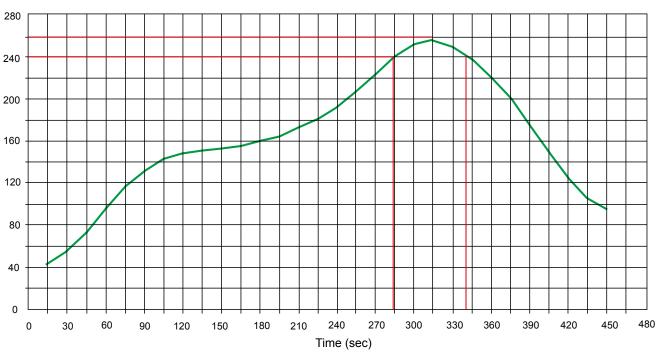
Absolute maximum rating@25℃

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units	
	H _{FE}	I _C =1mA,V _{CE} =5.0V	80		250		
DC Current Gain		I _C =10mA,V _{CE} =5.0V	80		250	-	
		I _C =50mA,V _{CE} =5.0V	30		250		
Collector-Emitter Saturation Voltage	V _{CE(sat)}	I _C =10mA,I _B =1.0mA	-		0.15	N/	
		I _C =50mA,I _B =5.0mA	-		0.2	V	
Base-Emitter Saturation Voltage	$V_{BE(sat)}$	I _C =10mA,I _B =1.0mA			1		
		I _C =50mA,I _B =5.0mA			1	V	

Transistor

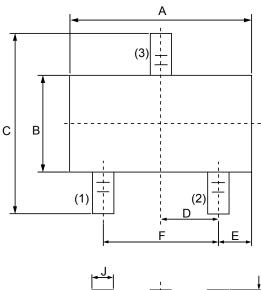
PT23T5551

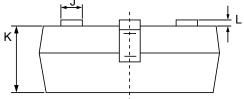
Solder Reflow Recommendation

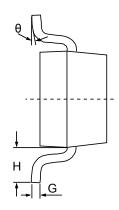


Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

Product dimension(SOT-23)



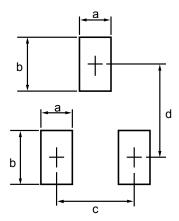




Transistor

PT23T5551

Dim	Millim	neters	Inches		
Dim	MIN	МАХ	MIN	МАХ	
А	2.80	3.00	0.1102	0.1197	
В	1.20	1.40	0.0472	0.0551	
С	2.10	2.50	0.0830	0.0984	
D	0.89	1.02	0.0350	0.0401	
E	0.45	0.60	0.0177	0.0236	
F	1.78	2.04	0.0701	0.0807	
G	0.085	0.177	0.0034	0.0070	
н	0.45	0.60	0.0180	0.0236	
J	0.37	0.50	0.0150	0.0200	
к	0.89	1.11	0.0350	0.0440	
L	0.013	0.100	0.0005	0.0040	
θ	0°	10°	0°	10°	



Dim	Millimeters			
Dim	MIN	MAX		
а		0.7		
b	-	1.2		
С		2.04		
d		2.2		

Ordering information

Device	ce Package Shipping	
PT23T5551	SOT-23 (Pb-Free)	3000 / Tape & Reel

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