



CHENMKO ENTERPRISE CO.,LTD

Halogens free devices

SURFACE MOUNT
N-Channel Enhancement Mode Field Effect Transistor
VOLTAGE 20 Volts CURRENT 20 Ampere

CHM21A2PAGP

APPLICATION

- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

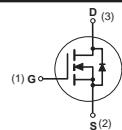
FEATURE

- * Small package. (TO-252)
- * Super high dense cell design for extremely low R_{DS(ON)}.
- * High power and current handing capability.

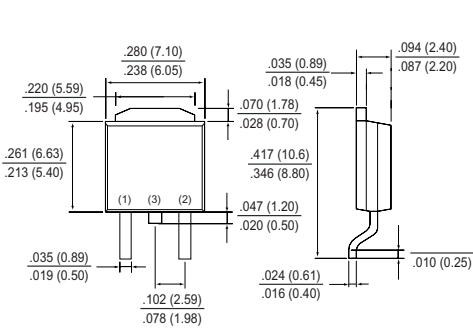
CONSTRUCTION

- * N-Channel Enhancement

CIRCUIT



D-PAK(TO-252)



1 Gate
2 Source
3 Drain(Heat Sink)

Dimensions in inches and (millimeters)

TO-252

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

Symbol	Parameter	CHM21A2PAGP	Units
V _{DSS}	Drain-Source Voltage	20	V
V _{GSS}	Gate-Source Voltage	±12	V
I _D	Maximum Drain Current - Continuous	20	A
	- Pulsed (Note 3)	60	
P _D	Maximum Power Dissipation at T _c = 25°C	38	W
T _J	Operating Temperature Range	-55 to 150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

3. Repetitive Rating , Pulse width limited by maximum junction temperature

4. Guaranteed by design , not subject to production testing

Thermal characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 1)	50	°C/W
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2006-02

ELECTRICAL CHARACTERISTIC (CHM21A2PAGP)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 20 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			1	μA
I_{GSSF}	Gate-Body Leakage	$V_{\text{GS}} = 12 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{\text{GS}} = -12 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$	0.5		1.5	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}} = 4.5 \text{ V}, I_D = 8 \text{ A}$		30	40	$\text{m}\Omega$
		$V_{\text{GS}} = 2.5 \text{ V}, I_D = 6.6 \text{ A}$		55	70	
g_{FS}	Forward Transconductance	$V_{\text{DS}} = 10 \text{ V}, I_D = 8 \text{ A}$		15		S

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{\text{DS}} = 10 \text{ V}, I_D = 8 \text{ A}$ $V_{\text{GS}} = 4.5 \text{ V}$		11	15	nC
Q_{gs}	Gate-Source Charge			3.6		
Q_{gd}	Gate-Drain Charge			2.8		
t_{on}	Turn-On Time	$V_{\text{DD}} = 10 \text{ V}$ $I_D = 1 \text{ A}, V_{\text{GS}} = 4.5 \text{ V}$ $R_{\text{GEN}} = 6 \Omega$		20	50	nS
t_r	Rise Time			12	30	
t_{off}	Turn-Off Time			50	100	
t_f	Fall Time			10	25	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_s	Drain-Source Diode Forward Current	(Note 1)			20	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_s = 4 \text{ A}, V_{\text{GS}} = 0 \text{ V}$ (Note 2)			1.3	V