

Halogens free devices



CHENMKO ENTERPRISE CO.,LTD

SURFACE MOUNT
N-Channel Enhancement Mode Field Effect Transistor
VOLTAGE 60 Volts CURRENT 3 Ampere

CHM6426XGP

APPLICATION

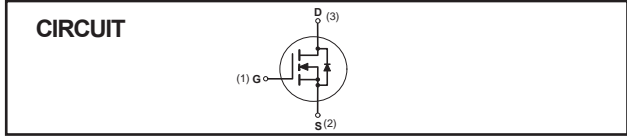
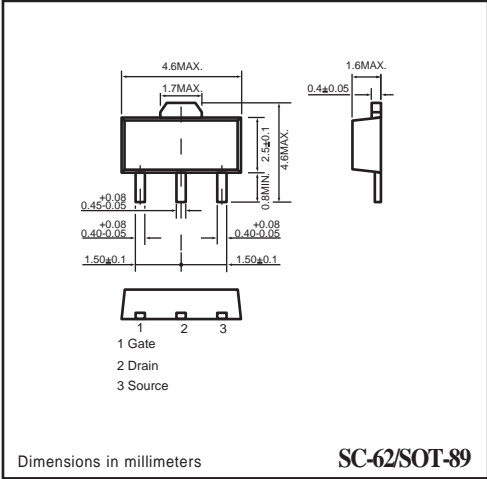
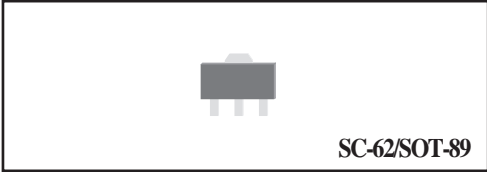
- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

FEATURE

- * Small package. (SC-62/SOT-89)
- * High density cell design for extremely low $R_{DS(ON)}$.
- * Rugged and reliable.

CONSTRUCTION

- * N-Channel Enhancement



Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | CHM6426XGP | Units |
|-----------|------------------------------------|------------|------------------|
| V_{DSS} | Drain-Source Voltage | 60 | V |
| V_{GSS} | Gate-Source Voltage | ± 20 | V |
| I_D | Maximum Drain Current - Continuous | 3 | A |
| | - Pulsed (Note 3) | 12 | |
| P_D | Maximum Power Dissipation | 1300 | mW |
| T_J | Operating Temperature Range | -55 to 150 | $^\circ\text{C}$ |
| T_{STG} | Storage Temperature Range | -55 to 150 | $^\circ\text{C}$ |

Note : 1. Surface Mounted on FR4 Board , $t \leq 10\text{sec}$
 2. Pulse Test , Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$
 3. Repetitive Rating , Pulse width limited by maximum junction temperature
 4. Guaranteed by design , not subject to production trsting

Thermal characteristics

| | | | |
|-----------------|--|-----|--------------------|
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 1) | 100 | $^\circ\text{C/W}$ |
|-----------------|--|-----|--------------------|

ELECTRICAL CHARACTERISTIC (CHM6426XGP)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Conditions | Min | Typ | Max | Units |
|--------|-----------|------------|-----|-----|-----|-------|
|--------|-----------|------------|-----|-----|-----|-------|

OFF CHARACTERISTICS

| | | | | | | |
|------------|---------------------------------|---|----|--|------|---------------|
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{GS} = 0\text{ V}, I_D = 250\ \mu\text{A}$ | 60 | | | V |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{DS} = 60\text{ V}, V_{GS} = 0\text{ V}$ | | | 1 | μA |
| I_{GSSF} | Gate-Body Leakage | $V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$ | | | +100 | nA |
| I_{GSSR} | Gate-Body Leakage | $V_{GS} = -20\text{ V}, V_{DS} = 0\text{ V}$ | | | -100 | nA |

ON CHARACTERISTICS (Note 2)

| | | | | | | |
|--------------|-----------------------------------|---|---|----|-----|------------|
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$ | 1 | | 3 | V |
| $R_{DS(on)}$ | Static Drain-Source On-Resistance | $V_{GS}=10\text{V}, I_D=3\text{A}$ | | 75 | 90 | m Ω |
| | | $V_{GS}=4.5\text{V}, I_D=2.4\text{A}$ | | 88 | 110 | |

Dynamic Characteristics

| | | | | | | |
|-----------|------------------------------|--|--|-----|--|----|
| C_{iss} | Input Capacitance | $V_{DS} = 25\text{V}, V_{GS} = 0\text{V},$ $f = 1.0\text{ MHz}$ | | 665 | | pF |
| C_{oss} | Output Capacitance | | | 75 | | |
| C_{rss} | Reverse Transfer Capacitance | | | 40 | | |

SWITCHING CHARACTERISTICS (Note 4)

| | | | | | | |
|-----------|--------------------|--|--|------|------|----|
| Q_g | Total Gate Charge | $V_{DS}=30\text{V}, I_D=3\text{A}$ $V_{GS}=10\text{V}$ | | 13.4 | 17.8 | nC |
| Q_{gs} | Gate-Source Charge | | | 1.7 | | |
| Q_{gd} | Gate-Drain Charge | | | 2.8 | | |
| t_{on} | Turn-On Time | $V_{DD}= 30\text{V}$ $I_D = 1.0\text{A}, V_{GS} = 10\text{ V}$ $R_{GEN} = 6\ \Omega$ | | 13 | 26 | nS |
| t_r | Rise Time | | | 4 | 8 | |
| t_{off} | Turn-Off Time | | | 33 | 66 | |
| t_f | Fall Time | | | 3 | 6 | |

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

| | | | | | | |
|----------|------------------------------------|---|--|--|-----|---|
| I_S | Drain-Source Diode Forward Current | (Note 1) | | | 3.0 | A |
| V_{SD} | Drain-Source Diode Forward Voltage | $I_S = 3\text{ A}, V_{GS} = 0\text{ V}$ | | | 1.1 | V |

RATING CHARACTERISTIC CURVES (CHM6426XGP)

Typical Electrical Characteristics

Figure 1. Output Characteristics

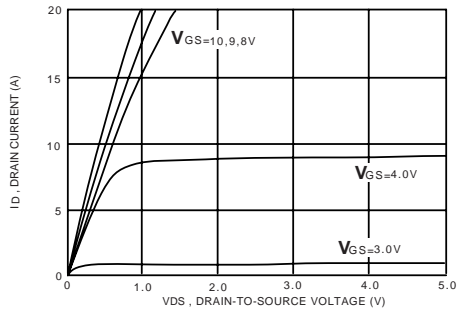


Figure 2. Transfer Characteristics

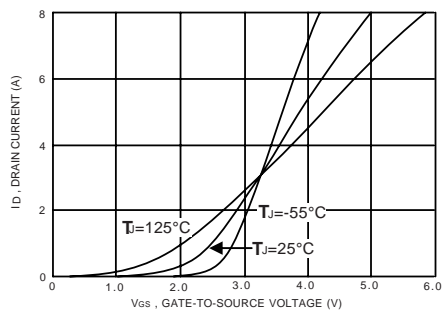


Figure 3. Gate Charge

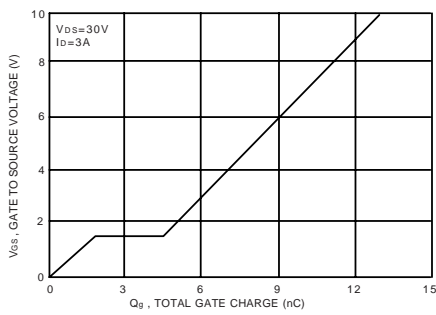


Figure 4. On-Resistance Variation with Temperature

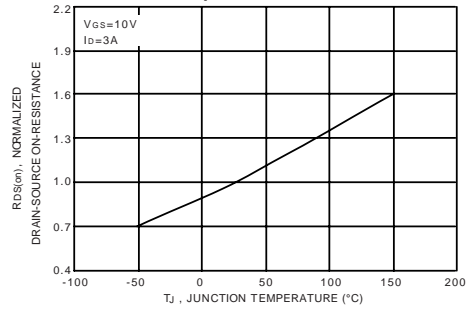


Figure 5. Gate Threshold Variation with Temperature

