



CHENMKO ENTERPRISE CO.,LTD

Halogens free devices

SURFACE MOUNT
N-Channel Enhancement Mode Field Effect Transistor
VOLTAGE 60 Volts CURRENT 2.8 Ampere

CHM6168PAGP

APPLICATION

- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

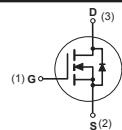
FEATURE

- * Small package. (TO-252)
- * Super high dense cell design for extremely low R_{DSON}.
- * High power and current handing capability.

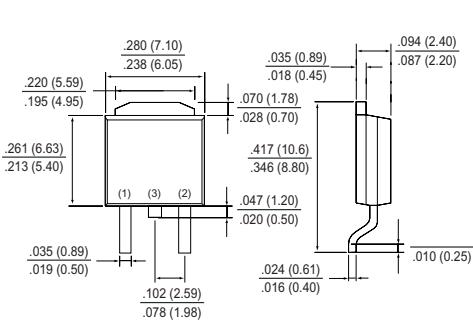
CONSTRUCTION

- * N-Channel Enhancement

CIRCUIT



D-PAK(TO-252)



1 Gate
2 Source
3 Drain(Heat Sink)

Dimensions in inches and (millimeters)

TO-252

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

Symbol	Parameter	CHM6168PAGP	Units
V _{DSS}	Drain-Source Voltage	60	V
V _{GSS}	Gate-Source Voltage	±20	V
I _D	Maximum Drain Current - Continuous	28	A
	- Pulsed (Note 3)	112	
P _D	Maximum Power Dissipation at T _c = 25 °C	38	W
T _J	Operating Temperature Range	-55 to 175	°C
T _{STG}	Storage Temperature Range	-55 to 175	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec
 2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%
 3. Repetitive Rating , Pulse width limited by maximum junction temperature
 4. Guaranteed by design , not subject to production testing

Thermal characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 1)	50	°C/W
20FFEB			

ELECTRICAL CHARACTERISTIC (CHM6168PAGP)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			V
$I_{\text{DS}(\text{SS})}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 60 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			1	μA
I_{GSSF}	Gate-Body Leakage	$V_{\text{GS}} = 20\text{V}, V_{\text{DS}} = 0 \text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{\text{GS}} = -20\text{V}, V_{\text{DS}} = 0 \text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$	1		3	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}, I_D=19\text{A}$		18	23	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_D=10\text{A}$		27	38	
g_{FS}	Forward Transconductance	$V_{\text{DS}} = 10\text{V}, I_D = 28\text{A}$		23		S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{\text{DS}} = 25\text{V}, V_{\text{GS}} = 0\text{V}, f = 1.0 \text{ MHz}$		1120		pF
C_{oss}	Output Capacitance			125		
C_{rss}	Reverse Transfer Capacitance			75		

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{\text{DS}}=48\text{V}, I_D=28\text{A}$ $V_{\text{GS}}=10\text{V}$		24	31	nC
Q_{gs}	Gate-Source Charge			6		
Q_{gd}	Gate-Drain Charge			6		
t_{on}	Turn-On Time	$V_{\text{DD}}= 30\text{V}$ $I_D = 1\text{A}, V_{\text{GS}}= 10 \text{ V}$ $R_{\text{GEN}}= 6 \Omega$		15	30	nS
t_r	Rise Time			57	10	
t_{off}	Turn-Off Time			38	76	
t_f	Fall Time			10	20	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_s	Drain-Source Diode Forward Current	(Note 1)			28	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_s = 28\text{A}, V_{\text{GS}} = 0 \text{ V}$	(Note 2)		1.2	V

RATING CHARACTERISTIC CURVES (CHM6168PAGP)

Typical Electrical Characteristics

Figure 1. Output Characteristics

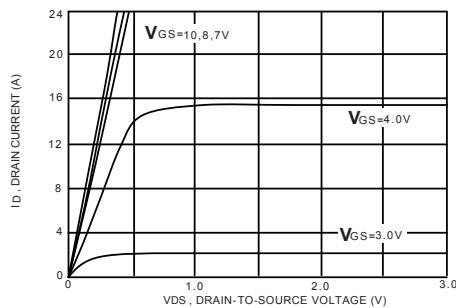


Figure 2. Transfer Characteristics

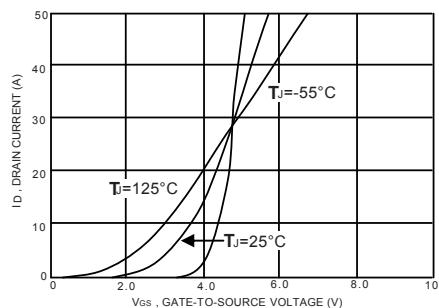


Figure 3. Gate Charge

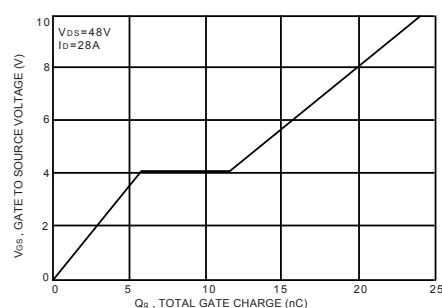


Figure 4. On-Resistance Variation with Temperature

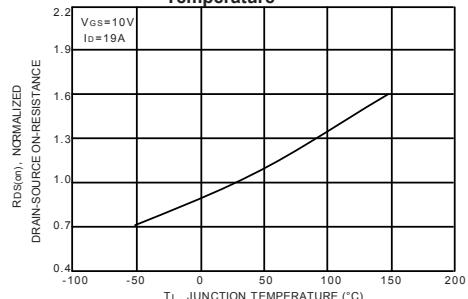


Figure 5. Gate Threshold Variation with Temperature

