



CHENMKO ENTERPRISE CO., LTD

SURFACE MOUNT

N-Channel Enhancement Mode Field Effect Transistor

VOLTAGE 100 Volts CURRENT 36 Ampere

CHM540ANGP

APPLICATION

- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

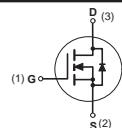
FEATURE

- * Small package. (D2PAK)
- * Super high dense cell design for extremely low R_{DSON}.
- * High power and current handing capability.

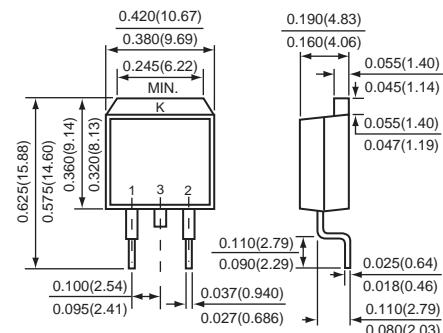
CONSTRUCTION

- * N-Channel Enhancement

CIRCUIT



D2PAK



1 Gate
2 Source
3 Drain (Heat Sink)

Dimensions in inches and (millimeters)

D2PAK

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

Symbol	Parameter	CHM540ANGP	Units
V _{DSS}	Drain-Source Voltage	100	V
V _{GSS}	Gate-Source Voltage	±20	V
I _D	Maximum Drain Current - Continuous	36	A
	- Pulsed (Note 3)	120	
P _D	Maximum Power Dissipation at T _c = 25°C	140	W
T _J	Operating Temperature Range	-55 to 150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

3. Repetitive Rating , Pulse width limited by maximum junction temperature

4. Guaranteed by design , not subject to production testing

Thermal characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 1)	62.5	°C/W
2008-01			

ELECTRICAL CHARACTERISTIC (CHM540ANGP)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100			V
$I_{\text{DS}}^{\text{SS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 100 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			25	μA
I_{GSSF}	Gate-Body Leakage	$V_{\text{GS}} = 20 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{\text{GS}} = -20 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$	2		4	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10 \text{ V}, I_D=18 \text{ A}$		40	48	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{\text{DS}} = 25 \text{ V}, I_D = 18 \text{ A}$		14		S

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{\text{DS}} = 25 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1.0 \text{ MHz}$		832		pF
C_{oss}	Output Capacitance			240		
C_{rss}	Reverse Transfer Capacitance			105		

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{\text{DS}}=80 \text{ V}, I_D=18 \text{ A}$ $V_{\text{GS}}=10 \text{ V}$		37.5	48	nC
Q_{gs}	Gate-Source Charge			6		
Q_{gd}	Gate-Drain Charge			18		
t_{on}	Turn-On Time	$V_{\text{DD}}= 50 \text{ V}$ $I_D = 18 \text{ A}, V_{\text{GS}} = 10 \text{ V}$ $R_{\text{GEN}} = 5.1 \Omega$		13	40	nS
t_r	Rise Time			11	35	
t_{off}	Turn-Off Time			32	65	
t_f	Fall Time			15	45	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_s	Drain-Source Diode Forward Current	(Note 1)			36	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_s = 18 \text{ A}, V_{\text{GS}} = 0 \text{ V}$	(Note 2)		1.3	V