



CHENMKO ENTERPRISE CO.,LTD

Halogens free devices

SURFACE MOUNT
N-Channel Enhancement Mode Field Effect Transistor
VOLTAGE 30 Volts CURRENT 12 Ampere

CHM3055LAPAGP

APPLICATION

- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

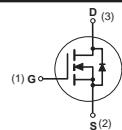
FEATURE

- * Small package. (TO-252)
- * Super high dense cell design for extremely low R_{DS(ON)}.
- * High power and current handing capability.

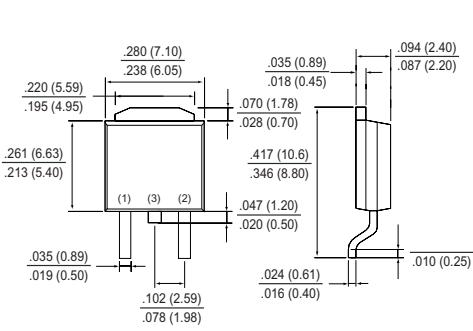
CONSTRUCTION

- * N-Channel Enhancement

CIRCUIT



D-PAK(TO-252)



1 Gate
2 Source
3 Drain(Heat Sink)

Dimensions in inches and (millimeters)

TO-252

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

Symbol	Parameter	CHM3055LAPAGP	Units
V _{DSS}	Drain-Source Voltage	30	V
V _{GSS}	Gate-Source Voltage	±20	V
I _D	Maximum Drain Current - Continuous	12	A
	- Pulsed (Note 3)	45	
P _D	Maximum Power Dissipation at T _c = 25°C	31	W
T _J	Operating Temperature Range	-55 to 150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

3. Repetitive Rating , Pulse width limited by maximum junction temperature

4. Guaranteed by design , not subject to production testing

Thermal characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 1)	50	°C/W
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2006-02

ELECTRICAL CHARACTERISTIC (CHM3055LAPAGP)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA
I_{GSSF}	Gate-Body Leakage	$V_{GS} = 20\text{V}, V_{DS} = 0 \text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{GS} = -20\text{V}, V_{DS} = 0 \text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	0.8		2.5	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=12\text{A}$		45	60	$\text{m}\Omega$
		$V_{GS}=5\text{V}, I_D=12\text{A}$		70	90	
g_{FS}	Forward Transconductance	$V_{DS} = 10\text{V}, I_D = 12\text{A}$		20		S

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{DS}=15\text{V}, I_D=6\text{A}$ $V_{GS}=10\text{V}$		10	15	nC
Q_{gs}	Gate-Source Charge			2		
Q_{gd}	Gate-Drain Charge			3		
t_{on}	Turn-On Time	$V_{DD}= 15\text{V}$ $I_D=12\text{A}, V_{GS}= 10 \text{ V}$ $R_{GEN}= 2.5 \Omega$		12	25	nS
t_r	Rise Time			5	15	
t_{off}	Turn-Off Time			14	30	
t_f	Fall Time			14	30	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_s	Drain-Source Diode Forward Current			12	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_s = 12\text{A}, V_{GS} = 0 \text{ V}$		0.9	V