



**CHENMKO ENTERPRISE CO.,LTD**

Halogens free devices

**SURFACE MOUNT**

**N-Channel Enhancement Mode Field Effect Transistor**

VOLTAGE 100 Volts CURRENT 1.3 Ampere

**CHM210BGP**

**APPLICATION**

- \* Servo motor control.
- \* Other switching applications.

**FEATURE**

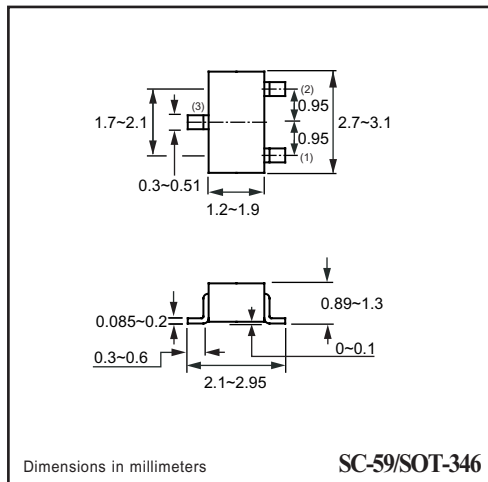
- \* Small flat package. (SC-59)
- \* High density cell design for extremely low  $R_{DS(ON)}$ .
- \* Rugged and reliable.
- \* High saturation current capability.

**CONSTRUCTION**

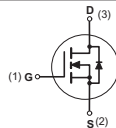
- \* N-Channel Enhancement



SC-59/SOT-346



**CIRCUIT**



**Absolute Maximum Ratings**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	CHM210BGP	Units
$V_{DSS}$	Drain-Source Voltage	100	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Maximum Drain Current - Continuous	1.3	A
	- Pulsed (Note 1)	18	
$P_D$	Maximum Power Dissipation (Note 1)	750	mW
$T_J$	Operating Temperature Range	-55 to 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$

Note : 1. Part mounted on FR-4 board with recommended pad layout.  
2. Short duration test pulse used to minimize self-heating effect

**Thermal characteristics**

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1)	166	$^\circ\text{C/W}$
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## ELECTRICAL CHARACTERISTIC ( CHM210BGP )

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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### OFF CHARACTERISTICS

$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 10\ \mu\text{A}$	100			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 80\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
$I_{GSSF}$	Gate-Body Leakage	$V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$			+100	nA
$I_{GSSR}$	Gate-Body Leakage	$V_{GS} = -20\text{ V}, V_{DS} = 0\text{ V}$			-100	nA

### ON CHARACTERISTICS (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	1.0		2.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS}=5\text{V}, I_D=1\text{A}$		230	240	$\text{m}\Omega$
		$V_{GS}=10\text{V}, I_D=2\text{A}$		220	230	
$g_{FS}$	Forward Transconductance	$V_{DS} = 10\text{ V}, I_D = 2\text{ A}$		10		S

### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$		802		$\text{pF}$
$C_{oss}$	Output Capacitance			80		
$C_{rss}$	Reverse Transfer Capacitance			41		

### SWITCHING CHARACTERISTICS (Note 4)

$Q_g$	Total Gate Charge	$V_{DS}=50\text{V}, I_D=2\text{A}$ $V_{GS}=10\text{V}$		15		nC
$Q_{gs}$	Gate-Source Charge			2.0		
$Q_{gd}$	Gate-Drain Charge			4.0		
$t_{on}$	Turn-On Time	$V_{DD}= 30\text{ V}$ $I_D = 1.0\text{ A}, V_{GS} = 10\text{ V}$ $R_{GEN} = 6\ \Omega$		16		nS
$t_r$	Rise Time			330		
$t_{off}$	Turn-Off Time			39		
$t_f$	Fall Time			111		

### DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

$I_S$	Drain-Source Diode Forward Current	(Note 1)			10	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$I_S = 2\text{ A}, V_{GS} = 0\text{ V}$			1.4	V

# RATING CHARACTERISTIC CURVES ( CHM210BGP )

## Typical Electrical Characteristics

Figure 1. Output Characteristics

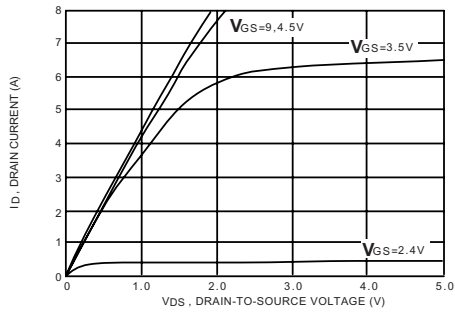


Figure 2. Transfer Characteristics

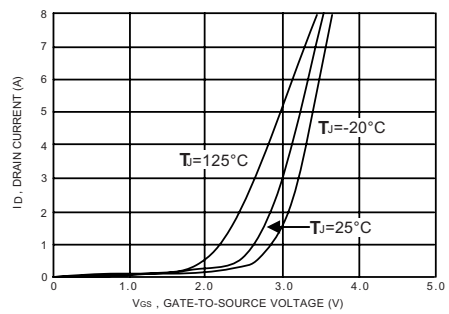


Figure 3. Gate Charge

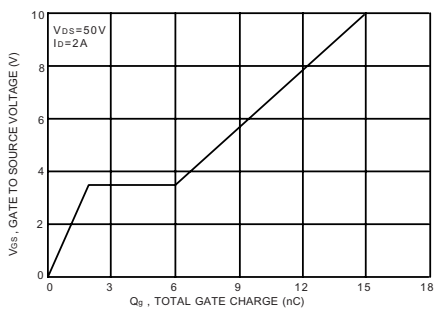


Figure 4. On-Resistance Variation with Temperature

