

 **CHENMKO ENTERPRISE CO.,LTD**
Halogens free devices

SURFACE MOUNT
N-Channel Enhancement Mode Field Effect Transistor
VOLTAGE 20 Volts CURRENT 2.8 Ampere

CHM1423WGP

APPLICATION

- * Power Management in Note book
- * Portable Equipment
- * Battery Powered System
- * DC/DC Converter
- * Load Switch
- * DSC
- * LCD Display inverter

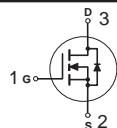
FEATURE

- * Small surface mounting type. (SC-70/SOT-323)
- * High density cell design for low R_{DS(ON)}

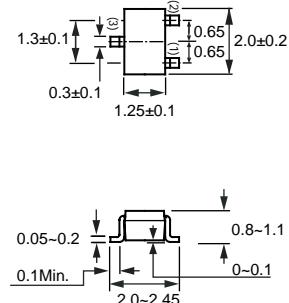
CONSTRUCTION

- * N-Channel Enhancement

CIRCUIT



SC-70/SOT-323



Dimensions in millimeters

SC-70/SOT-323

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

Symbol	Parameter	CHM1423WGP	Units
V _{DSS}	Drain-Source Voltage	20	V
V _{GSS}	Gate-Source Voltage	±12	V
I _D	Maximum Drain Current - Continuous (Note 1)	2.8	A
	- Pulsed (Note 2)	10	
I _S	Drain-Source Diode Forward Current (Note 1)	1.6	A
P _D	Maximum Power Dissipation (Note 1)	330	mW
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

Thermal characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient	100	°C/W
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2008-9

RATING CHARACTERISTIC CURVES (CHM1423WGP)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}$, $I_D = 250 \mu\text{A}$	20			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 20 \text{ V}$, $V_{\text{GS}} = 0 \text{ V}$			1	μA
I_{GSS}	Gate-Body Leakage	$V_{\text{GS}} = 12 \text{ V}$, $V_{\text{DS}} = 0 \text{ V}$			+100	nA
I_{GSS}	Gate-Body Leakage	$V_{\text{GS}} = -12 \text{ V}$, $V_{\text{DS}} = 0 \text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}$, $I_D = 250 \mu\text{A}$	0.45		1.2	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=4.5\text{V}$, $I_D=2.8\text{A}$		55	90	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$, $I_D=2.2\text{A}$		75	100	
V_{SD}	Diose Forward Voltage	$V_{\text{DS}} = 0\text{V}$, $I_S = 1.6 \text{ A}$		0.85	1.2	V

SWITCHING CHARACTERISTICS (Note 3)

Q_g	Total Gate Charge	$V_{\text{DS}}=10\text{V}$, $I_D=2.8\text{A}$ $V_{\text{GS}}=4.5\text{V}$		5.4	10	nC
Q_{gs}	Gate-Source Charge			0.65		
Q_{gd}	Gate-Drain Charge			1.4		
t_{on}	Turn-On Time	$V_{\text{DD}}= 10\text{V}$ $I_D = 2.8\text{A}$, $V_{\text{GEN}}= 4.5 \text{ V}$ $R_L = 5.5 \Omega$, $R_{\text{GEN}}= 6 \Omega$		12	25	nS
t_r	Rise Time			36	60	
t_{off}	Turn-Off Time			34	60	
t_f	Fall Time			10	25	

Note : 3. Guaranteed by design , not subject to production testing

RATING CHARACTERISTIC CURVES (CHM1423WGP)

Typical Electrical Characteristics

