



CHENMKO ENTERPRISE CO.,LTD

Halogens free devices

SURFACE MOUNT
N-Channel Enhancement Mode Field Effect Transistor
VOLTAGE 600 Volts CURRENT 9 Ampere

CHM09N6NGP

APPLICATION

- * Servo motor control.
- * Power MOSFET gate drivers.
- * Other switching applications.

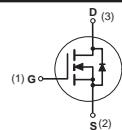
FEATURE

- * Small flat package. (D2PAK)
- * High density cell design for extremely low R_{DS(ON)}.
- * Rugged and reliable.

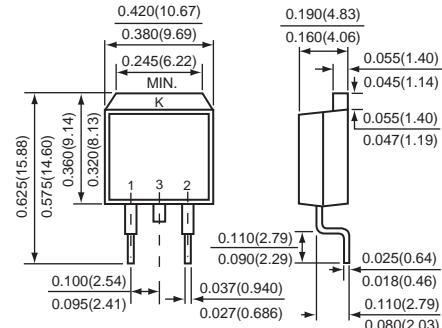
CONSTRUCTION

- * N-Channel Enhancement

CIRCUIT



D2PAK



1 Gate
2 Source
3 Drain (Heat Sink)

Dimensions in inches and (millimeters)

D2PAK

Absolute Maximum Ratings

T_A = 25°C unless otherwise noted

Symbol	Parameter	CHM09N6NGP	Units
V _{DSS}	Drain-Source Voltage	600	V
V _{GSS}	Gate-Source Voltage	±30	V
I _D	Maximum Drain Current - Continuous	9	A
	- Pulsed (Note 3)	35	
P _D	Maximum Power Dissipation	156	W
T _J	Operating Temperature Range	-55 to 150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

3. Repetitive Rating , Pulse width limited by maximum junction temperature

4. Guaranteed by design , not subject to production testing

Thermal characteristics

R _{θJA}	Thermal Resistance, Junction-to-Ambient (Note 1)	62.5	°C/W
2006-02			

ELECTRICAL CHARACTERISTIC (CHM09N6NGP)

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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OFF CHARACTERISTICS

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$	600			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 600 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			50	μA
I_{GSSF}	Gate-Body Leakage	$V_{\text{GS}} = 30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			+100	nA
I_{GSSR}	Gate-Body Leakage	$V_{\text{GS}} = -30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			-100	nA

ON CHARACTERISTICS (Note 2)

$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$	2		4	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10 \text{ V}, I_D=6 \text{ A}$		1.0	1.2	Ω
g_{FS}	Forward Transconductance	$V_{\text{DS}} = 50 \text{ V}, I_D = 6 \text{ A}$		5		S

SWITCHING CHARACTERISTICS (Note 4)

Q_g	Total Gate Charge	$V_{\text{DS}}=480 \text{ V}, I_D=2 \text{ A}$ $V_{\text{GS}}=10 \text{ V}$		73	85	nC
Q_{gs}	Gate-Source Charge			6		
Q_{gd}	Gate-Drain Charge			45		
t_{on}	Turn-On Time	$V_{\text{DD}}=200 \text{ V}$ $I_D = 9 \text{ A}, V_{\text{GS}} = 10 \text{ V}$ $R_{\text{GEN}} = 9.1 \Omega$		23	45	nS
t_r	Rise Time			26	50	
t_{off}	Turn-Off Time			105	165	
t_f	Fall Time			22	45	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I_s	Drain-Source Diode Forward Current	(Note 1)			9	A
V_{SD}	Drain-Source Diode Forward Voltage	$I_s = 9 \text{ A}, V_{\text{GS}} = 0 \text{ V}$ (Note 2)			1.5	V

RATING CHARACTERISTIC CURVES (CHM09N6NGP)

Typical Electrical Characteristics

Figure 1. Output Characteristics

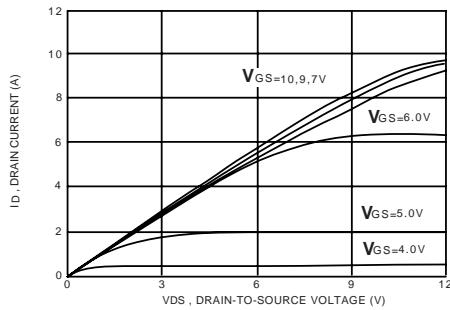


Figure 2. Transfer Characteristics

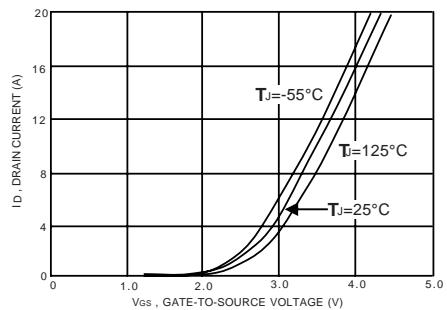


Figure 3. Gate Charge

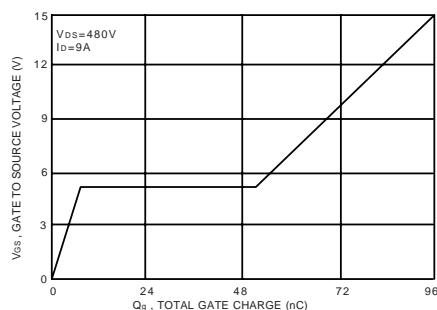


Figure 4. On-Resistance Variation with Temperature

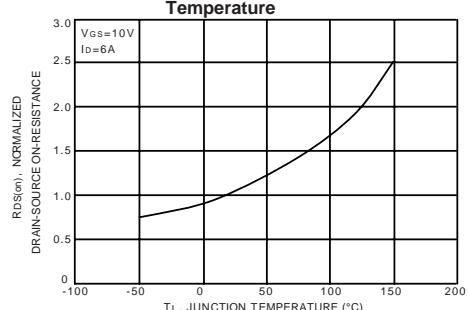


Figure 5. Gate Threshold Variation with Temperature

