



**CHENMKO ENTERPRISE CO., LTD**

**SURFACE MOUNT**

**N-Channel Enhancement Mode Field Effect Transistor**

**VOLTAGE 700 Volts CURRENT 1.6 Ampere**

**CHM02N7PAGP**

#### APPLICATION

- \* Servo motor control.
- \* Power MOSFET gate drivers.
- \* Other switching applications.

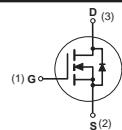
#### FEATURE

- \* Small package. (TO-252)
- \* Super high dense cell design for extremely low R<sub>DS(ON)</sub>.
- \* High power and current handing capability.

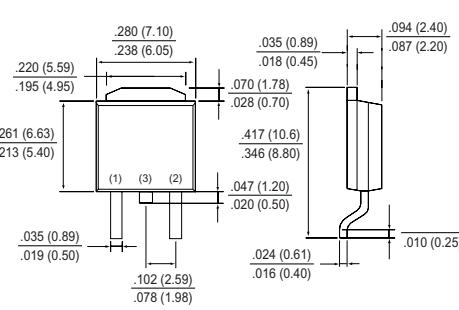
#### CONSTRUCTION

- \* N-Channel Enhancement

#### CIRCUIT



**D-PAK(TO-252)**



Dimensions in inches and (millimeters)

**TO-252**

#### Absolute Maximum Ratings

T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	CHM02N7PAGP	Units
V <sub>DSS</sub>	Drain-Source Voltage	700	V
V <sub>GSS</sub>	Gate-Source Voltage	±30	V
I <sub>D</sub>	Maximum Drain Current - Continuous	1.6	A
	- Pulsed (Note 3)	6	
P <sub>D</sub>	Maximum Power Dissipation at T <sub>c</sub> = 25°C	43	W
T <sub>J</sub>	Operating Temperature Range	-55 to 150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

3. Repetitive Rating , Pulse width limited by maximum junction temperature

4. Guaranteed by design , not subject to production testing

#### Thermal characteristics

R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient (Note 1)	50	°C/W
2006-02			

## ELECTRICAL CHARACTERISTIC ( CHM02N7PAGP )

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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### OFF CHARACTERISTICS

$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$	700			V
$I_{\text{DS}(\text{SS})}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 700 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			25	$\mu\text{A}$
$I_{\text{GSSF}}$	Gate-Body Leakage	$V_{\text{GS}} = 30\text{V}, V_{\text{DS}} = 0 \text{ V}$			+100	nA
$I_{\text{GSSR}}$	Gate-Body Leakage	$V_{\text{GS}} = -30\text{V}, V_{\text{DS}} = 0 \text{ V}$			-100	nA

### ON CHARACTERISTICS (Note 2)

$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$	2		4	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}, I_D=1\text{A}$		5.5	6.6	$\Omega$
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}} = 50\text{V}, I_D = 1\text{A}$		0.7		S

### SWITCHING CHARACTERISTICS (Note 4)

$Q_g$	Total Gate Charge	$V_{\text{DS}}=480\text{V}, I_D=2\text{A}$ $V_{\text{GS}}=10\text{V}$		14	20	nC
$Q_{\text{gs}}$	Gate-Source Charge			2.5		
$Q_{\text{gd}}$	Gate-Drain Charge			8.6		
$t_{\text{on}}$	Turn-On Time	$V_{\text{DD}}= 300\text{V}$ $I_D = 2.0\text{A}, V_{\text{GS}}= 10 \text{ V}$ $R_{\text{GEN}}= 18 \Omega$		19	35	nS
$t_r$	Rise Time			26	50	
$t_{\text{off}}$	Turn-Off Time			34	70	
$t_f$	Fall Time			15	40	

### DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

$I_s$	Drain-Source Diode Forward Current	(Note 1)			1.6	A
$V_{\text{SD}}$	Drain-Source Diode Forward Voltage	$I_s = 2.0\text{A}, V_{\text{GS}} = 0 \text{ V}$ (Note 2)			1.5	V