



CHENMKO ENTERPRISE CO.,LTD

Halogens free devices

**SURFACE MOUNT
NPN SILICON Transistor**

VOLTAGE 160 Volts CURRENT 0.6 Ampere

CHT5551XGP

APPLICATION

- * Telephony and professional communication equipment.
- * Other switching applications.

FEATURE

- * Suitable for high packing density.

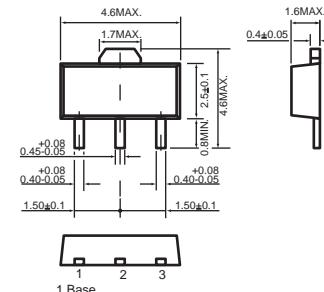
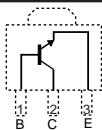
CONSTRUCTION

*NPN SILICON Transistor



SC-62/SOT-89

CIRCUIT



SC-62/SOT-89

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	—	180	V
V_{CEO}	collector-emitter voltage	open base	—	160	V
V_{EBO}	emitter-base voltage	open collector	—	6.0	V
I_C	collector current (DC)		—	600	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25^\circ\text{C}$; note 1	—	1200	mW
T_{stg}	storage temperature		-65	+150	°C
T_j	junction temperature		—	150	°C
T_{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

RATING CHARACTERISTIC CURVES (CHT5551XGP)

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	357	K/W

Note

1.Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

$T_{amb} = 25^{\circ}\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector cut-off current	$V_{CB} = 120\text{ V}$	—	50	nA
I_{CBO}	collector cut-off current	$V_{CB} = 120\text{ V}, T_A = 100^{\circ}\text{C}$	—	50	uA
I_{EBO}	emitter cut-off current	$V_{EB} = 4.0\text{V}$	—	50	nA
h_{FE}	DC current gain	$I_C = 1.0\text{ mA}; V_{CE} = 5\text{V}$ $I_C = 10\text{mA}; V_{CE} = 5\text{V}$ $I_C = 50\text{ mA}; V_{CE} = 5\text{V}$	80 80 30	— 250 —	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 10\text{ mA}; I_B = 1.0\text{ mA}$ $I_C = 50\text{ mA}; I_B = 5.0\text{ mA}$	— —	0.15 0.2	V
V_{BEsat}	base-emitter saturation voltage	$I_C = 10\text{mA}; I_B = 1.0\text{mA}$ $I_C = 50\text{ mA}; I_B = 5.0\text{ mA}$	— —	1.0 1.0	V
C_{ob}	collector capacitance	$I_E = i_e = 0; V_{CB} = 1.0\text{ V}; f = 1\text{ MHz}$	—	6.0	pF
h_{fe}		$V_{CE}=10\text{V}, I_C=1.0\text{mA}, f=1.0\text{KHz}$	50	200	
f_T	transition frequency	$I_C = 10\text{ mA}; V_{CE} = 1.0\text{ V}; f = 1.0\text{ MHz}$	100	300	MHz
F	noise figure	$I_C = 200\text{ mA}; V_{CE} = 5\text{ V}; R_S = 1.0\Omega; f = 10\text{Hz to } 15.7\text{KHz}$	—	8.0	dB