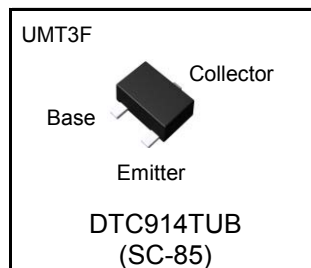


Parameter	Value
V_{CEO}	20V
V_{EBO}	40V
I_C	400mA
R_1	10k Ω

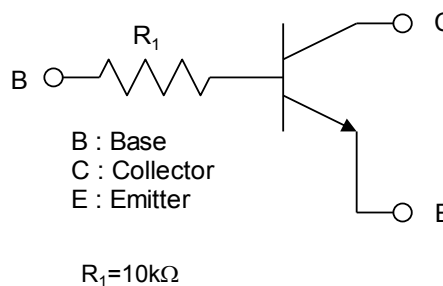
●Outline



●Features

- 1) Built-In Biasing Resistors
- 2) High Breakdown Voltage of Emitter to Base
 BV_{EBO} is Min. 40V at $I_E=50\mu A$
- 3) Low Output ON Resistance.
 R_{on} is Typ. 0.9 Ω at $V_I=7V$
- 4) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 5) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 6) Lead Free/RoHS Compliant.

●Inner circuit



●Application

Muting circuit

●Packaging specifications

Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
DTC914TUB	UMT3F	2021	TL	180	8	3,000	88

●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	V_{CBO}	40	V
Collector-emitter voltage	V_{CEO}	20	V
Emitter-base voltage	V_{EBO}	40	V
Collector current	I_C	400	mA
Power dissipation	P_D^{*1}	200	mW
Junction temperature	T_j	150	°C
Range of storage temperature	T_{stg}	-55 to +150	°C

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Collector-base breakdown voltage	BV_{CBO}	$I_C = 50\mu A$	40	-	-	V
Collector-emitter breakdown voltage	BV_{CEO}	$I_C = 1mA$	20	-	-	V
Emitter-base breakdown voltage	BV_{EBO}	$I_E = 50\mu A$	40	-	-	V
Collector cut-off current	I_{CBO}	$V_{CB} = 40V$	-	-	500	nA
Emitter cut-off current	I_{EBO}	$V_{EB} = 40V$	-	-	500	nA
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C / I_B = 30mA / 3mA$	-	30	100	mV
DC current gain	h_{FE}	$V_{CE} = 5V, I_C = 10mA$	820	-	2700	-
Input resistance	R_1	-	7	10	13	kΩ
Transition frequency	f_T^{*2}	$V_{CE} = 6V, I_E = -4mA,$ $f = 10MHz$	-	35	-	MHz
Output ON Resistance	R_{on}	$V_I = 7V,$ $R_L = 1k\Omega, f = 1kHz$	-	0.9	-	Ω

*1 Each terminal mounted on a reference footprint

*2 Characteristics of built-in transistor

●Electrical characteristic curves(Ta = 25°C)

Fig.1 Grounded emitter propagation characteristics

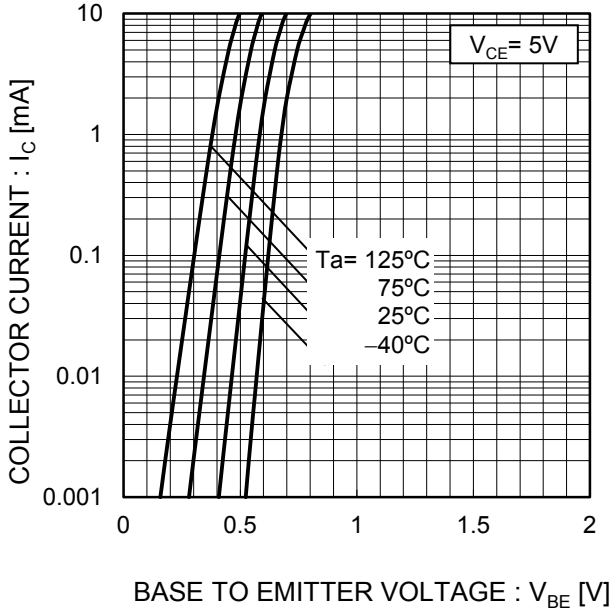


Fig.2 Grounded emitter output characteristics

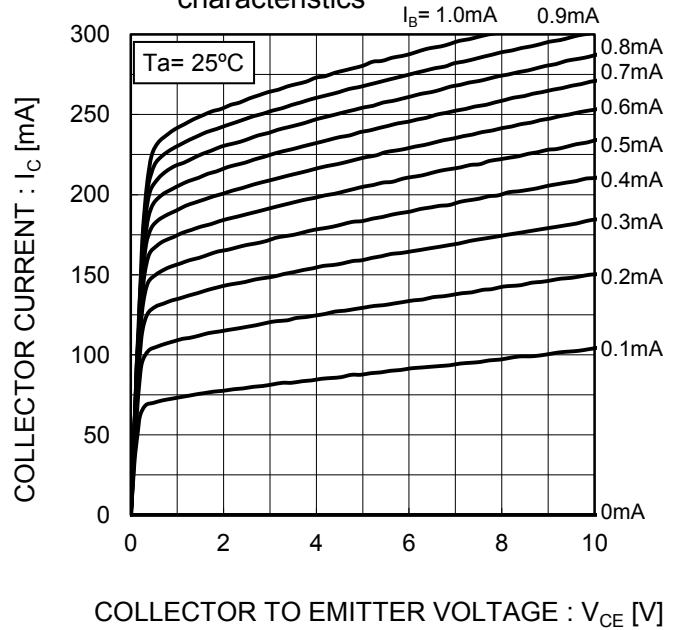


Fig.3 DC Current gain vs. Collector Current

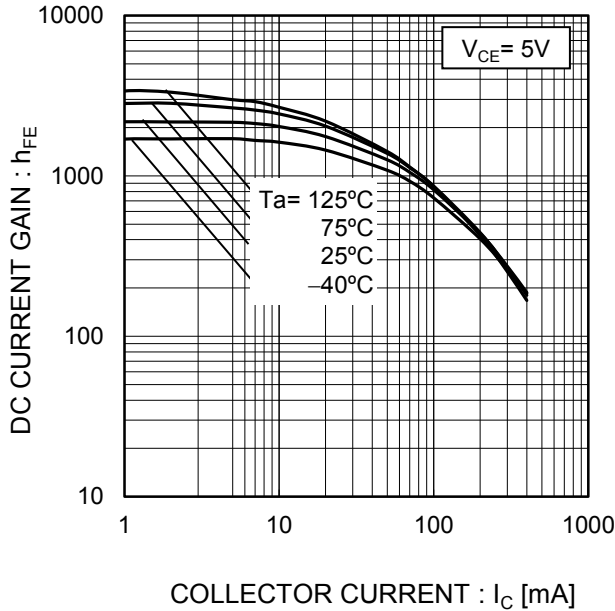
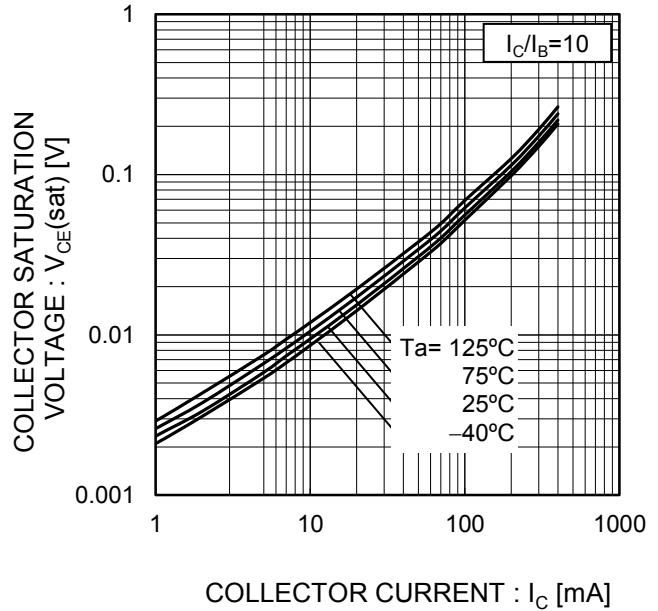


Fig.4 Collector-emitter saturation voltage vs. Collector Current



●Electrical characteristic curves(Ta = 25°C)

Fig.5 Output ON resistance vs. input voltage

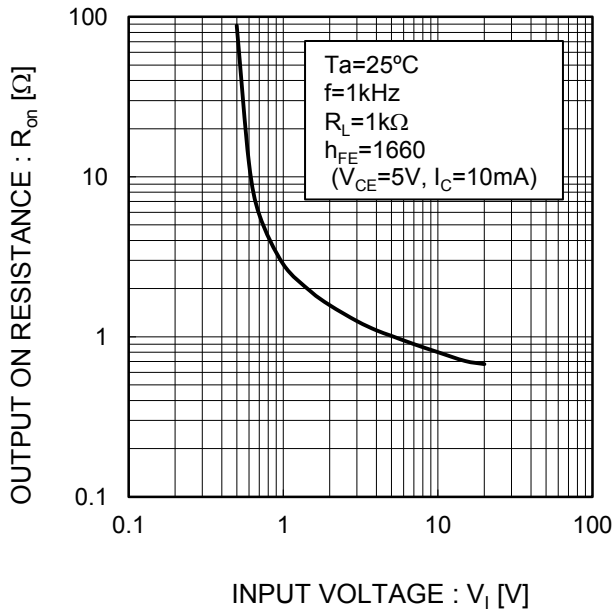
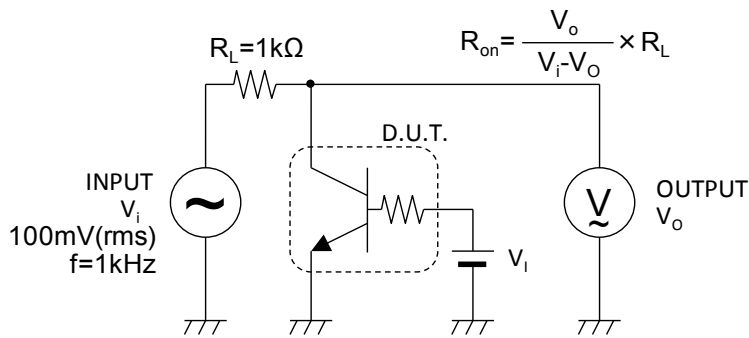
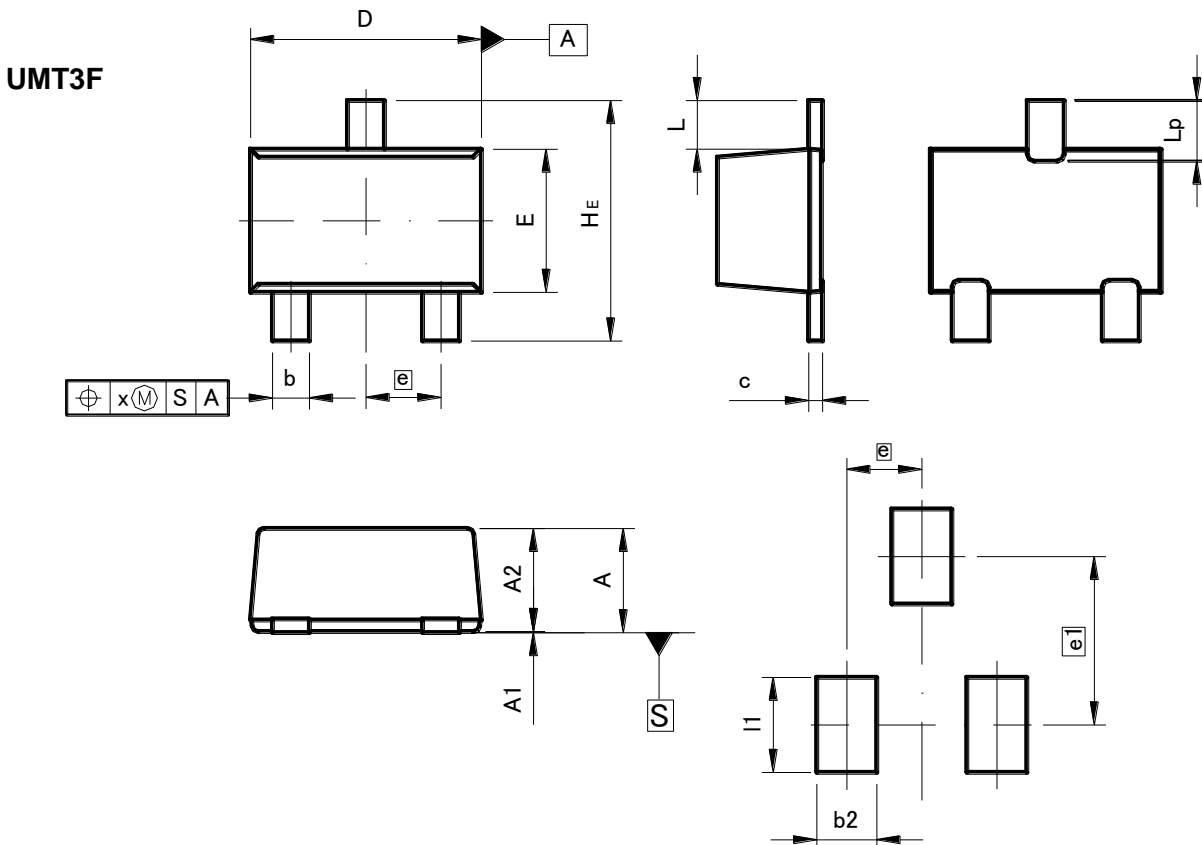


Fig.6 Ron measurement circuit.



●Dimensions (Unit : mm)



Pattern of terminal position areas
[Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.85	1.05	0.033	0.041
A1	0.00	0.10	0.000	0.004
A2	0.80	1.00	0.031	0.039
b	0.27	0.42	0.011	0.017
c	0.08	0.18	0.003	0.007
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
e	0.65		0.026	
HE	2.00	2.20	0.079	0.087
L	0.43		0.017	
Lp	0.43	0.63	0.017	0.025
x	-	0.10	-	0.004

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b2	-	0.52	-	0.020
e1	1.47		0.058	
l1	-	0.83	-	0.033

Dimension in mm / inches

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