**DTC043X** series

NPN 100mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

#### Datasheet

Parameter	Value
V <sub>CC</sub>	50V
I <sub>C(MAX.)</sub>	100mA
R <sub>1</sub>	4.7kΩ
R <sub>2</sub>	10kΩ

### Features

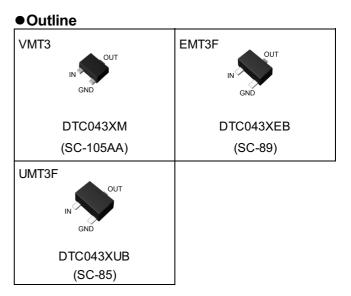
- 1) Built-In Biasing Resistors,  $R_1 = 4.7 k\Omega$ ,  $R_2 = 10 k\Omega$
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary PNP Types: DTA043X series
- 6) Lead Free/RoHS Compliant.

### Application

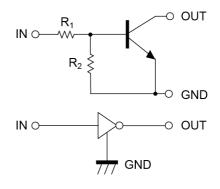
Switching circuit, Inverter circuit, Interface circuit, Driver circuit

#### • Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC043XM	VMT3	1212	T2L	180	8	8000	68
DTC043XEB	EMT3F	1616	TL	180	8	3000	68
DTC043XUB	UMT3F	2021	TL	180	8	3000	68



### Inner circuit



## • Absolute maximum ratings ( $T_a = 25^{\circ}C$ )

F	Symbol	Values	Unit	
Supply voltage	V <sub>CC</sub>	50	V	
Input voltage	V <sub>IN</sub>	20 to -7	V	
Output current	Ι <sub>ο</sub>	100	mA	
Collector current	I <sub>C(MAX)</sub> *1	100	mA	
Power dissipation	DTC043XM		150	
	DTC043XEB	P <sub>D</sub> <sup>*2</sup>	150	mW
	DTC043XUB		200	
Junction temperature		Tj	150	°C
Range of storage temperature		T <sub>stg</sub>	-55 to +150	C°

## • Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Cumph of	Conditions	Values			1.1:4	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
	V <sub>I(off)</sub>	V <sub>CC</sub> = 5V, I <sub>O</sub> = 100µA	-	-	0.5	V	
Input voltage	V <sub>I(on)</sub>	V <sub>O</sub> = 0.3V, I <sub>O</sub> = 5mA	2.5	-	-		
Output voltage	V <sub>O(on)</sub>	I <sub>O</sub> /I <sub>I</sub> =5mA/0.5mA	-	0.05	0.15	V	
Input current	I <sub>I</sub>	V <sub>1</sub> = 5V	-	-	1.8	mA	
Output current	I <sub>O(off)</sub>	$V_{CC} = 50V, V_{I} = 0V$	-	-	500	nA	
DC current gain	G <sub>I</sub>	V <sub>O</sub> = 10V, I <sub>O</sub> = 5mA	35	-	-	-	
Input resistance	R <sub>1</sub>	-	3.29	4.7	6.11	kΩ	
Resistance ratio	$R_2/R_1$	-	1.7	2.1	2.6	-	
Transition frequency	f <sub>T</sub> *1	V <sub>CE</sub> = 10V, I <sub>E</sub> = -5mA, f = 100MHz	-	250	-	MHz	

\*1 Characteristics of built-in transistor

\*2 Each terminal mounted on a reference footprint



# •Electrical characteristic curves (T<sub>a</sub> =25°C)

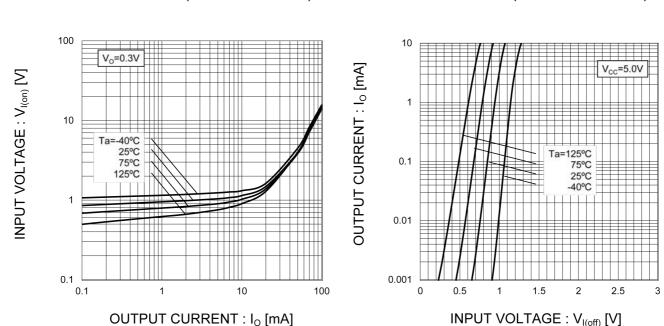


Fig.1 Input voltage vs. output current (ON characteristics)

Fig.4 DC current gain vs. output current

Fig.2 Output current vs. input voltage

(OFF characteristics)

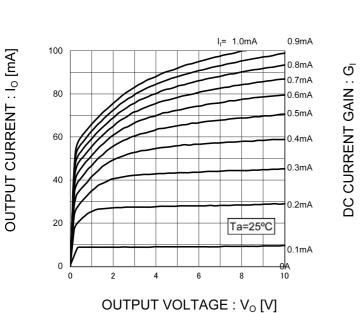
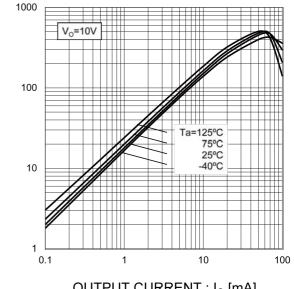


Fig.3 Output current vs. output voltage



OUTPUT CURRENT : Io [mA]



# ●Electrical characteristic curves (T<sub>a</sub> =25°C)

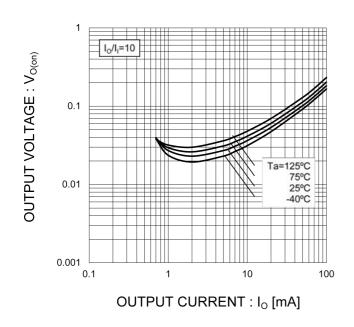
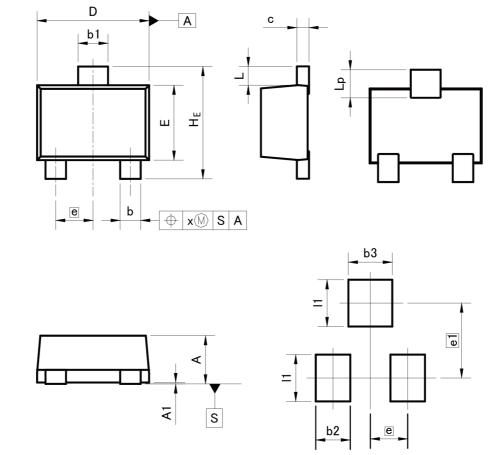


Fig.5 Output voltage vs. output current



### Dimensions

**VMT3** 



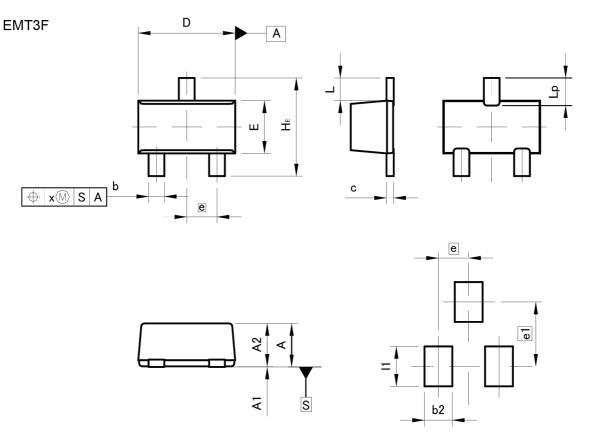
Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
	MIN	MAX	MIN	MAX
Α	0.45	0.55	0.018	0.022
A1	0.00	0.10	0.000	0.004
b	0.17	0.27	0.007	0.011
b1	0.27	0.37	0.011	0.015
с	0.08	0.18	0.003	0.007
D	1.10	1.30	0.043	0.051
E	0.70	0.90	0.028	0.035
е	0.4	40	0.02	
HE	1.10	1.30	0.043	0.051
L	0.10	0.30	0.004	0.012
Lp	0.20	0.40	0.008	0.016
x	-22	0.10	<u></u>	0.004
	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2	(22)	0.37	-	0.015
b3	: <del></del> ;	0.47	-	0.019
e1	0.	80	0.0	31
11	-	0.50	-	0.020

Dimension in mm/inches



### Dimensions



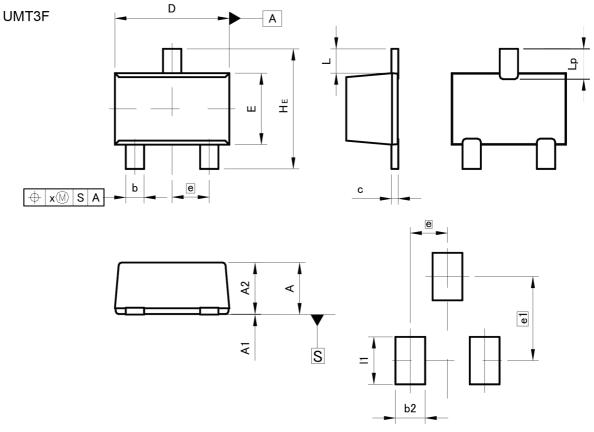
Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM -	MILIM	ETERS	INCHES		
	MIN	MAX	MIN	MAX	
A	0.65	0.85	0.026	0.033	
A1	0.00	0.10	0.000	0.004	
A2	0.60	0.80	0.024	0.031	
b	0.21	0.36	0.008	0.014	
с	0.08	0.18	0.003	0.007	
D	1.50	1.70	0.059	0.067	
E	0.76	0.96	0.030	0.038	
е	0.50		0.020		
HE	1.50	1.70	0.059	0.067	
L	0.3	37	0.015		
Lp	0.35	0.55	0.014	0.022	
x		0.10	-	0.004	
	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
b2	-	0.46		0.018	
e1	<b>—</b>	1.05		0.041	
11	-	0.65		0.026	

Dimension in mm/inches



### Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES	
	MIN	MAX	MIN	MAX	
A	0.85	1.05	0.033	0.041	
A1	0.00	0.10	0.000	0.004	
A2	0.80	1.00	0.031	0.039	
b	0.27	0.42	0.011	0.017	
с	0.08	0.18	0.003	0.007	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.65		0.026		
HE	2.00	2.20	0.079	0.087	
L	0.4	43	0.017		
Lp	0.43	0.63	0.017	0.025	
x		0.10	-	0.004	
	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
b2	-	0.52	-	0.020	
e1	1.47		0.0	58	
11	-	0.83		0.033	

Dimension in mm/inches



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