

**AEC-Q101 Qualified** 

# Medium Power Transistor (50V,0.5A)

# 2SD1949FRA / 2SD1484KFRA

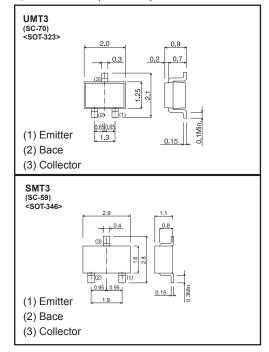
#### Features

- 1) High current.(Ic=0.5A)
- 2) Low saturation voltage, typically V<sub>CE(sat)</sub>=0.1V at Ic / I<sub>B</sub>=150mA / 15mA.

# ● Absolute maximum rationgs (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-emitter voltage	V <sub>CEO</sub>	50	V
Emitter-base voltage	V <sub>EBO</sub>	5	V
Collector current	I <sub>c</sub>	0.5	A
Collector power dissipation	Pc	0.2	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

# Dimensions (Unit : mm)



## ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV <sub>CBO</sub>	50	_	-	V	I <sub>C</sub> =100μA
Collector-emitter breakdown voltage	BV <sub>CEO</sub>	50	_	-	V	I <sub>C</sub> =1mA
Emitter-base breakdown voltage	BV <sub>EBO</sub>	5	_	-	V	I <sub>E</sub> =100μA
Collector outoff current	I <sub>CBO</sub>	-	_	0.5	μΑ	V <sub>CB</sub> =30V
Emitter cutoff current	I <sub>EBO</sub>	-	-	0.5	μА	V <sub>EB</sub> =4V
DC current rransfer ratio	h <sub>FE</sub>	120	_	390	_	V <sub>CE</sub> /I <sub>C</sub> =3V/10mA
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	-	-	0.4	V	I <sub>C</sub> /I <sub>B</sub> =150mA/15mA
Transition frequency	f <sub>T</sub>	-	250	-	MHz	V <sub>CE</sub> =5V , I <sub>E</sub> = -20mA , f=100MHz
Output capacitance	Cob	_	6.5	_	pF	V <sub>CB</sub> =10V , I <sub>E</sub> =0A , f=1MHz

# Packaging specifications and h<sub>FE</sub>

Туре	2SD1949FRA	2SD1484KFRA
Package	UMT3	SMT3
h <sub>FE</sub>	QR	QR
Marking	Y*	Y*
Code	T106	T146
Basic ordering unit (pleces)	3000	3000

<sup>\*</sup> Danotes hee

### hre values are classified as follows:

Item	Q	R
hfe	120 to 270	180 to 390

## Electrical characteristic curves

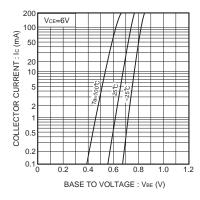


Fig.1 Ground emitter propagation characteristics

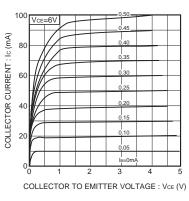


Fig.2 Ground emitter output characteristics

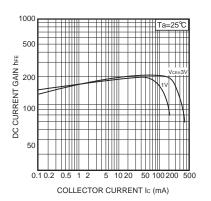


Fig.3 DC current gain vs. Collector current (I)

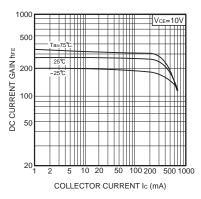


Fig.4 DC current gain vs. Collector currnet (II)

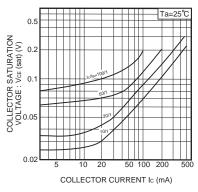


Fig.5 Collector-emitter saturation voltage vs. Collector current

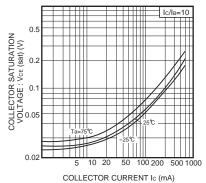


Fig.6 Collector-emitter saturation voltage vs. collector current

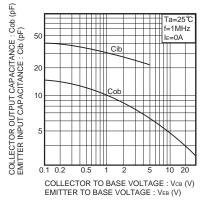


Fig.7 Input-and-output capacity vs.voltage characteristic

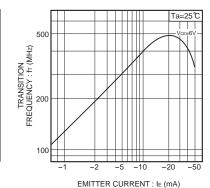


Fig.8 Transition frequency vs.emitter current

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