

# Midium Power Transistors (50V / 3A)

# **QS5W2**

#### Structure

NPN Silicon epitaxial planar transistor

#### Features

- 1) Low saturation voltage, typically  $V_{CE\;(sat)} = 0.35 V\;(Max.)\;(I_C\,/\,I_B = 1A\,/\,50 mA)$
- 2) High speed switching

## Applications

Driver

## Packaging specifications

	Package	TSMT5
Type	Code	TR
	Basic ordering unit (pieces)	3000

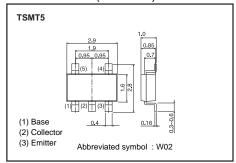
#### ● Absolute maximum ratings (Ta = 25°C)

#### <It is the same ratings for the Tr.1 and Tr.2>

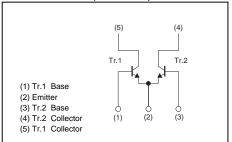
Parameter		Symbol	Limits	Unit	
Collector-base voltage		$V_{CBO}$	50	V	
Collector-emitter voltage		$V_{CEO}$	50	V	
Emitter-base voltage		$V_{EBO}$	6	V	
Collector current	DC	Ic	3	Α	
	Pulsed	I <sub>CP</sub> *1	6	Α	
Power dissipation		P <sub>D</sub> *2	0.5	W/Total	
		P <sub>D</sub> *3	1.25	W/Total	
		P <sub>D</sub> *3	0.9	W/Element	
Junction temperature		$T_j$	150	°C	
Range of storage temperature		T <sub>stg</sub>	-55 to 150	°C	

<sup>\*1</sup> Pw=10ms, Single Pulse

#### • Dimensions (Unit : mm)



#### • Inner circuit (Unit : mm)



<sup>\*2</sup> Mounted on a recommended land.

<sup>\*3</sup> Mounted on a 25 x 25 x 0.8[mm] ceramic board.

# ●Electrical characteristics (Ta=25°C)

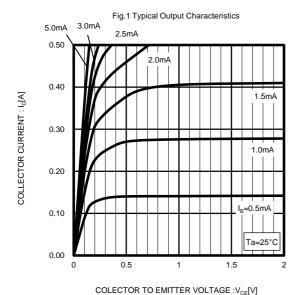
<It is the same ratings for the Tr.1 and Tr.2>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-emitter breakdown voltage	$BV_{CEO}$	50	-	-	V	I <sub>C</sub> = 1mA	
Collector-base breakdown voltage	$BV_{CBO}$	50	-	-	V	I <sub>C</sub> = 100μA	
Emitter-base breakdown voltage	$BV_{EBO}$	6	-	-	V	I <sub>E</sub> = 100μA	
Collector cut-off current	I <sub>CBO</sub>	-	-	1	μA	V <sub>CB</sub> = 50V	
Emitter cut-off current	I <sub>EBO</sub>	-	-	1	μA	V <sub>EB</sub> = 4V	
Collector-emitter staturation voltage	V <sub>CE(sat)</sub> *1	-	130	350	mV	$I_C= 1A$ , $I_B= 50mA$	
DC current gain	h <sub>FE</sub>	180	-	450	-	$V_{CE}$ = 3V, $I_{C}$ = 50mA	
Transition frequency	f <sub>T</sub> *1	-	320	ı	MHz	V <sub>CE</sub> = 10V I <sub>E</sub> =-500mA, f=100MHz	
Collector output capacitance	C <sub>ob</sub>	-	13	-	pF	V <sub>CB</sub> = 10V, I <sub>E</sub> =0A f=1MHz	
Turn-on time	t <sub>on</sub> * <sub>2</sub>	-	50	-	ns	1 1 5 \ 1 1 1 5 0 m \	
Storage time	t <sub>stg</sub> * <sub>2</sub>	-	450	-	ns	I <sub>C</sub> = 1.5A, I <sub>B1</sub> = 150mA, I <sub>B2</sub> =-150mA, V <sub>CC</sub> ~10V	
Fall time	t <sub>f</sub> *2	-	80	-	ns	1.02 1.0011111, 1.00 1.00	

<sup>\*1</sup> Pulsed

<sup>\*2</sup> See switching time test circuit

# ●Electrical characteristic curves (Ta=25°C)



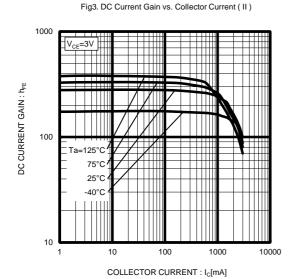


Fig.5 Collector-Emitter Saturation Voltage vs. Collector Current ( II )

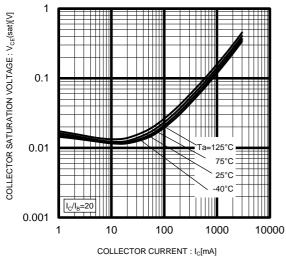


Fig.2 DC Current Gain vs. Collector Current ( I )

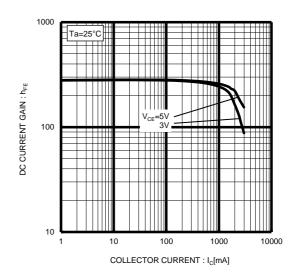
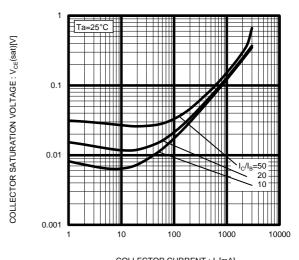
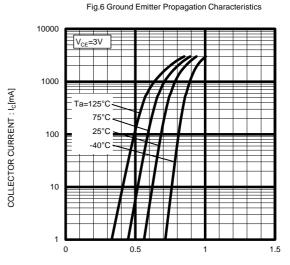


Fig.4 Collector-Emitter Saturation Voltage vs. Collector Current ( I )



COLLECTOR CURRENT :  $I_C[mA]$ 



BASE TO EMITTER VOLTAGE : VBE[V]

Fig.7 Emitter Input Capacitance vs. Emitter-Base Voltage Collector Output Capacitance vs. Collector-Base Voltage

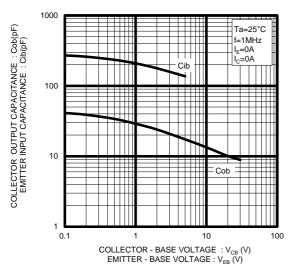


Fig.9 Safe Operating Area

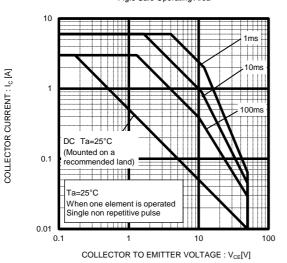
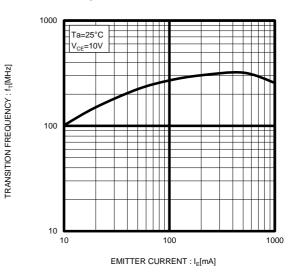
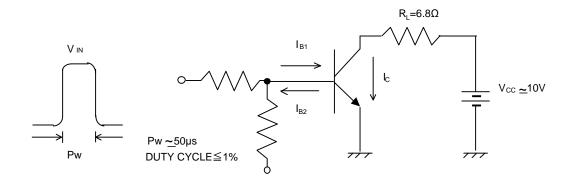


Fig.8 Gain Bandwidth Product vs. Emitter Current



# • Switching time test circuit



BASE CURENT WAVEFORM

90% t<sub>stg</sub> t<sub>f1</sub> t<sub>stg</sub> t<sub>f1</sub> t<sub>c</sub>

COLLECTOR CURRENT WAVEFORM

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